

## Consensus

Number	Object
1	<i>atomic registers</i>
2	<code>getAndSet()</code> , <code>getAndAdd()</code> , Queue, Stack
:	:
$m$	$(m, m(m + 1)/2)$ -assignment
:	:
$\infty$	<i>memory-to-memory move</i> , <code>compareAndSet()</code> , <i>load-linked/store-conditional</i> <sup>a</sup>

<sup>a</sup> See Appendix B for details.

**FIGURE 6.1** Concurrent computability and the universality hierarchy of synchronization operations.