```
1  public class AtomicSRSWRegister implements Register<int> {
2    private static int RANGE = M;
3    boolean[] r_bit = new boolean[RANGE]; // atomic boolean SRSW
4    public AtomicSRSWRegister(int capacity) {
5      for (int i = 1; i <= RANGE; i++)
6        r_bit[i] = false;
7      r_bit[0] = true;
8    }
9    public void write(int x) {
10     r_bit[x] = true;
11     for (int i = x - 1; i >= 0; i--)
12       r_bit[i] = false;
13   }
14   public int read() {
15     for (int i = 0; i <= RANGE; i++)
16       if (r_bit[i]) {
17         return i;
18       }
19     return -1; // impossible
20   }
21 }
```

FIGURE 4.22 Boolean to *M*-valued atomic SRSW register algorithm.