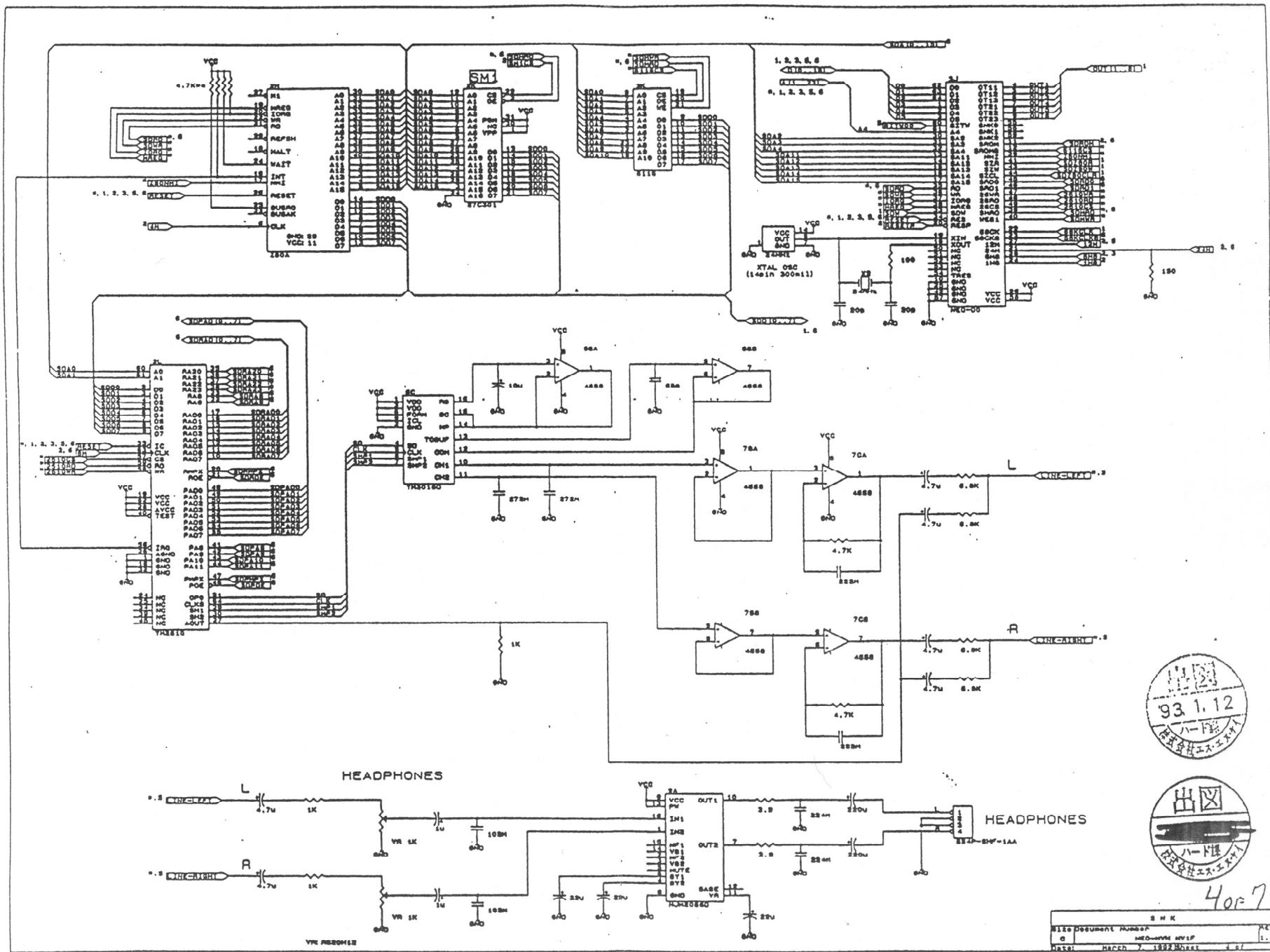
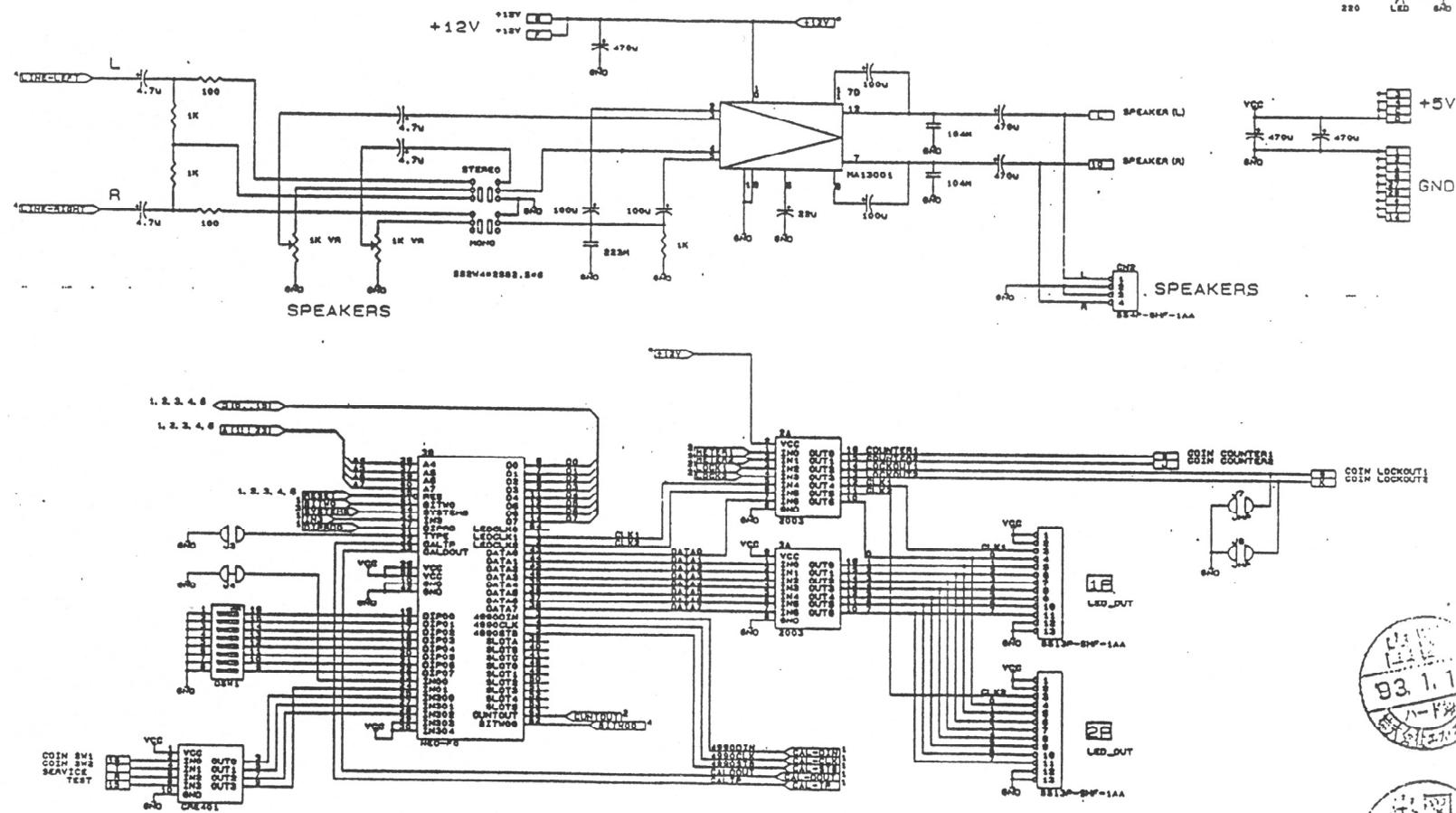


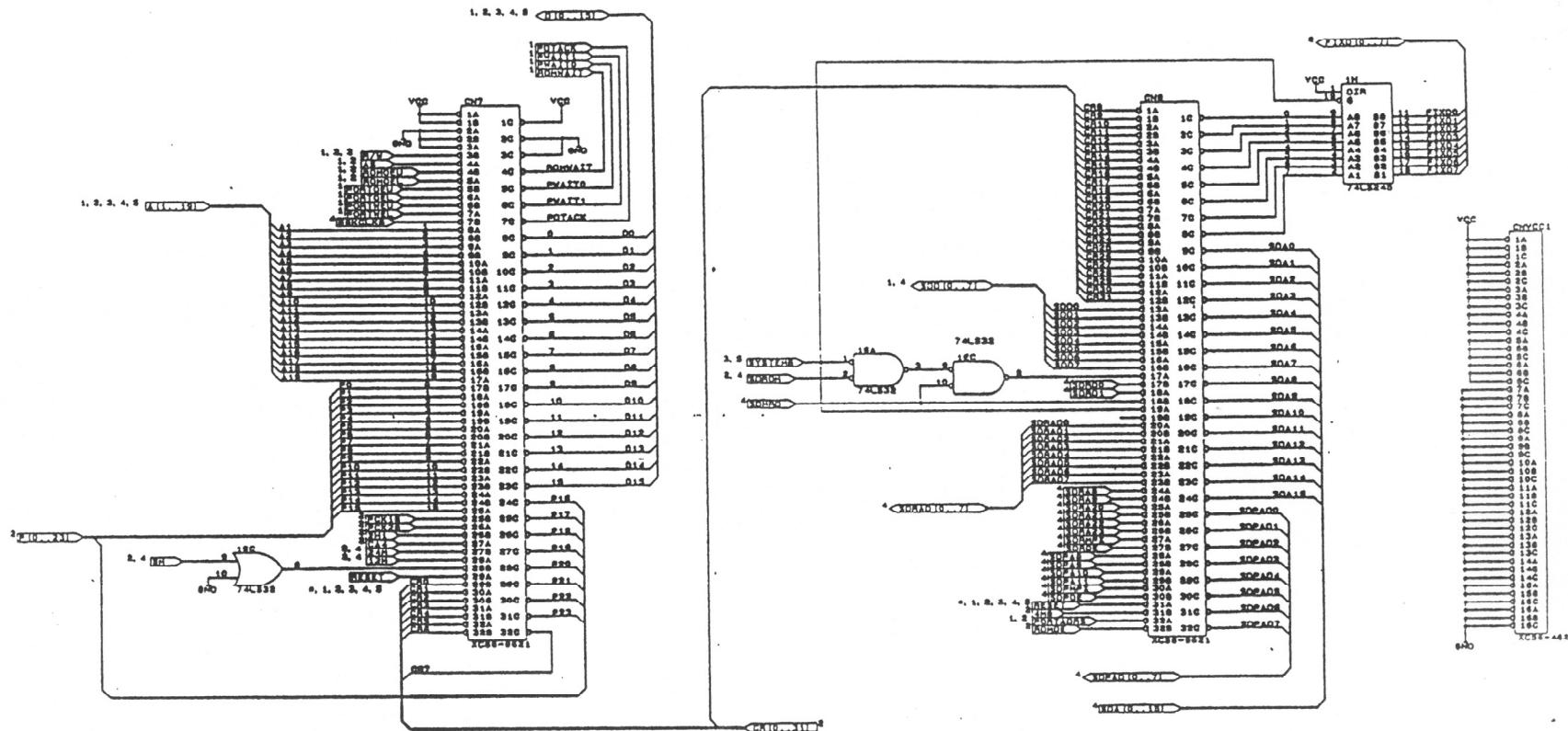
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CN8

A	B	C	
1 VCC	VCC	VCC	1
2 GND	GND	GND	2
3 GND	R/W	GND	3
4 AS	ROMOEU	ROMWAIT	4
5 ROMDEL	PORTOEU	PWAITO	5
6 PORTOEL	PORTWEU	PWAIT1	6
7 PORTWEL	68KCLKB	PDIACK	7
8 A1	A2	D0	8
9 A3	A4	D1	9
10 A5	A6	D2	10
11 A7	A8	D3	11
12 A9	A10	D4	12
13 A11	A12	D5	13
14 A13	A14	D6	14
15 A15	A16	D7	15
16 A17	A18	D8	16
17 A19	P0	D9	17
18 P1	P2	D10	18
19 P3	P4	D11	19
20 P5	P6	D12	20
21 P7	P8	D13	21
22 P9	P10	D14	22
23 P11	P12	D15	23
24 P13	P14	P16	24
25 P15	PCK18	P17	25
26 PCK28	2H1	P18	26
27 CA4	24M	P19	27
28 12M	8M	P20	28
29 RESET	CH0	P21	29
30 CR1	CR2	P22	30
31 CR3	CR4	P23	31
32 CR5	CR6	P27	32

CN8

A	B	C	
1 CR8	CR9	FIX00	1
2 CR10	CR11	FIX01	2
3 CR12	CR13	FIX02	3
4 CR14	CR15	FIX03	4
5 CR16	CR17	FIX04	5
6 CR18	CR19	FIX05	6
7 CR20	CR21	FIX06	7
8 CR22	CR23	FIX07	8
9 CR24	CR25	SDA0	9
10 CR26	CR27	SDA1	10
11 CR28	CR29	SDA2	11
12 CR30	CR31	SDA3	12
13 SDD0	SDD1	SDA4	13
14 SDD2	SDD3	SDA5	14
15 SDD4	SDD5	SDA6	15
16 SDD6	SDD7	SDA7	16
17 SDROM'	SDR00	SDA8	17
18 SDR01	SDR00	SDA9	18
19 SYSTEMB	N.C.	SDA10	19
20 SORAD0	SORAD1	SDA11	20
21 SORAD2	SORAD3	SDA12	21
22 SORAD4	SORAD5	SDA13	22
23 SORAD6	SORAD7	SDA14	23
24 SDRA8	SDRA9	SDA15	24
25 SDRA20	SDRA21	SDPA00	25
26 SDRA22	SDRA23	SDPA01	26
27 SDRMPX	SDROE	SDPA02	27
28 SDPAB	SDPA9	SDPA03	28
29 SDPA10	SDPA11	SDPA04	29
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31 RESET	4MA	SDPA06	31
32 PORTA0BS	ROMOF	SDPA07	32

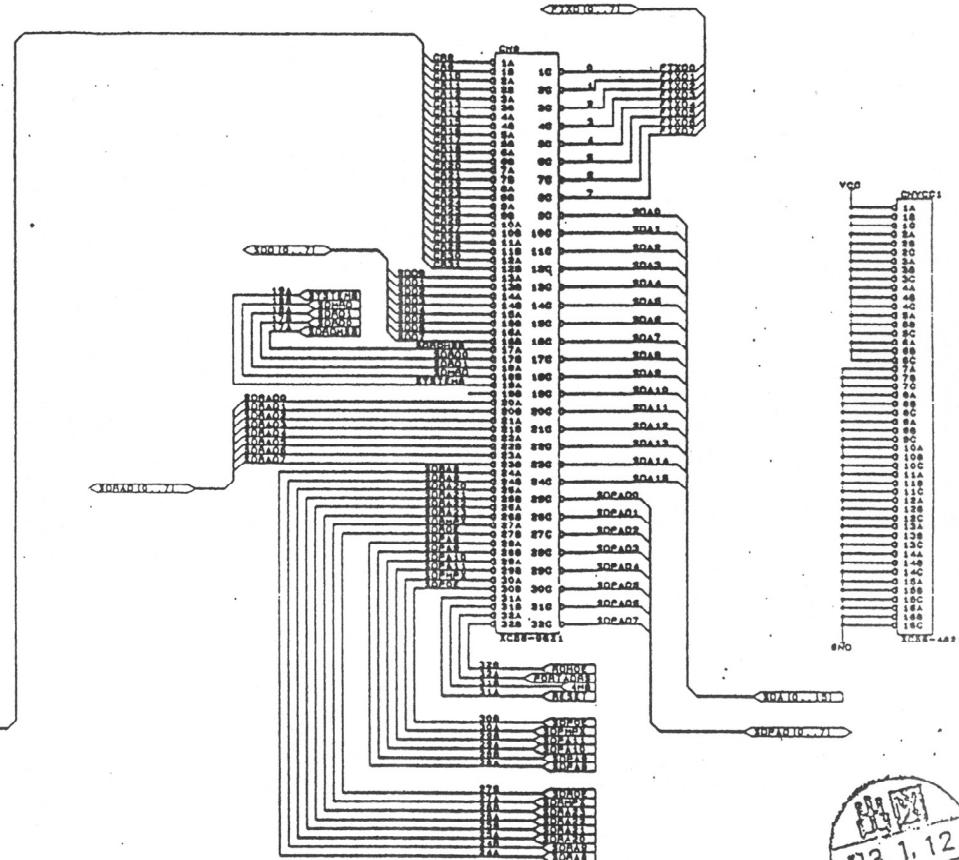
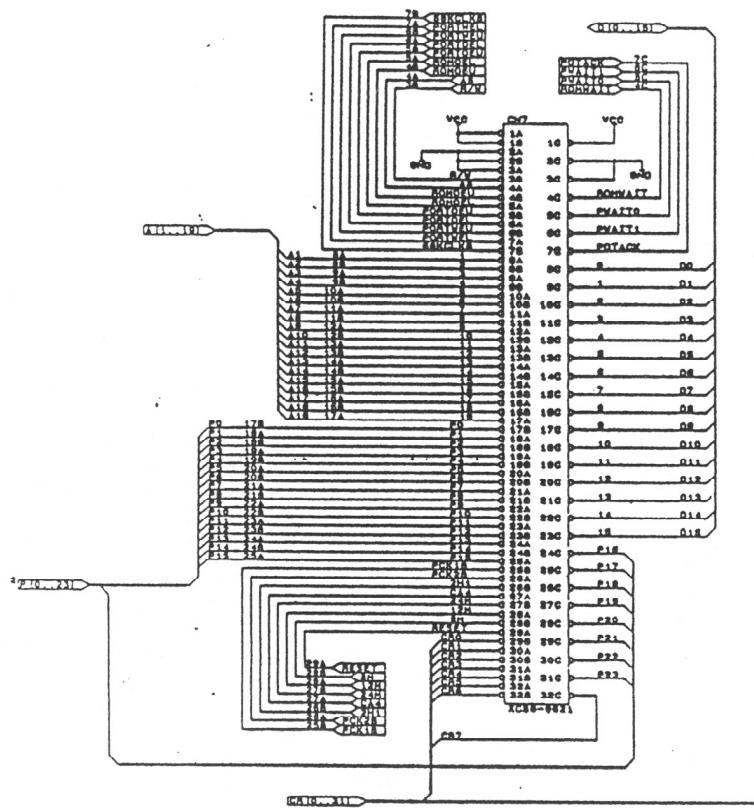
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5 VCC	VCC	VCC	5
6 VCC	VCC	VCC	6
7 GND	GND	GND	7
8 GND	GND	GND	8
9 GND	GND	GND	9
10 GND	GND	GND	10
11 GND	GND	GND	11
12 GND	GND	GND	12
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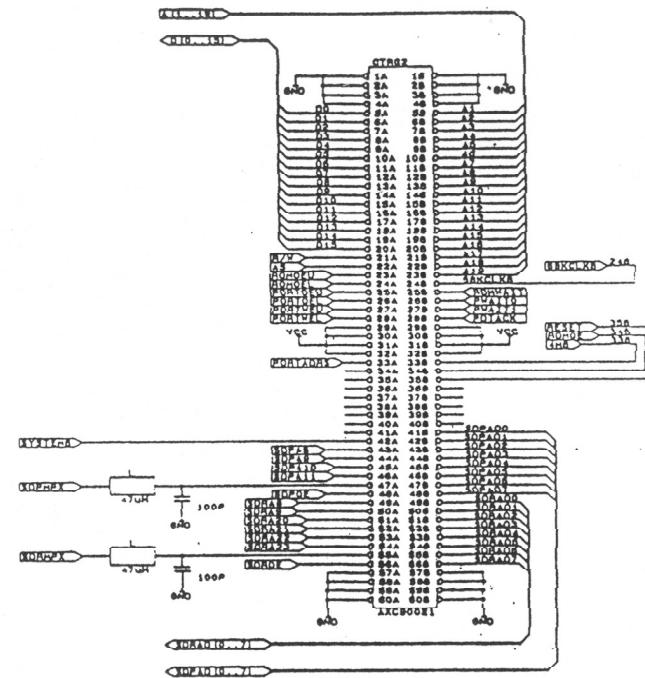
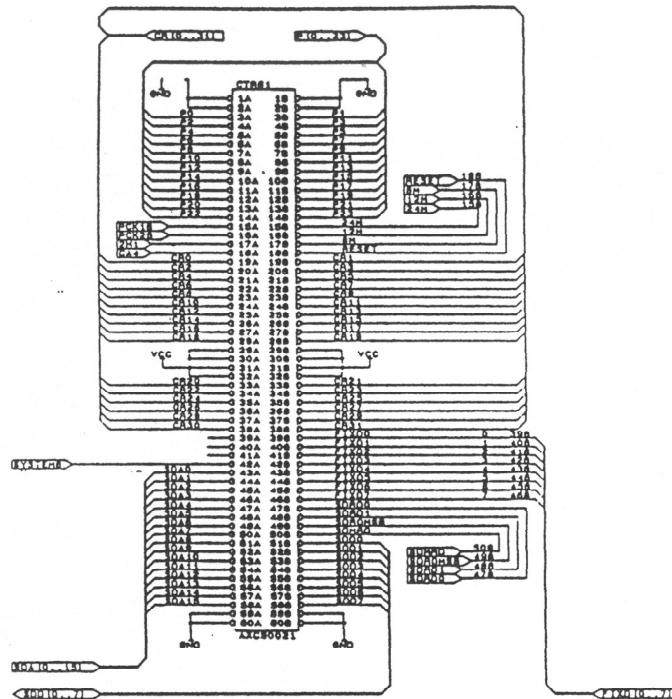
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