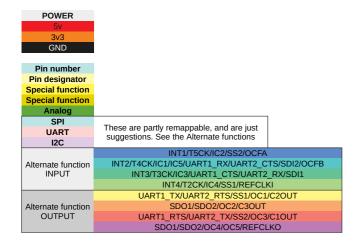
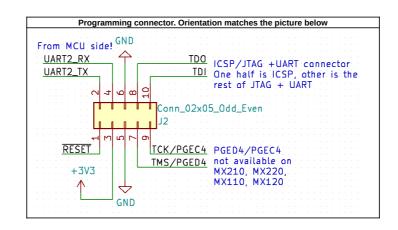
## Board pinout (ALL)





| Alt func. IN | Alt func. OUT | Comms | Analog | Special function | Pin designator | Pin number |                                 | Pin no. | Pin designator | Special function | Analog | Comms       | Alt. Function<br>OUT | Alt function. |
|--------------|---------------|-------|--------|------------------|----------------|------------|---------------------------------|---------|----------------|------------------|--------|-------------|----------------------|---------------|
|              |               |       |        |                  | 3v3            |            | □ J3 0 J1 0 m² 0                |         | 5v             |                  |        |             |                      |               |
|              |               |       |        |                  | 3v3            |            |                                 |         | GND            |                  |        |             |                      |               |
|              |               |       |        |                  | GND            |            |                                 | 9       | RB11           | USB D+ / PGED2   |        |             | SDO1//OC2/           | INT3/         |
|              |               |       |        |                  | GND            |            | D 13 2 0                        | 8       | RB10           | USB D- / PGEC2   |        |             | UART1_RTS/           | INT1/         |
| INT4/        | UART1_TX/     |       | AN0    | PGED3            | RA0            | 19         | 0 0 0                           | 43      | RB7            |                  |        |             | UART1_TX/            | INT4/         |
| INT3/        | SDO1//OC2/    |       | AN1    | PGEC3            | RA1            | 20         | O O                             | 41      | RB5            | USB ID           |        | SDI1 (MISO) | SDO1//OC2/           | INT3/         |
| INT1/        | UART1_RTS/    |       | AN2    | PGED1            | RB0            | 21         | <b>○</b> ■ ■ ■ □ R10 ₹ <b>○</b> | 11      | RB13           |                  | AN11   | SDO1 (MOSI) | SDO1//OC5/           | INT2/         |
| INT3/        | SDO1//OC2/    |       | AN3    | PGEC1            | RB1            | 22         |                                 | 14      | RB14           |                  | AN10   | SCK1        | UART1_RTS/           | INT1/         |
| INT2/        | SDO1//OC5/    | SDA2  | AN4    |                  | RB2            | 23         |                                 | 3       | RC7            |                  |        | UART1_TX    | UART1_TX/            | INT4/         |
| INT4/        | UART1_TX/     | SCL2  | AN5    |                  | RB3            | 24         | O THE STATE OF                  | 2       | RC6            |                  |        | UART1_RX    | SDO1//OC5/           | INT2/         |
| INT4/        | UART1_TX/     |       | AN6    |                  | RC0            | 25         | <b>O</b>                        | 1       | RB9            |                  |        | SDA1        | UART1_RTS/           | INT1/         |
| INT2/        | SDO1//OC5/    |       | AN7    |                  | RC1            | 26         |                                 | 44      | RB8            |                  |        | SCL1        | SDO1//OC2/           | INT3/         |
| INT1/        | UART1_RTS/    |       | AN8    |                  | RC2            | 27         | 0 44 14 0                       | 15      | RB15           |                  | AN9    |             | UART1_TX/            | INT4/         |
|              |               |       |        |                  | 3v3            |            |                                 | 36      | RC3            |                  | AN12   |             | SDO1//OC5/           | INT2/         |
| INT4/        | UART1_TX/     |       |        | BUTTON_USER      | RC5            | 38         |                                 | 37      | RC4            | LED_USER         |        |             | UART1_RTS/           | INT1/         |
|              |               |       |        | TCK/PGEC4        | RA7            | 13         | ● R8                            | 35      | RA9            | TDI              |        |             | SDO1//OC2/           | INT3/         |
|              |               |       |        | TMS/PGED4        | RA10           | 12         | ● BOOT USER O                   | 32      | RA8            | TDO              |        |             | SDO1//OC2/           | INT3/         |
|              |               |       |        |                  | GNI            |            | 0 2 1 50                        |         | GND            |                  |        |             |                      |               |
|              |               |       |        |                  | 3v3            |            | SW2<br>SW1<br>SW1<br>O O        | 4       | RC8            |                  |        | UART2_RX    | SDO1//OC2/           | INT3/         |
|              |               |       |        | MCLR/RESET       | MCLR/RESET     | 18         |                                 | 5       | RC9            |                  |        | UART2_TX    | UART1_RTS/           | INT1/         |
|              |               |       |        |                  |                |            |                                 |         |                |                  |        |             |                      |               |
|              |               |       |        |                  |                |            |                                 |         |                |                  |        |             |                      |               |