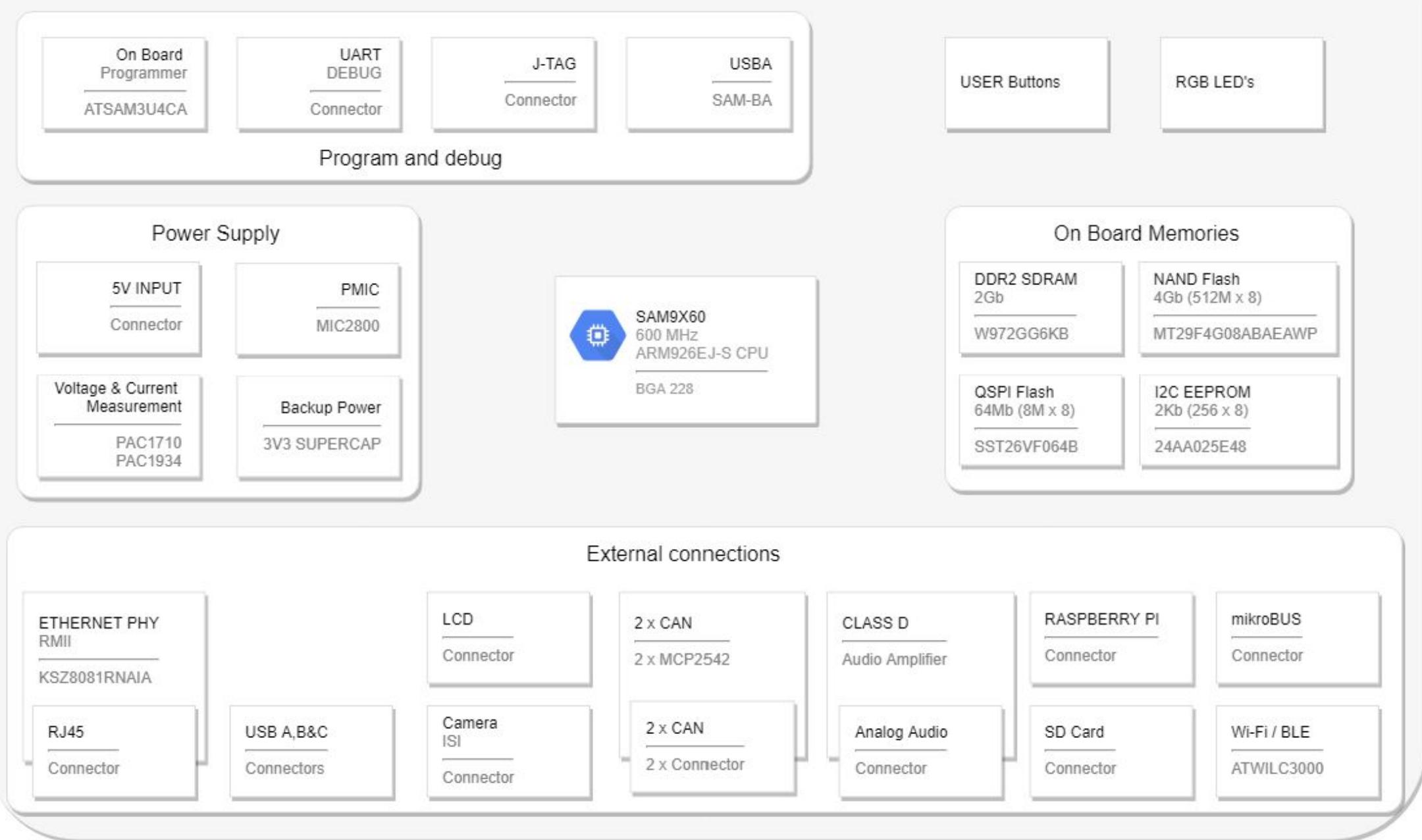
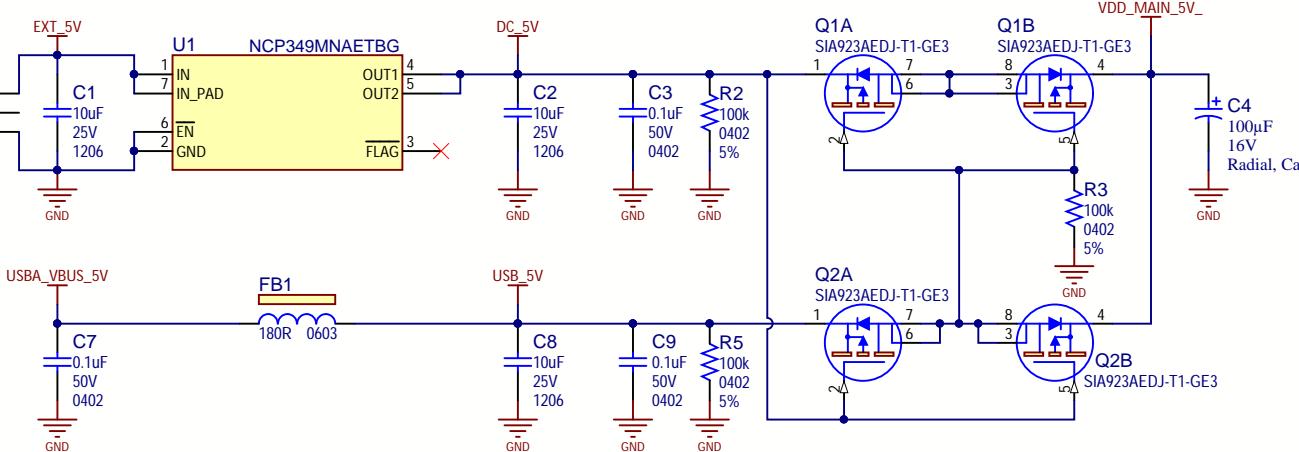
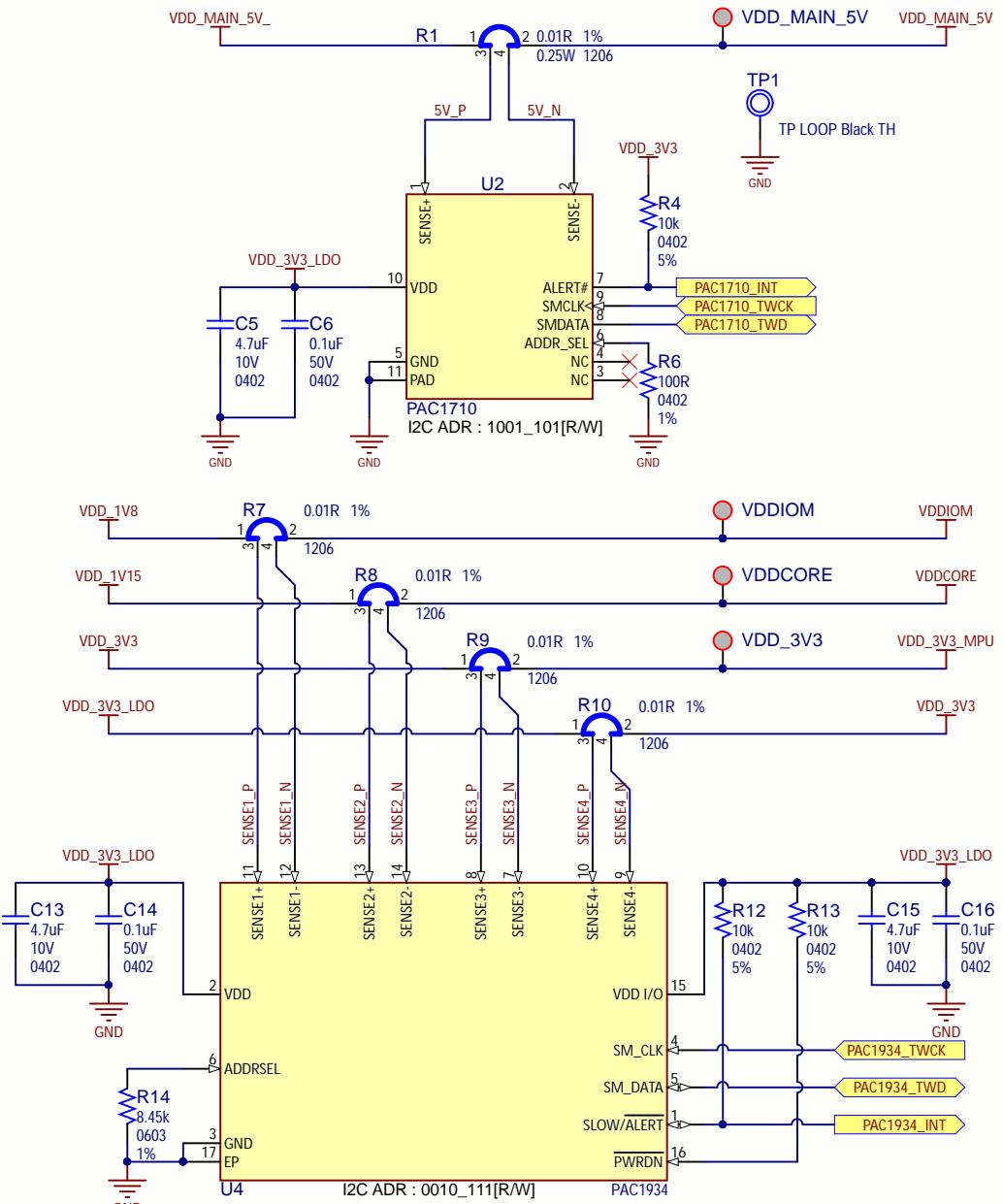
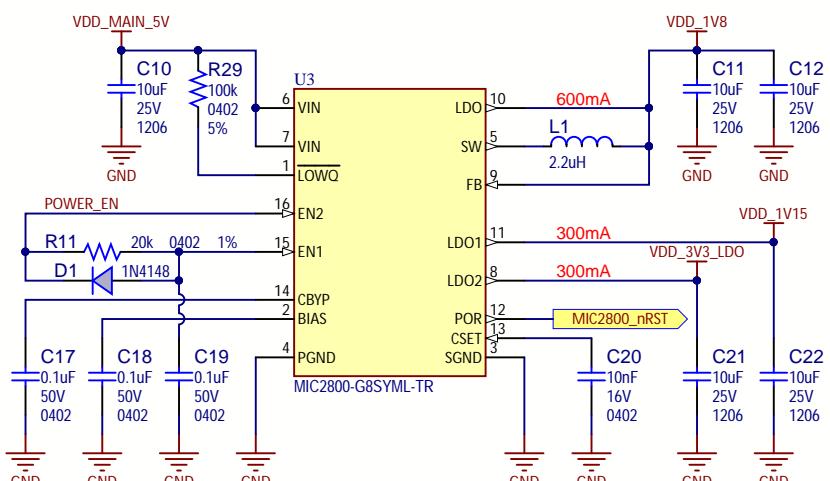
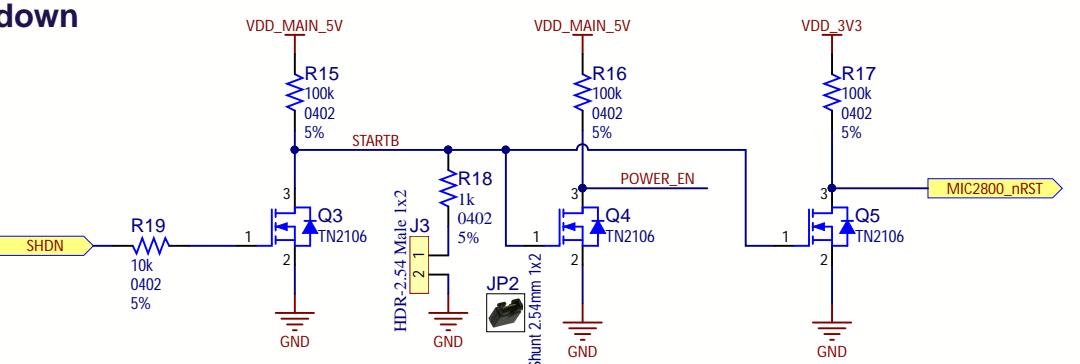
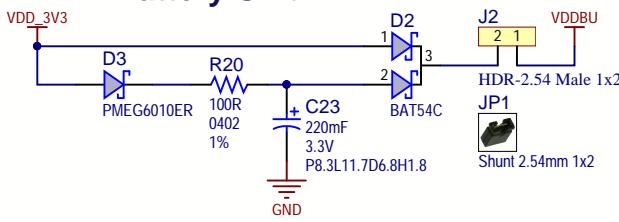




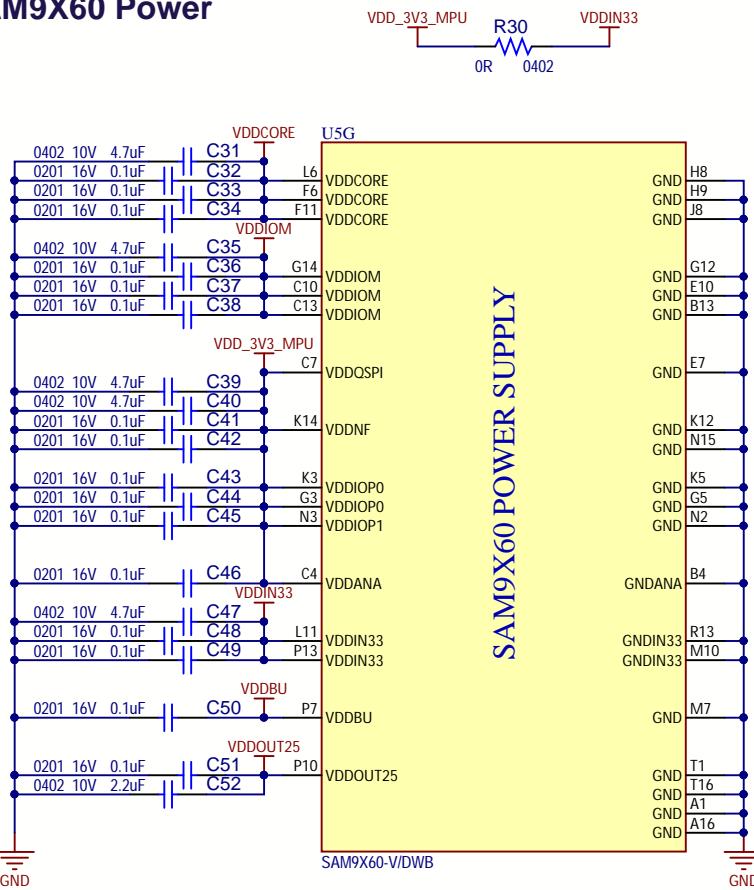
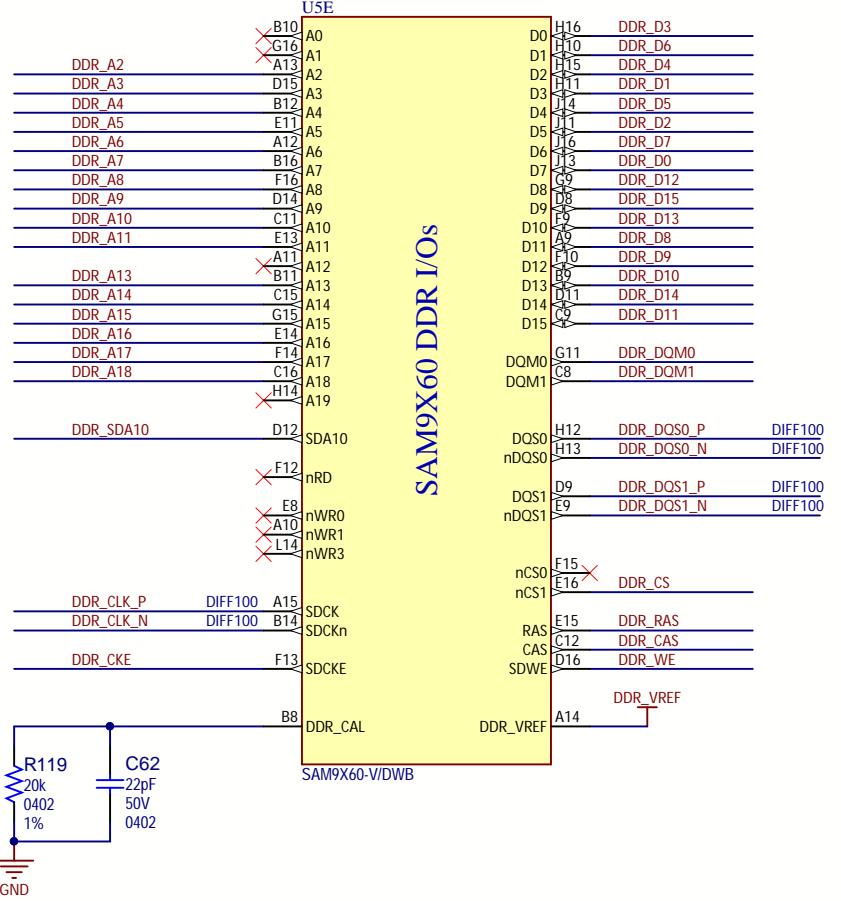
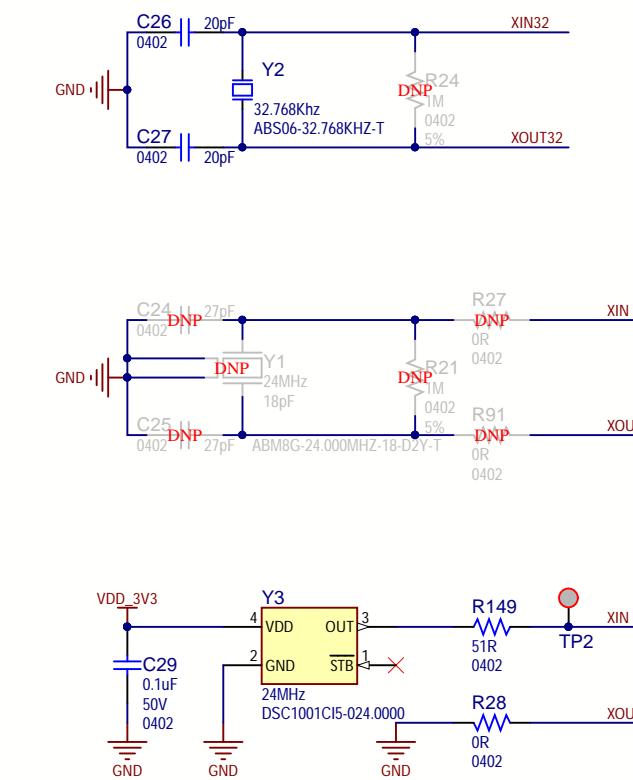
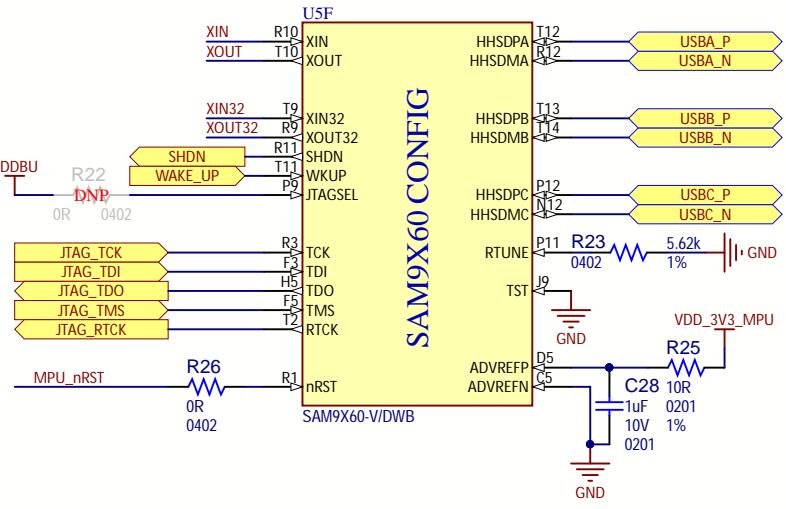
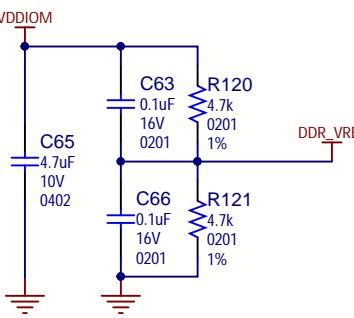
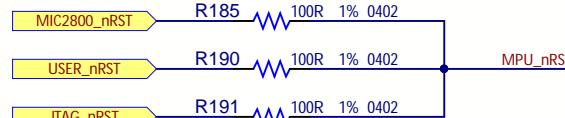
## SAM9X60 - EVALUATION KIT



Drawn By: IGn	Engineer: IGn	<b>MICROCHIP</b>	
PartNumber: DT100126	Project Title <i>SAM9X60 Evaluation Kit</i>		
Sheet Title <b>Block Diagram</b>	Variant Name <b>Standard_WILC3000_DNP</b>	Designed with <b>Altium</b> Altium.com	
Size B	Sch #: 03-11039 Revision:2	Date: 9/20/2019 10:27:44 AM	Sheet 1 of 13
File: 01 - Block Diagram.SchDoc			

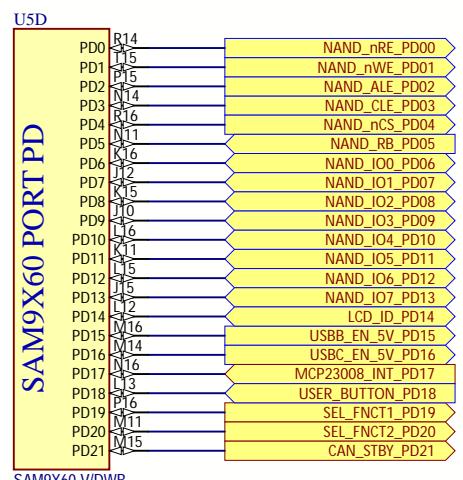
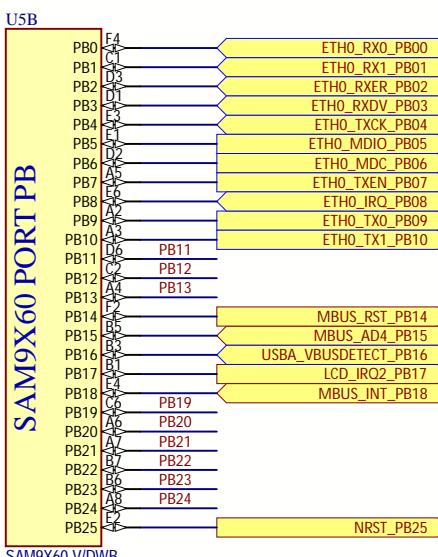
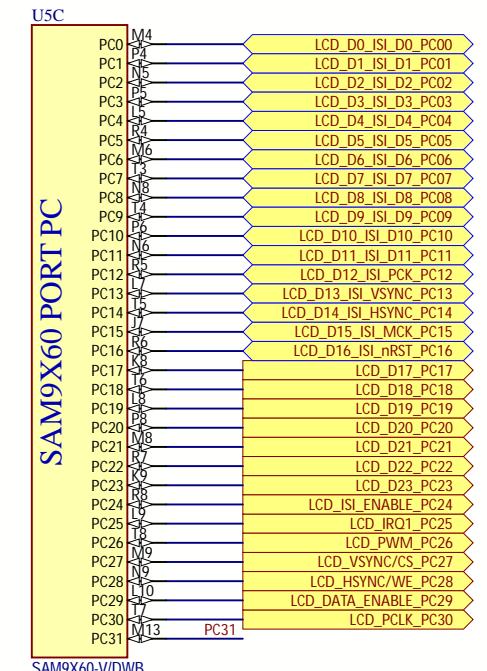
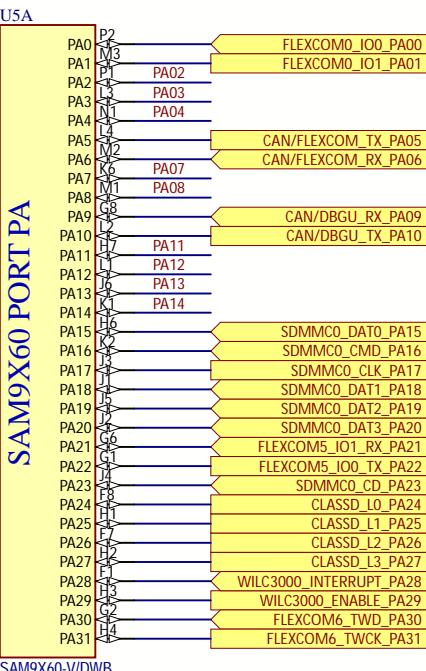
**Input Power Options****Power Measurement****PMIC****Shut-down****Battery Unit**

Drawn By: IGn	
Engineer: IGn	
PartNumber: DT100126	Project Title <b>SAM9X60 Evaluation Kit</b>
Sheet Title <b>Power supply</b>	Variant Name <b>Standard_WILC3000_DNP</b>
Size <b>B</b>	Sch #: 03-11039
Revision: 2	Date: 9/20/2019 10:27:44 AM
File: 02 - Power supply.SchDoc	Sheet 2 of 13

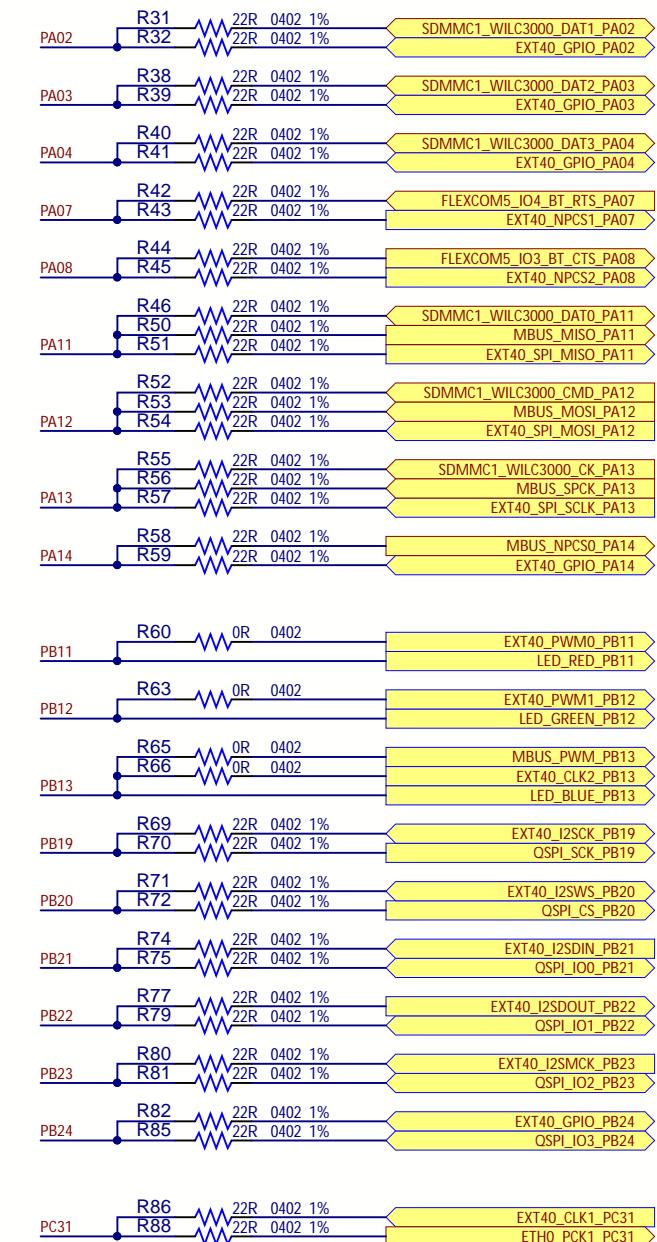
**SAM9X60 Power****SAM9X60 DDR Controller****SAM9X60 Clocks****SAM9X60 Core****DDR\_VREF****SAM9X60 Reset**

Drawn By: IGn	MICROCHIP
Engineer: IGn	
PartNumber: DT100126	Project Title <i>SAM9X60 Evaluation Kit</i>
Sheet Title <b>Processor SAM9X60</b>	Variant Name <b>Standard_WILC3000_DNP</b>
Size <b>B</b>	Date: 9/20/2019 10:27:44 AM
Revision: 2	Sheet 3 of 13
File: 03 - Processor SAM9X60.SchDoc	

## SAM9X60 PIOs



## SAM9X60 PIO Muxing



Drawn By:  
IGn



Engineer:  
IGn

PartNumber:

Project Title:

**SAM9X60 Evaluation Kit**

Sheet Title:

Variant Name:

Processor IOs

**Standard\_WILC3000\_DNP**

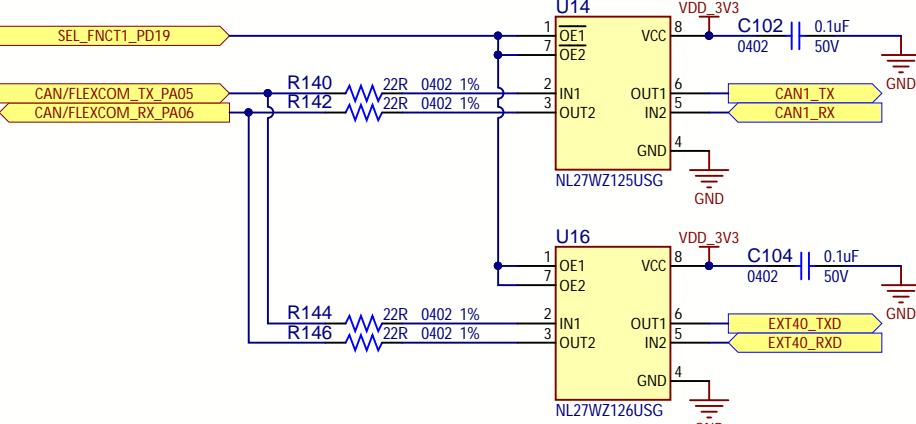
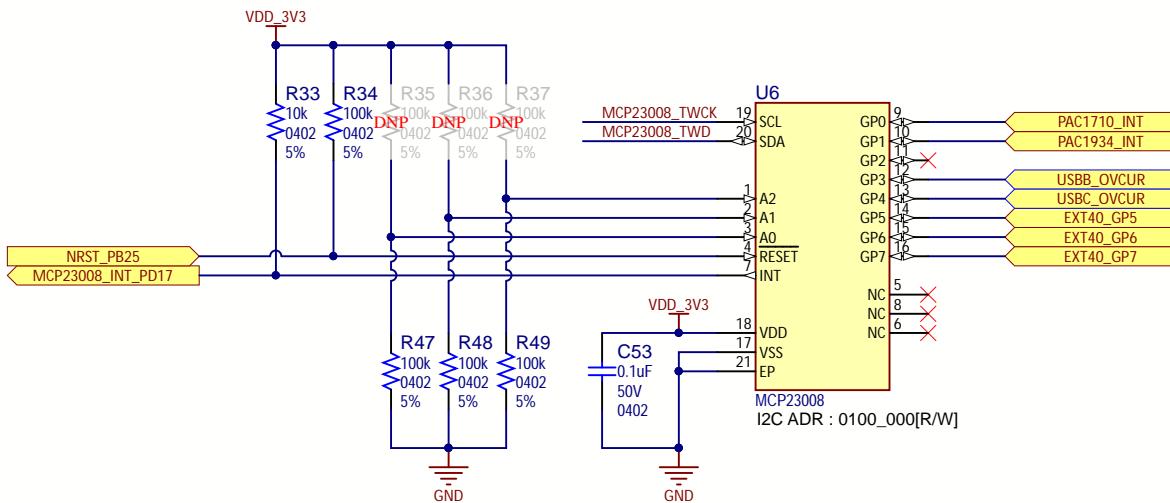
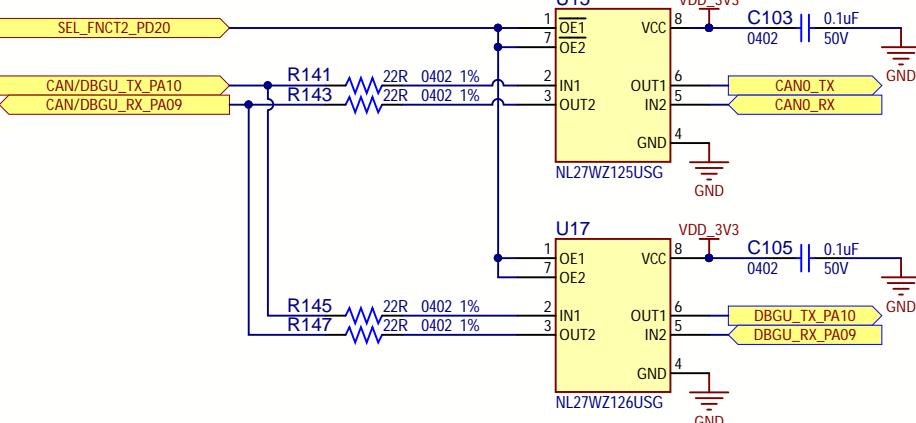
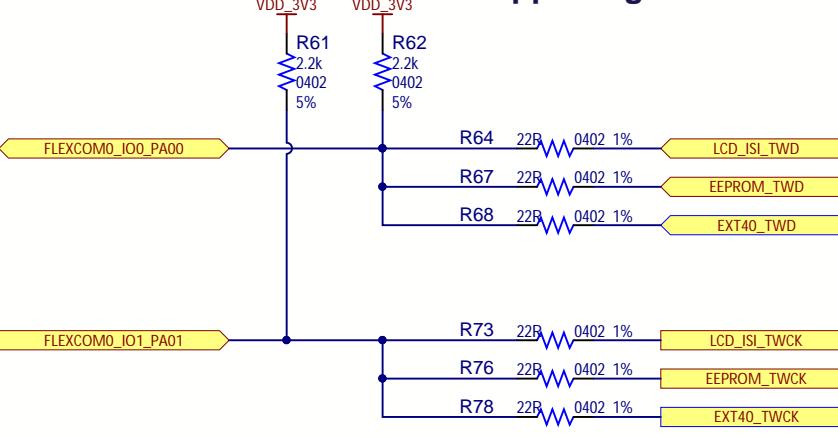
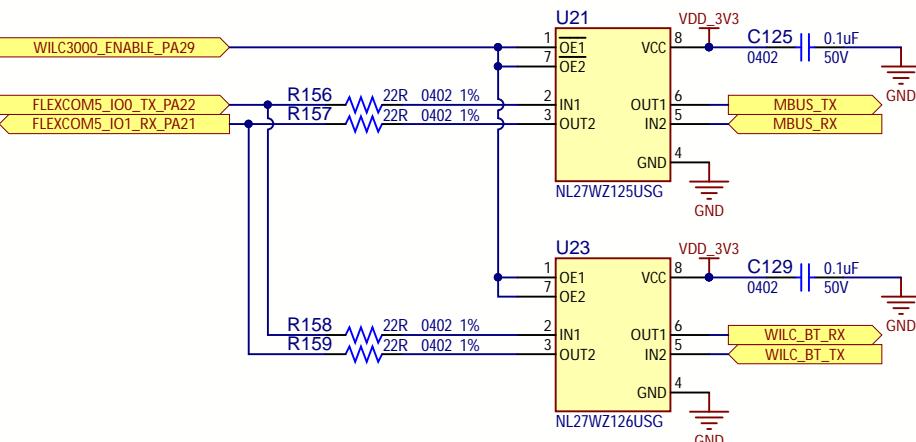
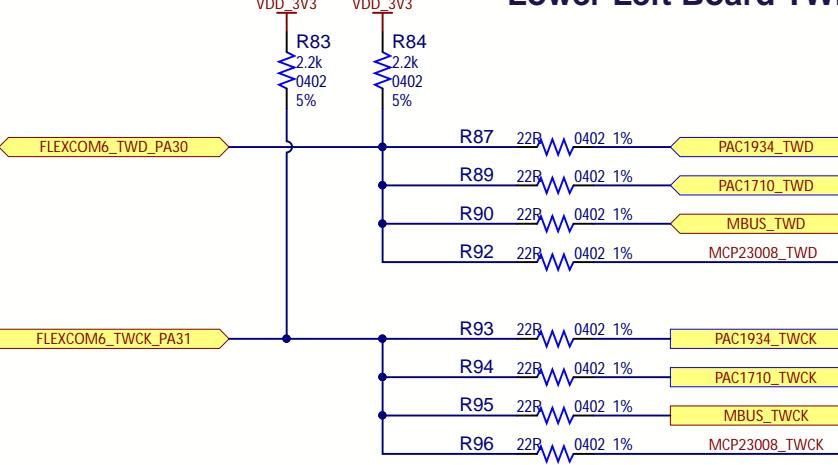
Size: **B** Sch #: 03-11039 Date: 9/20/2019 10:27:45 AM

Revision: 2

Sheet: 4 of 13

File: 04 - Processor IOs.SchDoc

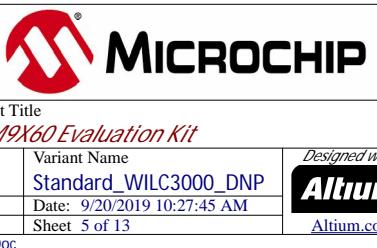
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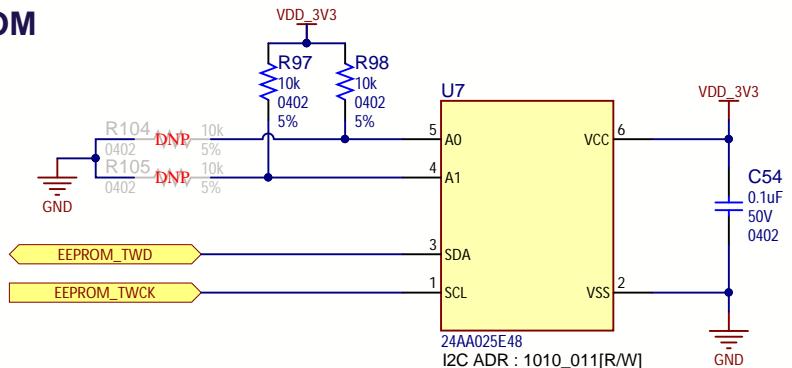
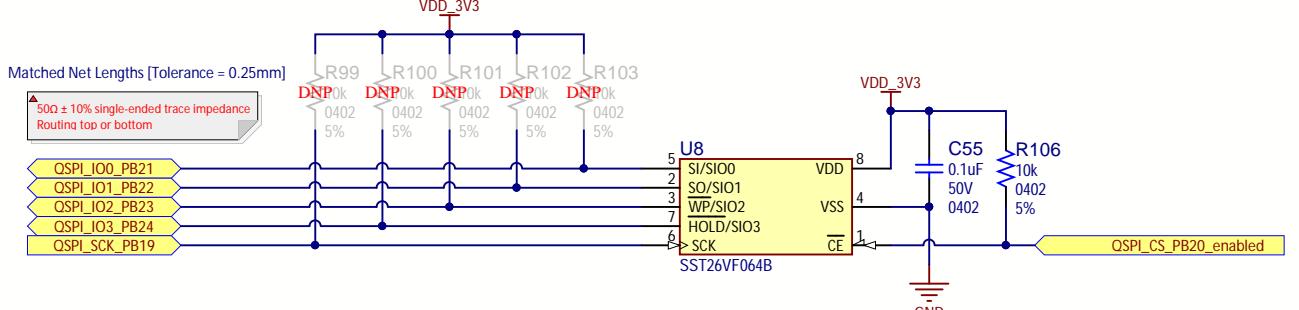
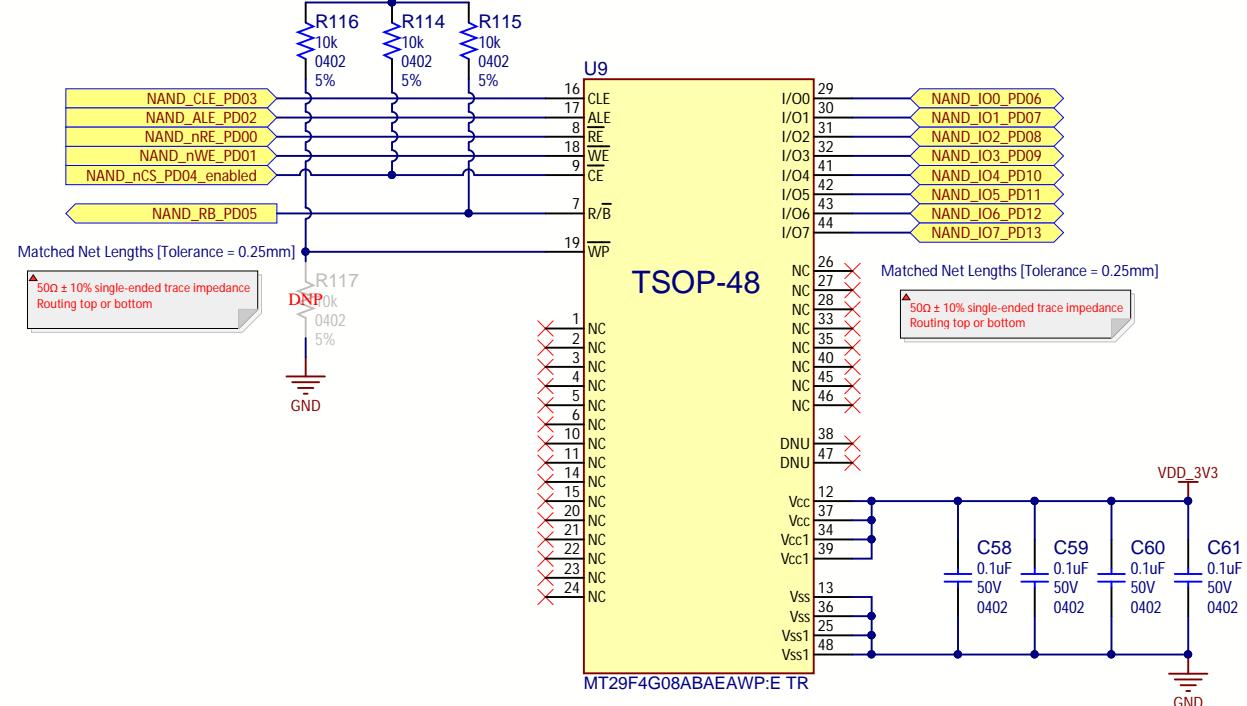
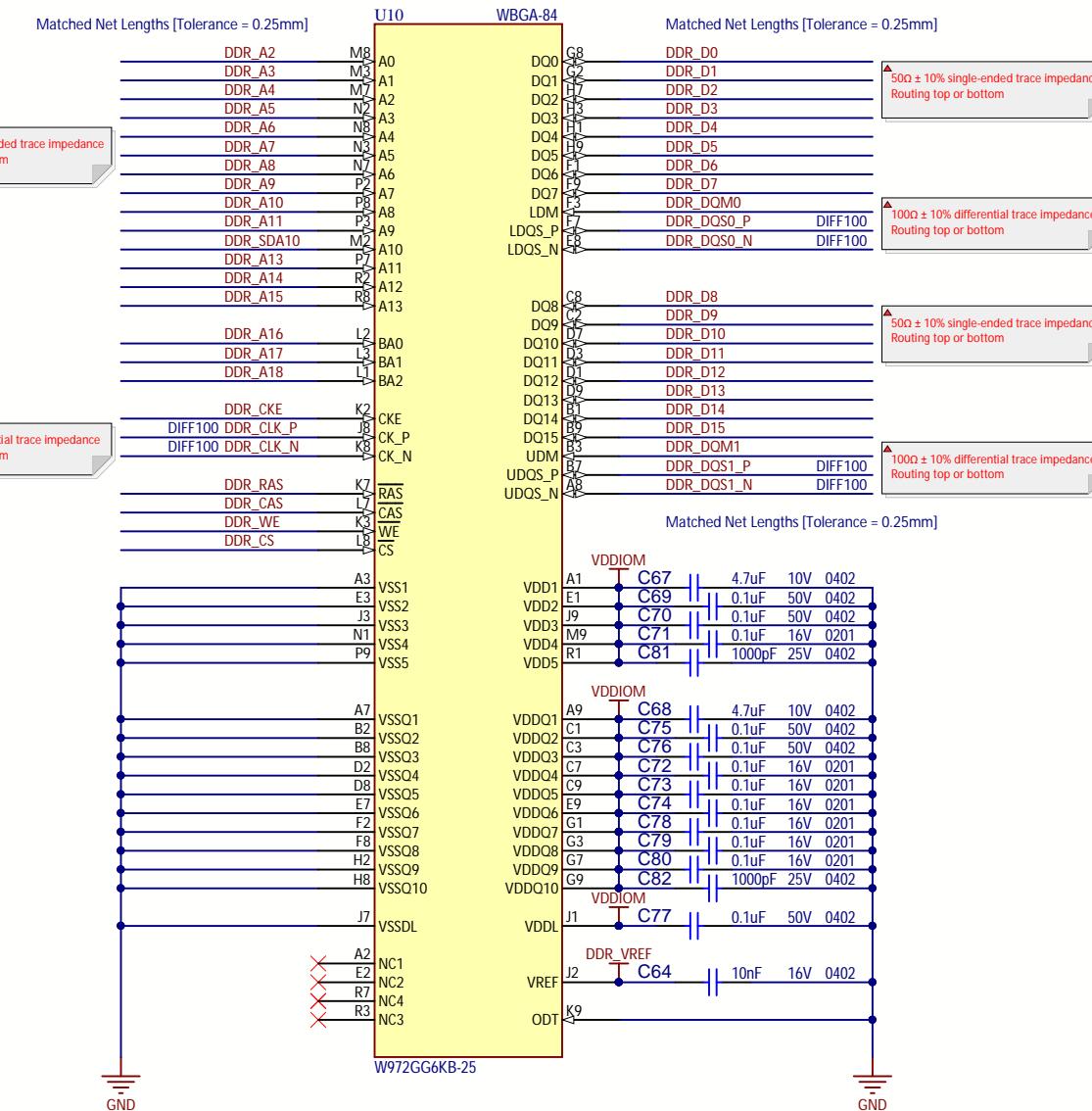
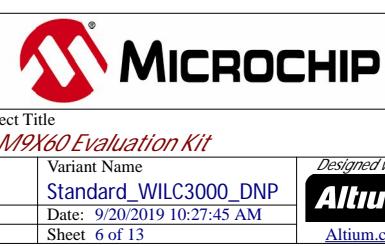
**CAN1 / EXT40 UART Selection****Port Expander****CAN0 / DEBUG UART Selection****Upper Right Board TWI****MikroBUS UART / WILC BT UART Selection****Lower Left Board TWI**Drawn By:  
IGNEngineer:  
IGNPartNumber:  
DT100126Project Title:  
SAM9X60 Evaluation KitSheet Title:  
Processor IO ExpansionsVariant Name:  
Standard\_WILC3000\_DNP

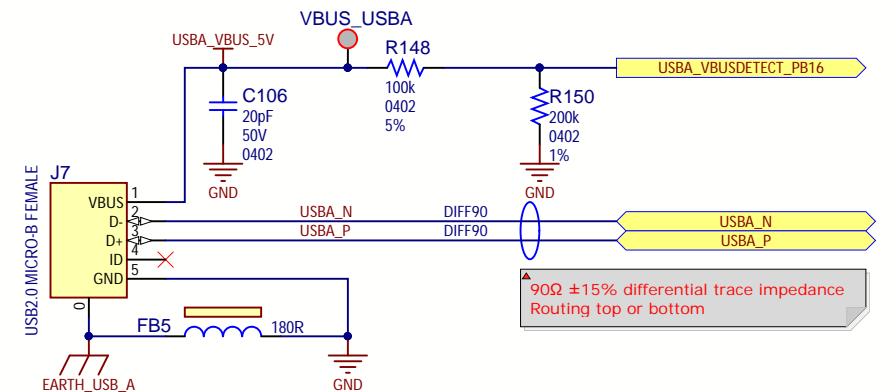
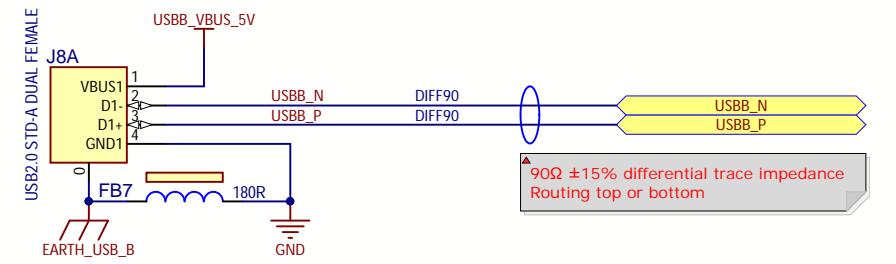
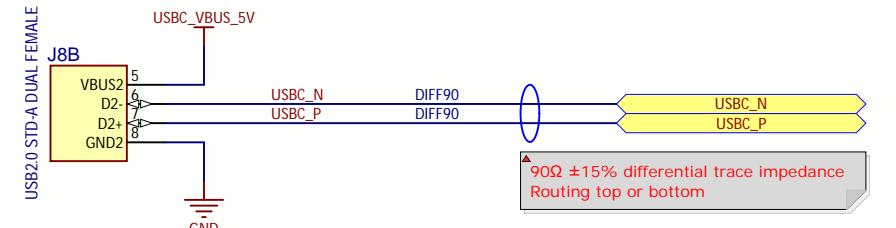
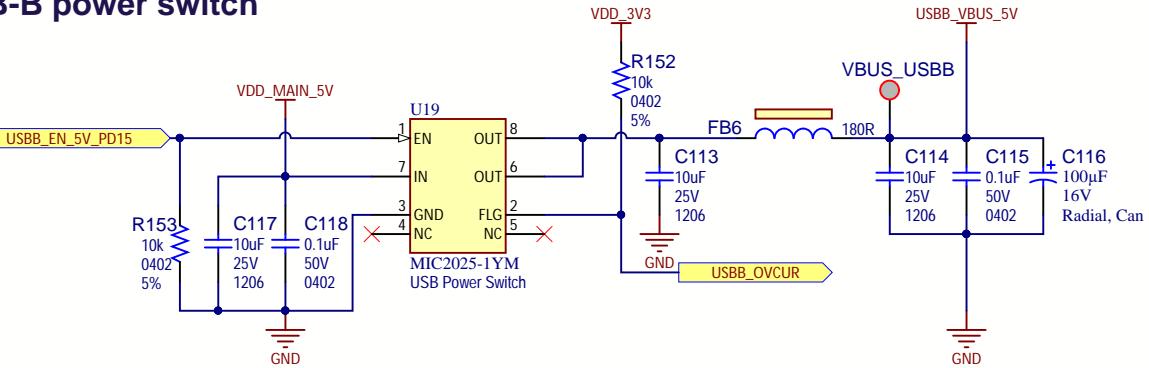
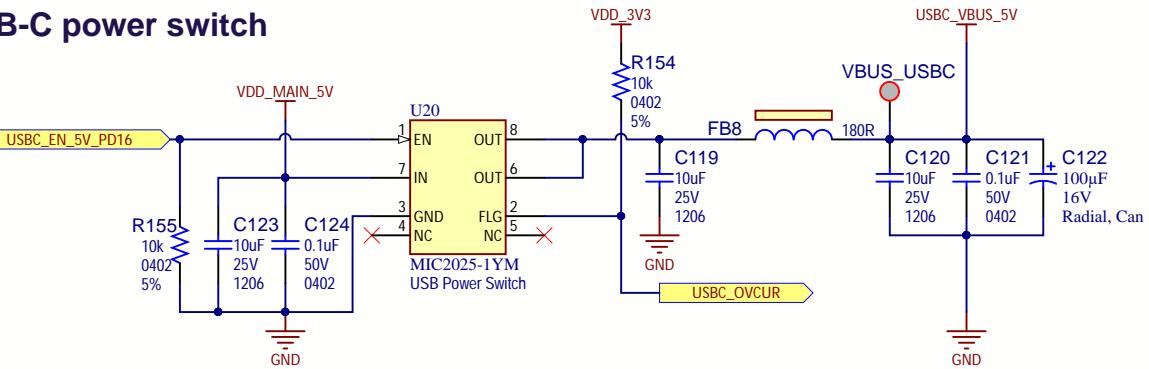
Size: B Sch #: 03-11039 Date: 9/20/2019 10:27:45 AM

Revision: 2 Sheet: 5 of 13

File: 05 - Processor IO Expansions.SchDoc



**TWI EEPROM****Serial Quad I/O Flash****4Gb NAND Flash****2Gb DDR2-800 SDRAM 16bit**Drawn By:  
IGnEngineer:  
IGnProject Title:  
**SAM9X60 Evaluation Kit**PartNumber:  
DT100126Sheet Title  
**On Board Memories**Variant Name  
**Standard\_WILC3000\_DNP**Size **B**Sch #: **03-11039**Date: **9/20/2019 10:27:45 AM**Revision: **2**Sheet **6** of 13File: **06 - On Board Memories.SchDoc**

**USB-A port****USB-B port****USB-C port****USB-B power switch****USB-C power switch**Drawn By:  
IgNEngineer:  
IgNPartNumber:  
DT100126Project Title:  
*SAM9X60 Evaluation Kit*

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Altium.com

Sheet Title  
**USB interfaces**Size  
**B**Revision:  
2

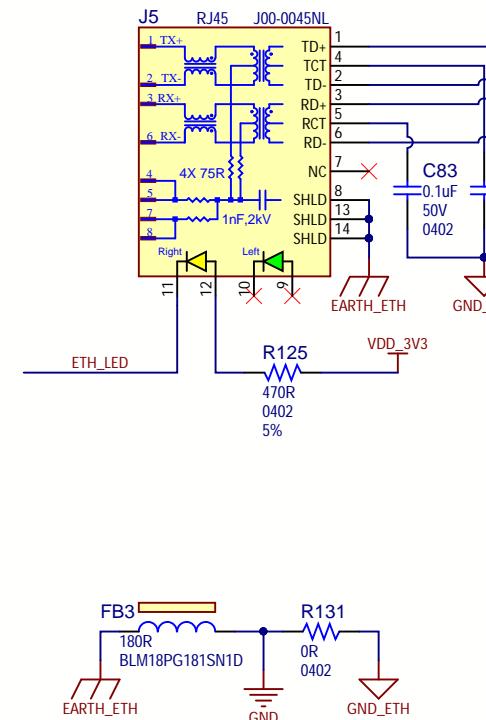
Sch #: 03-11039

Date: 9/20/2019 10:27:46 AM

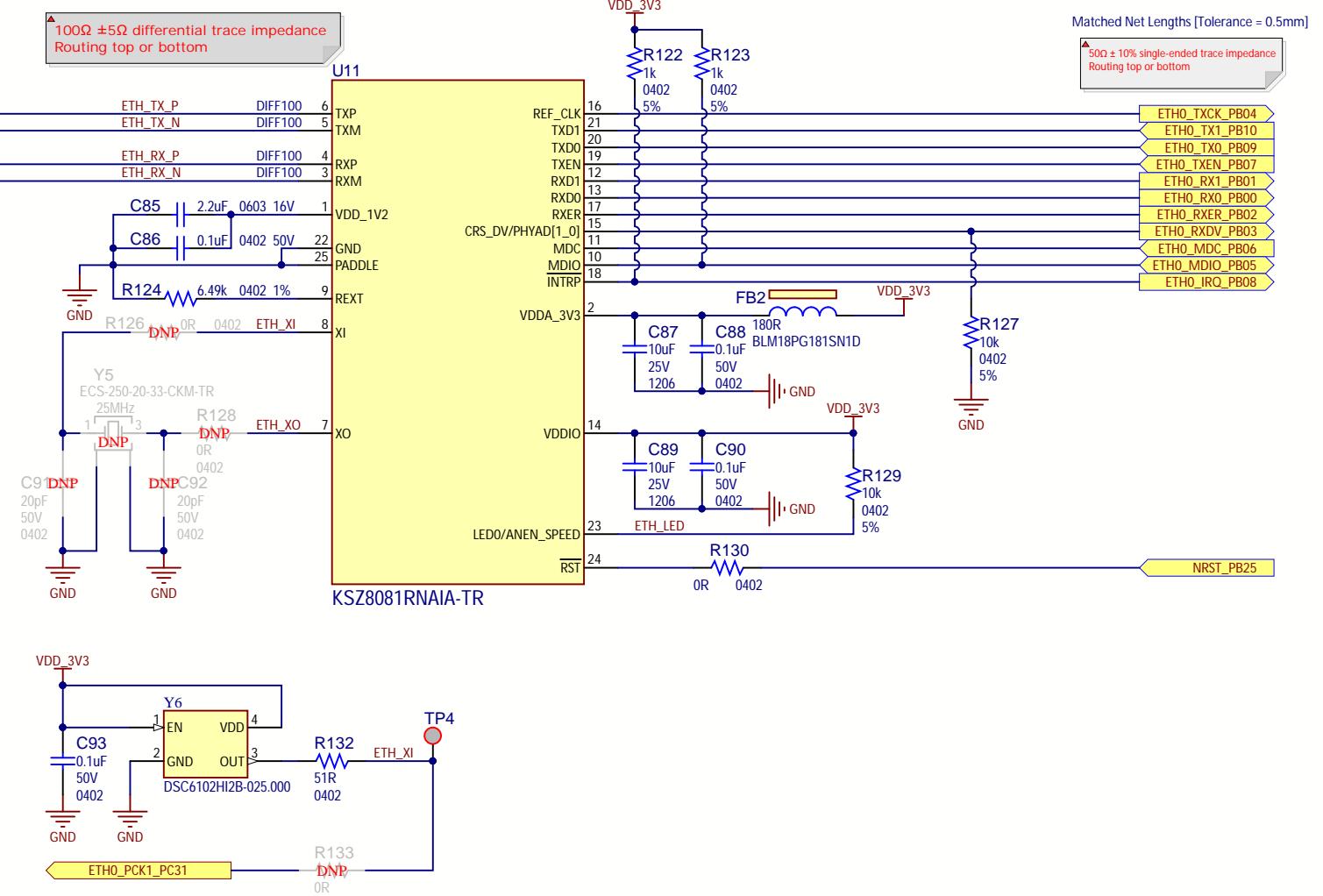
Sheet  
7 of 13

File: 07 - USB ports.SchDoc

## Ethernet Connector



## Ethernet PHY



Drawn By:  
IGn

Engineer:  
IGn

PartNumber:  
DT100126

 **MICROCHIP**

Project Title  
*SAM9X60 Evaluation Kit*

Sheet Title:  
Ethernet MAC

Variant Name  
Standard\_WILC3000\_DNP

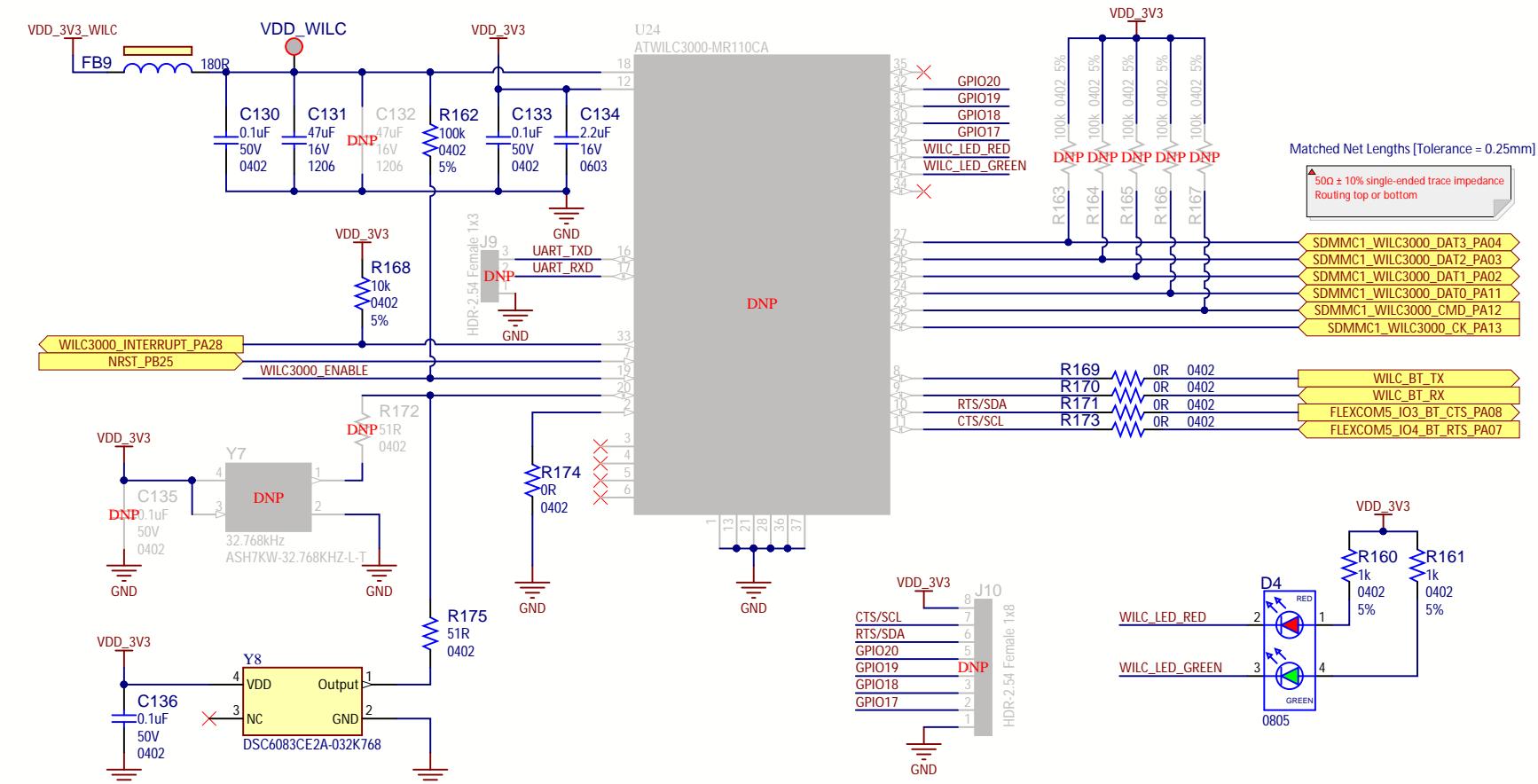
Size B  
Sch #: 03-11039  
Revision:2

Date: 9/20/2019 10:27:46 AM  
Sheet 8 of 13

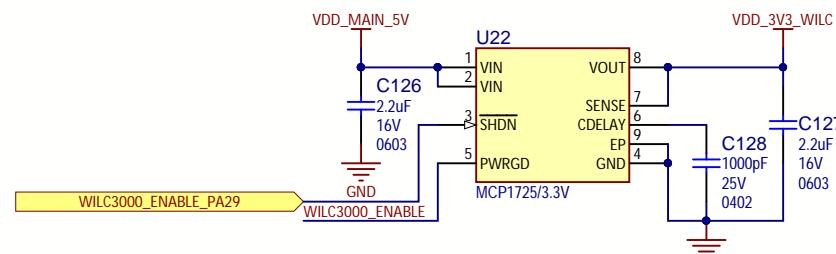
File: 08 - Ethernet MAC.SchDoc

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Altium.com

## WiFi / BT MODULE



## WiFi / BT ENABLE



Drawn By:  
IGn

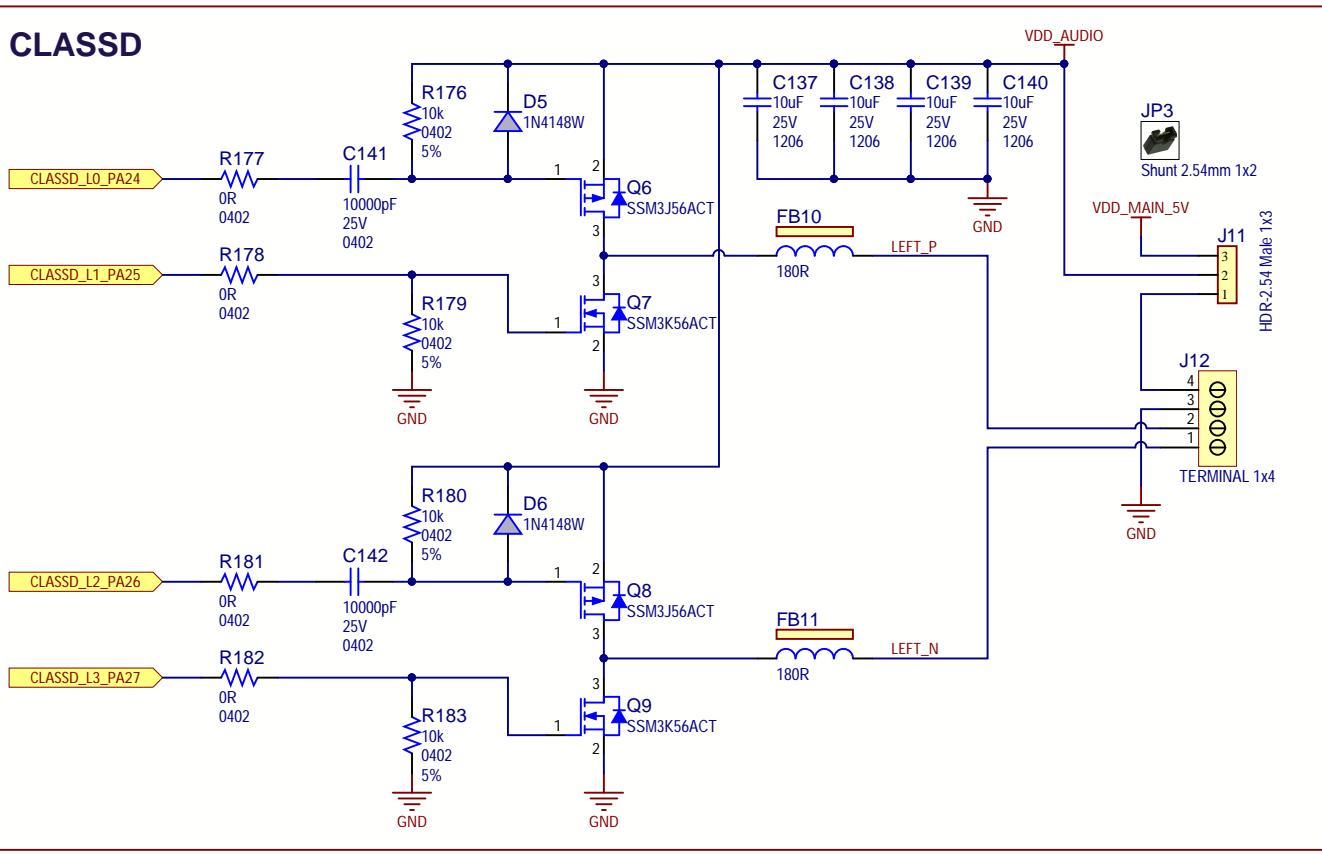
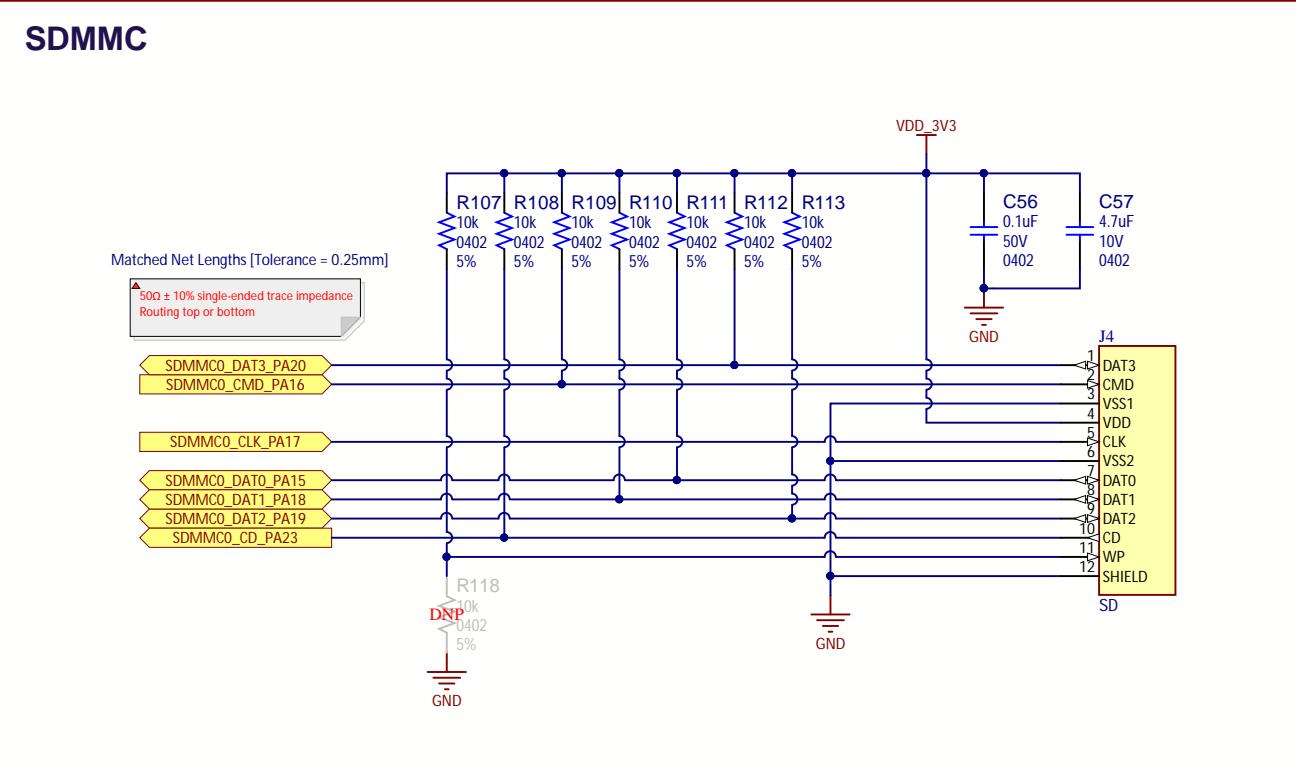
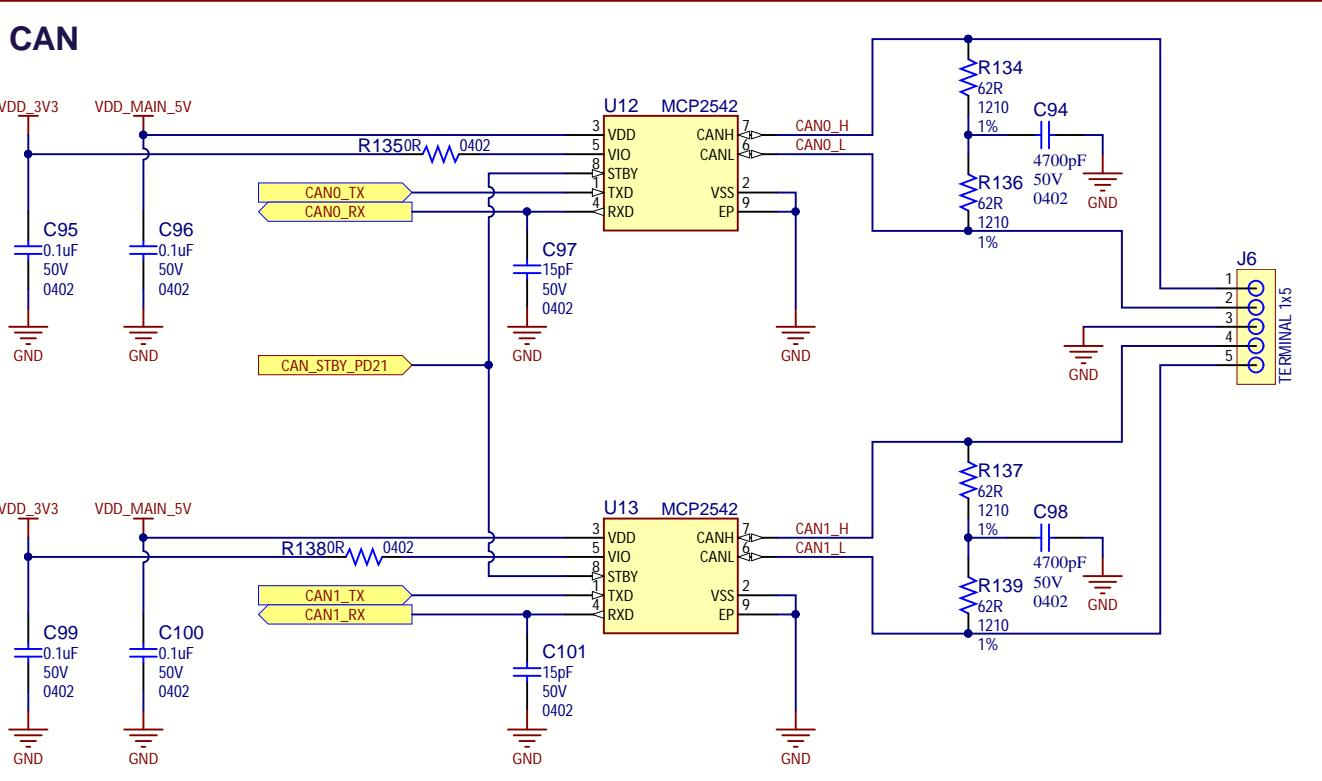
Engineer:  
IGn

PartNumber:  
DT100126

Project Title:  
**SAM9X60 Evaluation Kit**

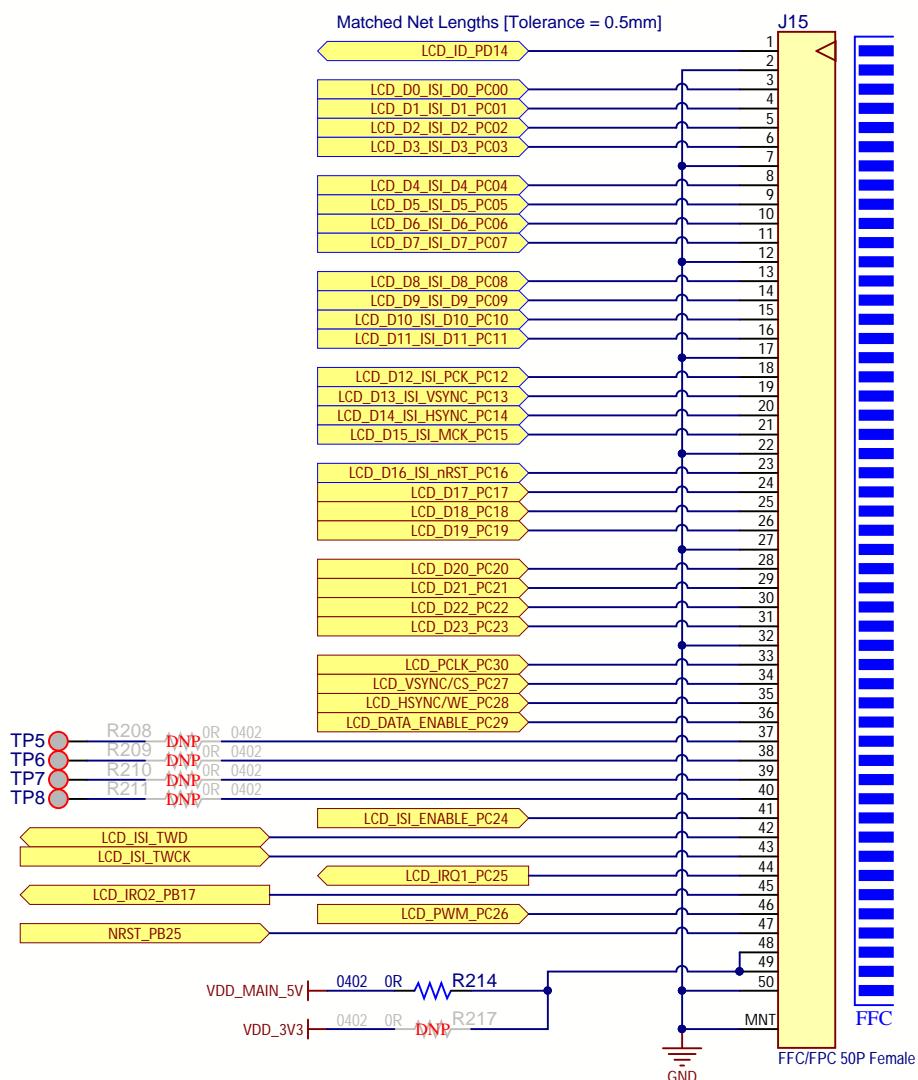
Sheet Title <b>Wi-Fi BT</b>	Variant Name <b>Standard_WILC3000_DNP</b>	Designed with <b>Altium</b> Altium.com
Size <b>B</b>	Sch #: 03-11039 Revision: 2	Date: 9/20/2019 10:27:46 AM Sheet 9 of 13

File: 09 - Wi-Fi BT module.SchDoc

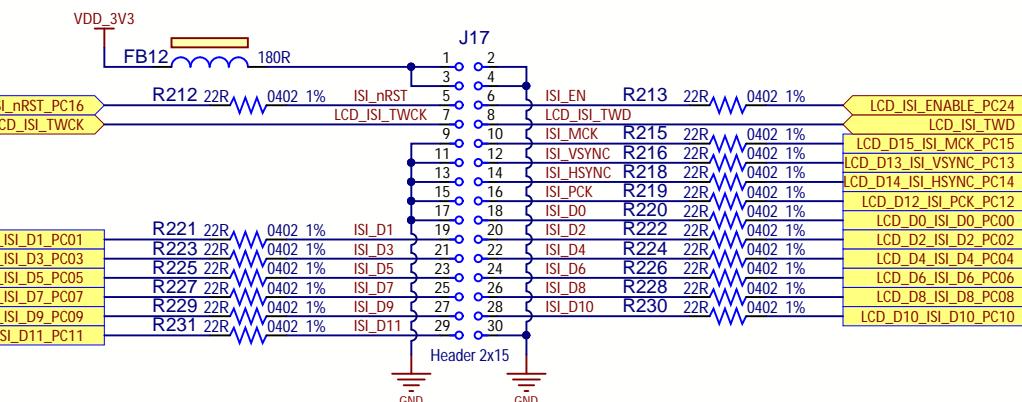


Drawn By: IGn	<b>MICROCHIP</b>	
Engineer: IGn		
PartNumber: DT100126	Project Title <i>SAM9X60 Evaluation Kit</i>	
Sheet Title <b>SDMMC, CAN &amp; CLASSD</b>	Variant Name <b>Standard_WILC3000_DNP</b>	Designed with <b>Altium</b> Altium.com
Size <b>B</b>	Sch #: 03-11039	Date: 9/20/2019 10:27:46 AM
Revision: 2		Sheet 10 of 13
File: 10 - SDMMC, CAN & CLASSD.SchDoc		

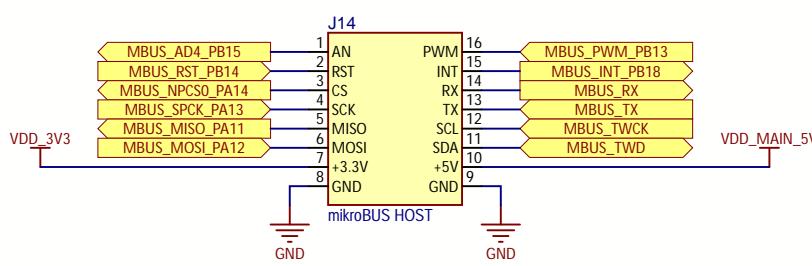
## LCD



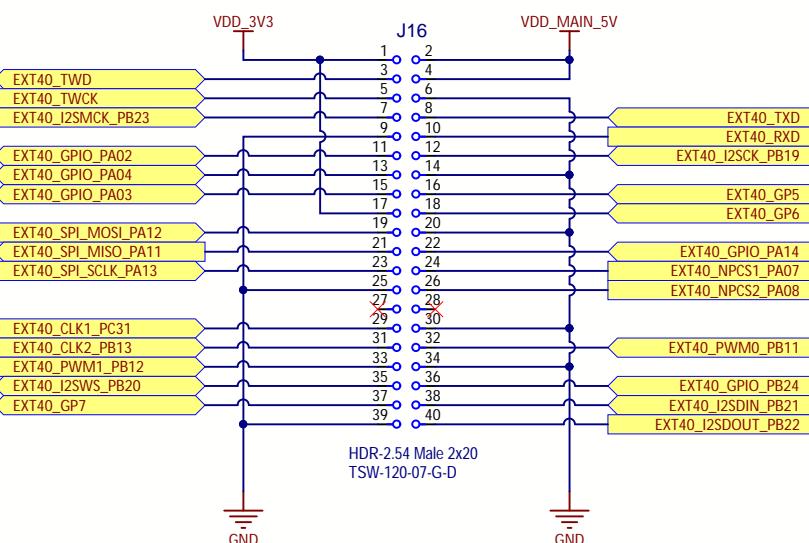
## ISI Camera



## MikroBUS

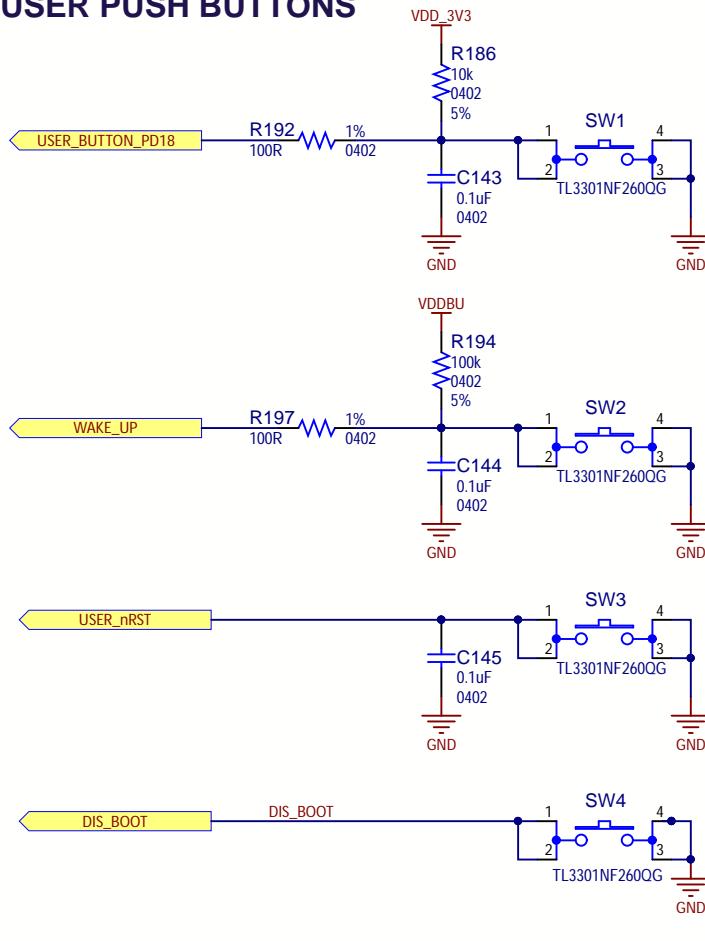


## 40-pin GPIO

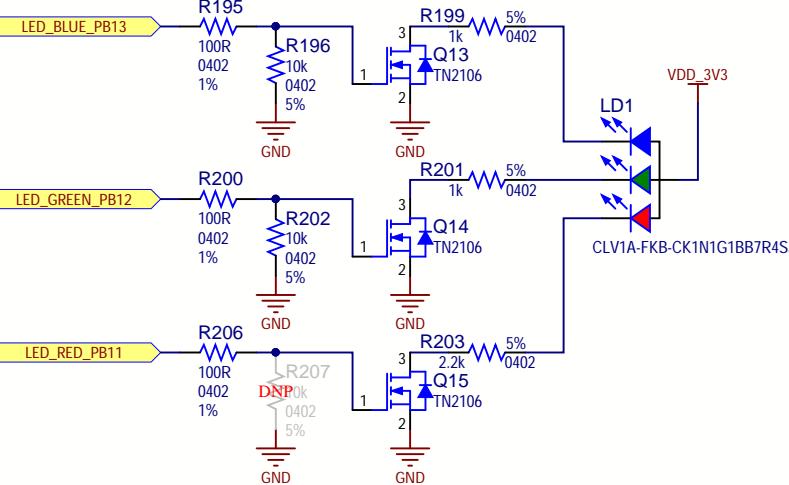


Drawn By: IGN	Engineer: IGN	 MICROCHIP
PartNumber: DT100126	Project Title <i>SAM9X60 Evaluation Kit</i>	
Sheet Title <i>Expansion connectors</i>	Variant Name <i>Standard_WILC3000_DNP</i>	Designed with 
Size B	Sch #: 03-11039 Revision:2	Date: 9/20/2019 10:27:47 AM Sheet 11 of 13
File: 11 - Expansion connectors.SchDoc		

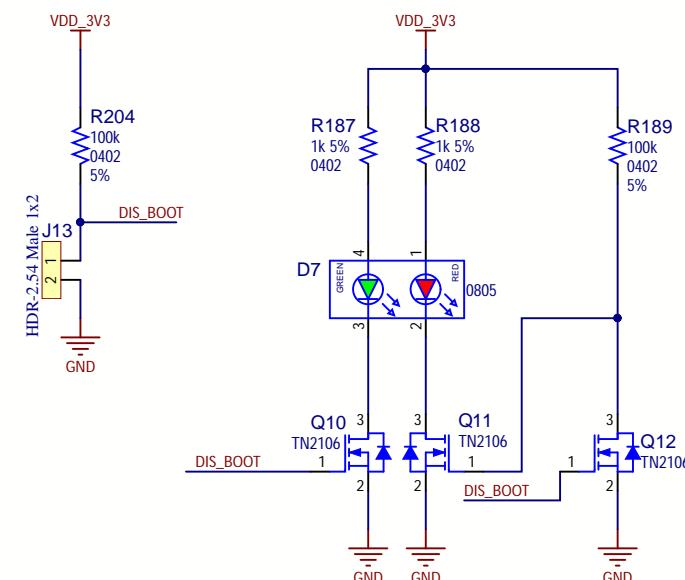
## USER PUSH BUTTONS



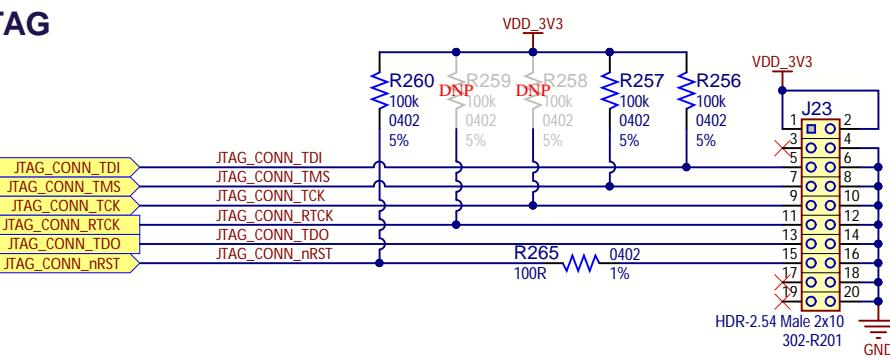
## RGB LEDs



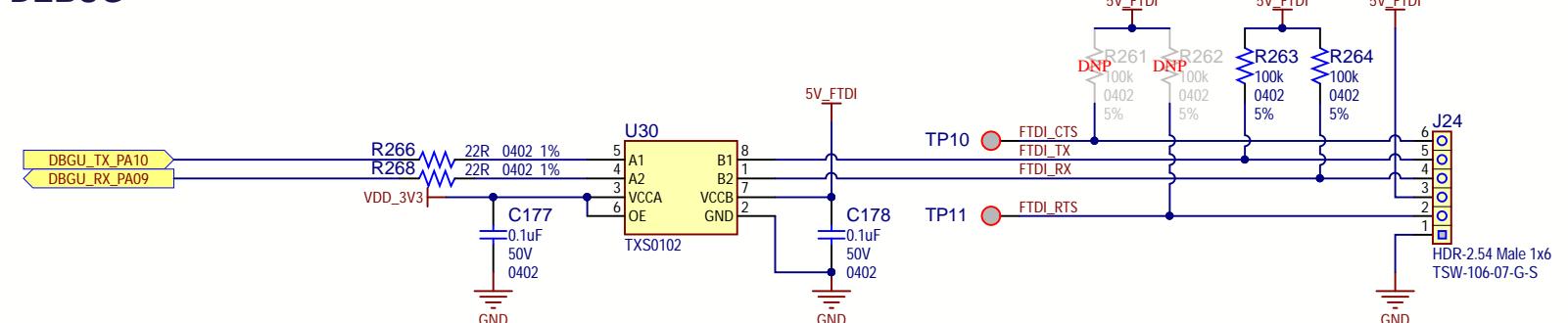
## DISABLE BOOT



## JTAG



## FTDI DEBUG

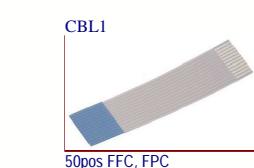


## PLACEHOLDERS

**LABEL1**  
MICROCHIP Need Help?  
http://support.microchip.com  
Assy# Serial #  
Label Need Help Large

PAD1  
RUBBER PAD D9.5 H4.8  
PAD3  
RUBBER PAD D9.5 H4.8

PAD2  
RUBBER PAD D9.5 H4.8  
PAD4  
RUBBER PAD D9.5 H4.8



Drawn By:  
IGn

Engineer:  
IGn

PartNumber:  
DT100126

Sheet Title  
User Interaction

Size  
B

Revision: 2

Project Title  
**SAM9X60 Evaluation Kit**

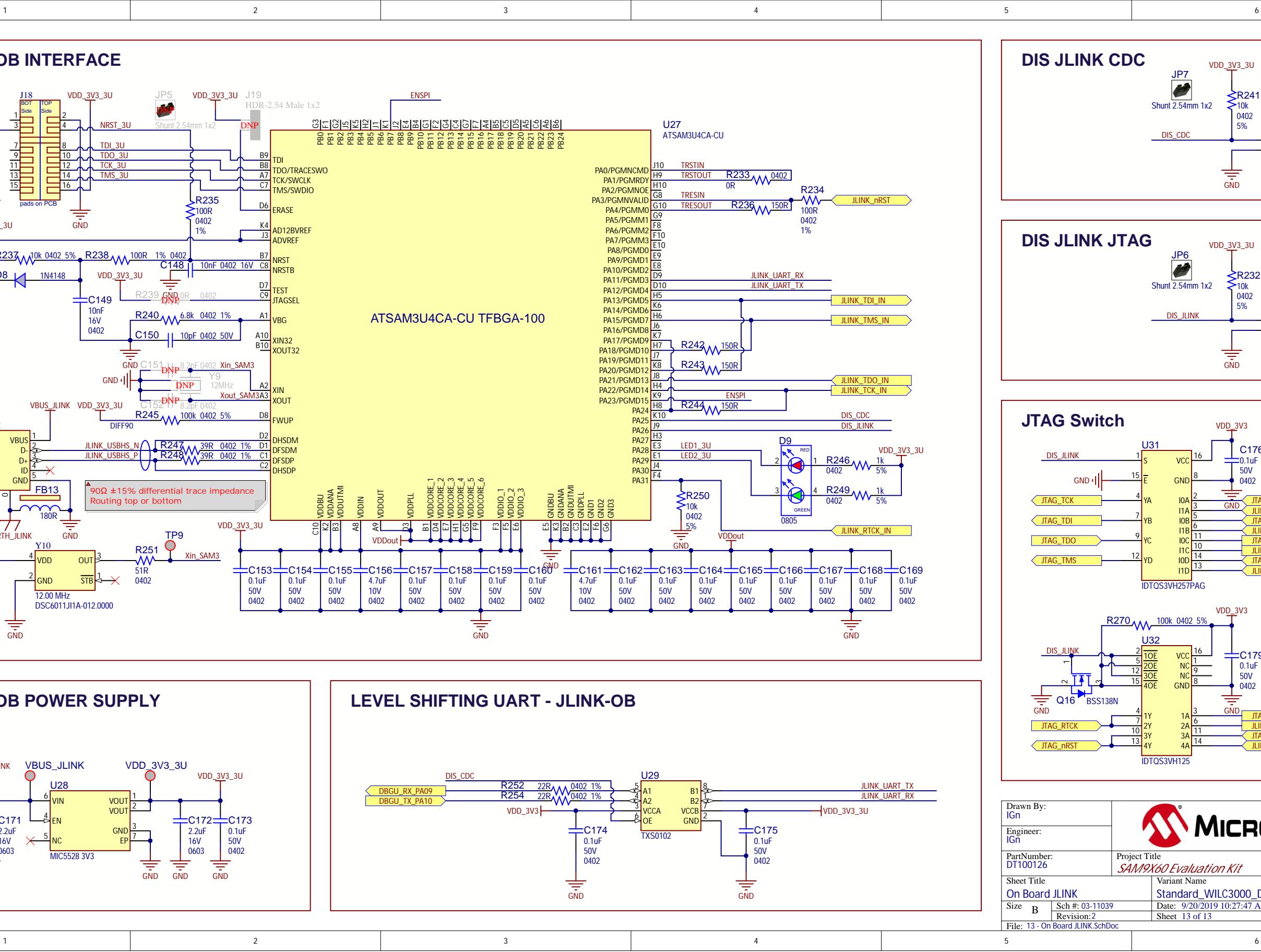
Variant Name  
**Standard\_WILC3000\_DNP**

Date: 9/20/2019 10:27:47 AM

File: 12 - User Interaction.SchDoc

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Drawn By: IGn			
Engineer: IGn			
PartNumber: DT100126	Project Title <i>SAM9X60 Evaluation Kit</i>		
Sheet Title <i>On Board JLINK</i>	Variant Name <i>Standard_WILC3000_DNP</i>		Designed with <b>Altium</b> Altium.com
Size B Sch #: 03-11039 Revision: 2	Date: 9/20/2019 10:27:47 AM		
File: 13 - On Board JLINK.SchDoc	Sheet 13 of 13		