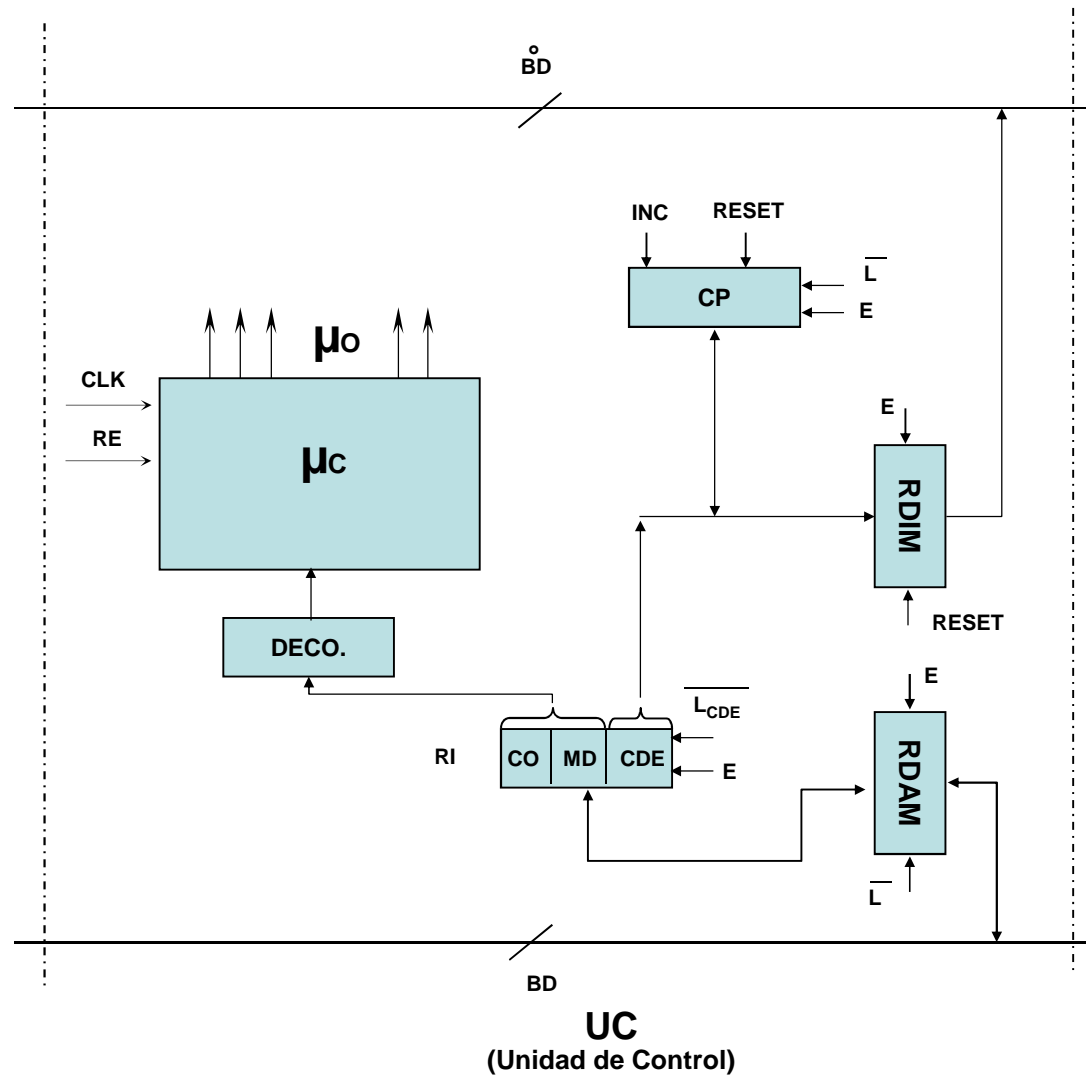
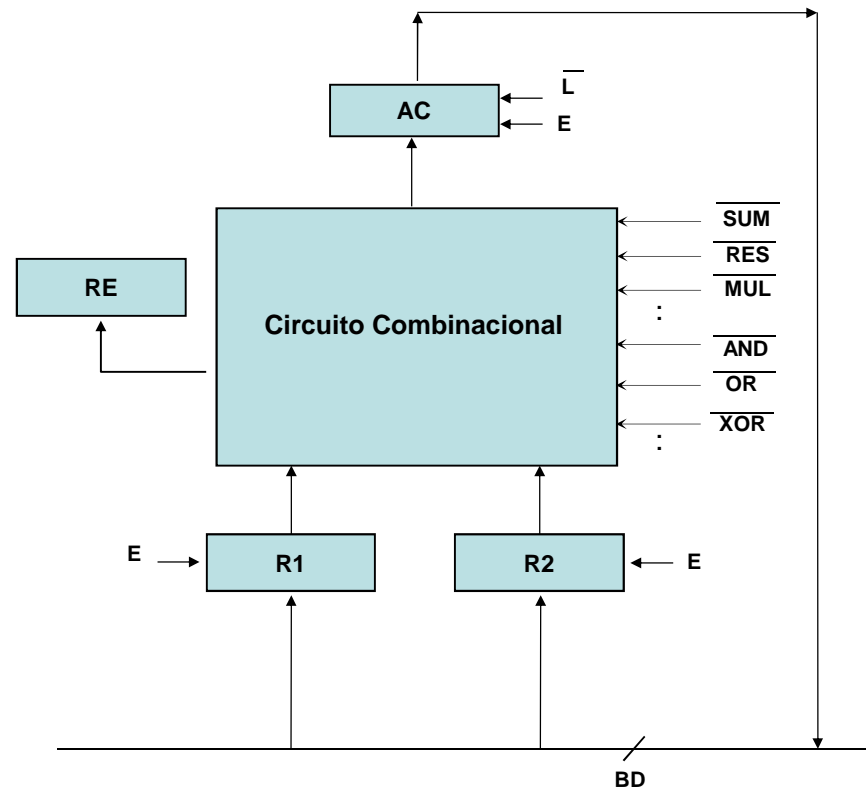


ARQUITECTURA BASICA DEL COMPUTADOR

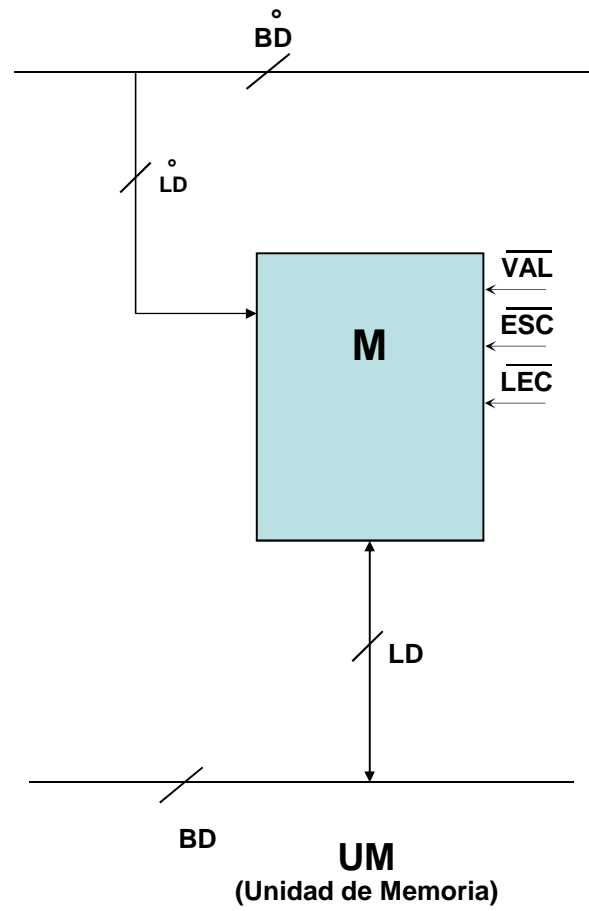


ARQUITECTURA BASICA DEL COMPUTADOR



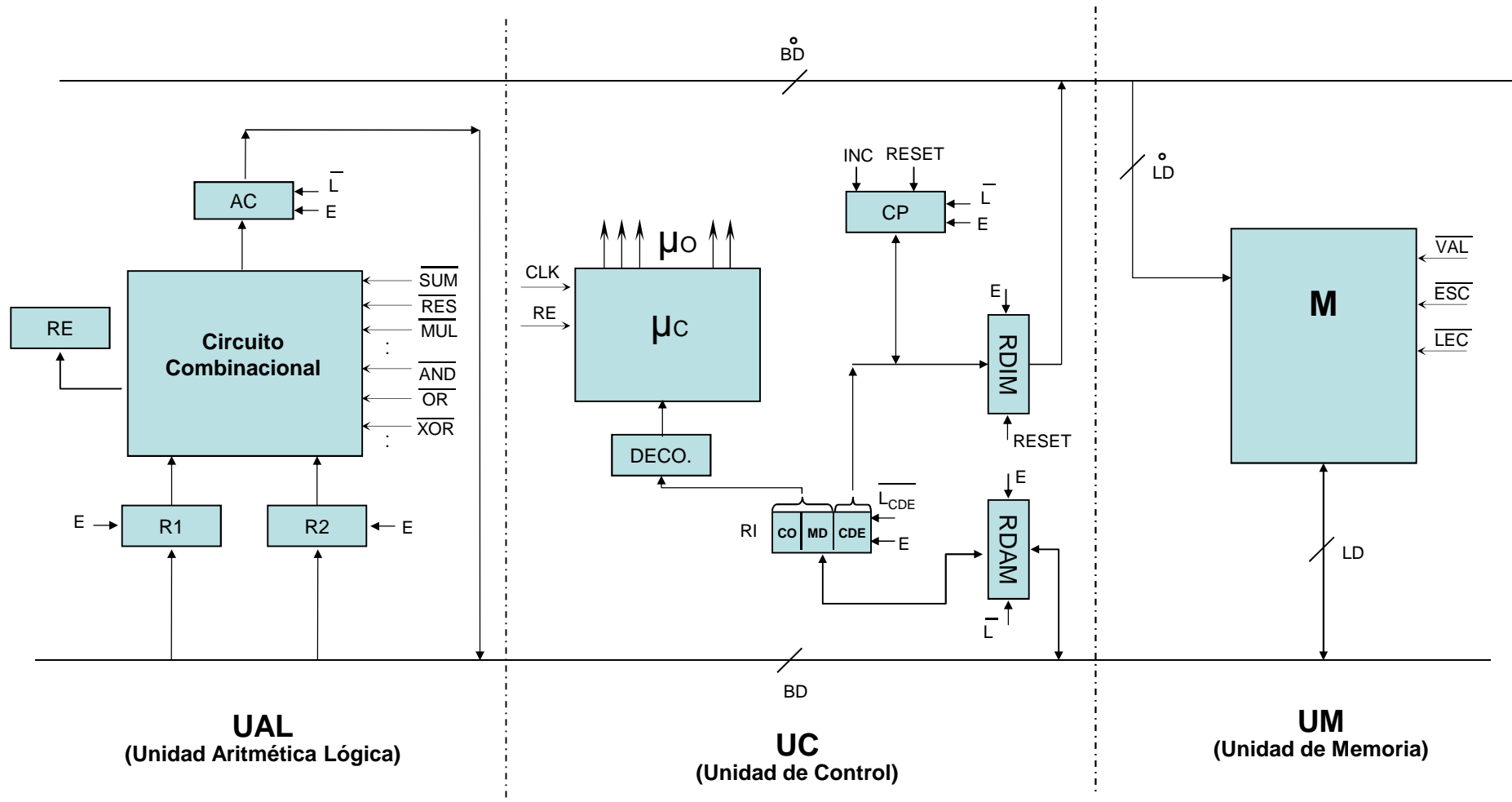
UAL
(Unidad Aritmética Lógica)

ARQUITECTURA BASICA DEL COMPUTADOR



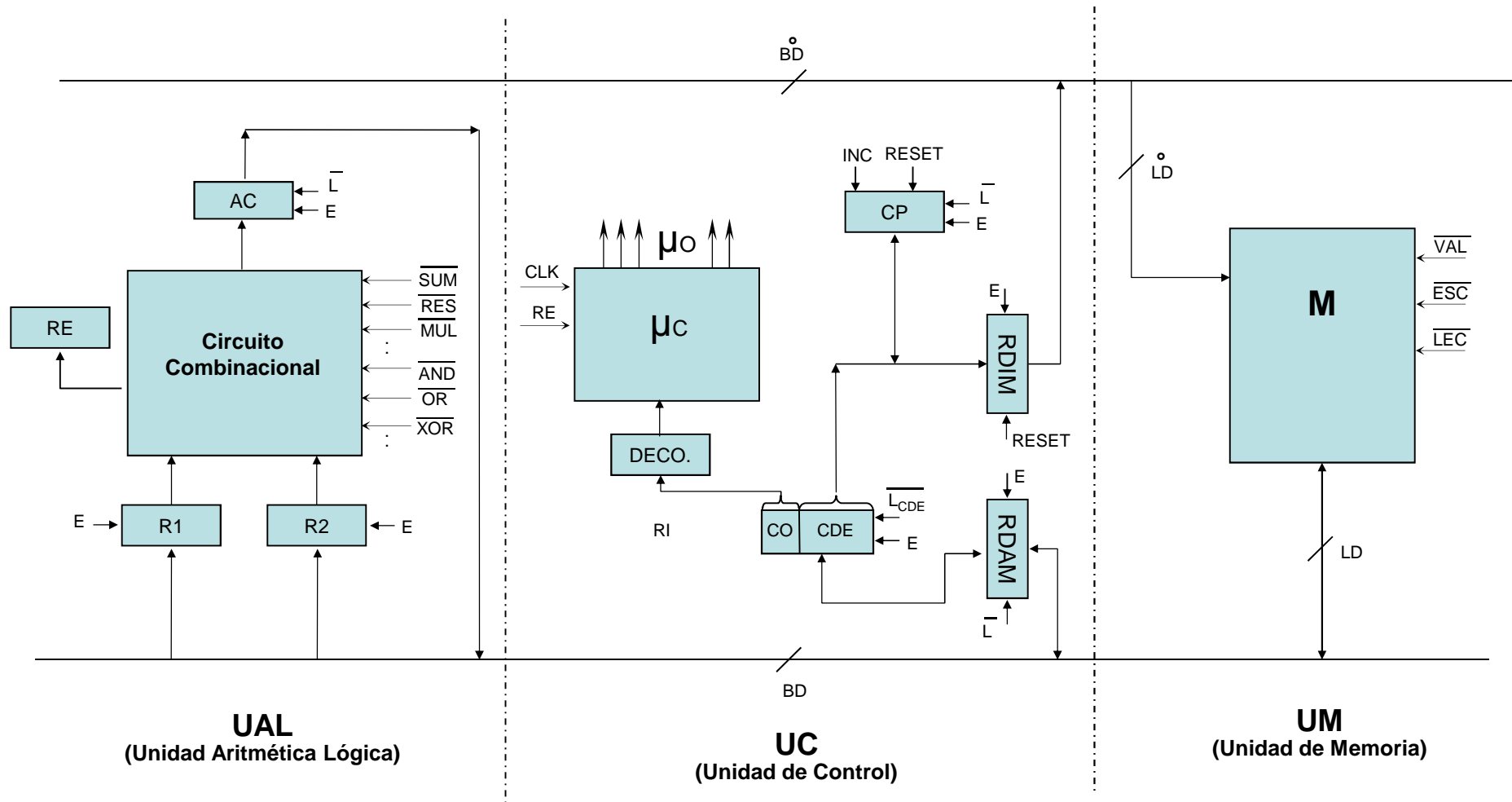
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(FI de 1 Palabra)



ARQUITECTURA BASICA DEL COMPUTADOR

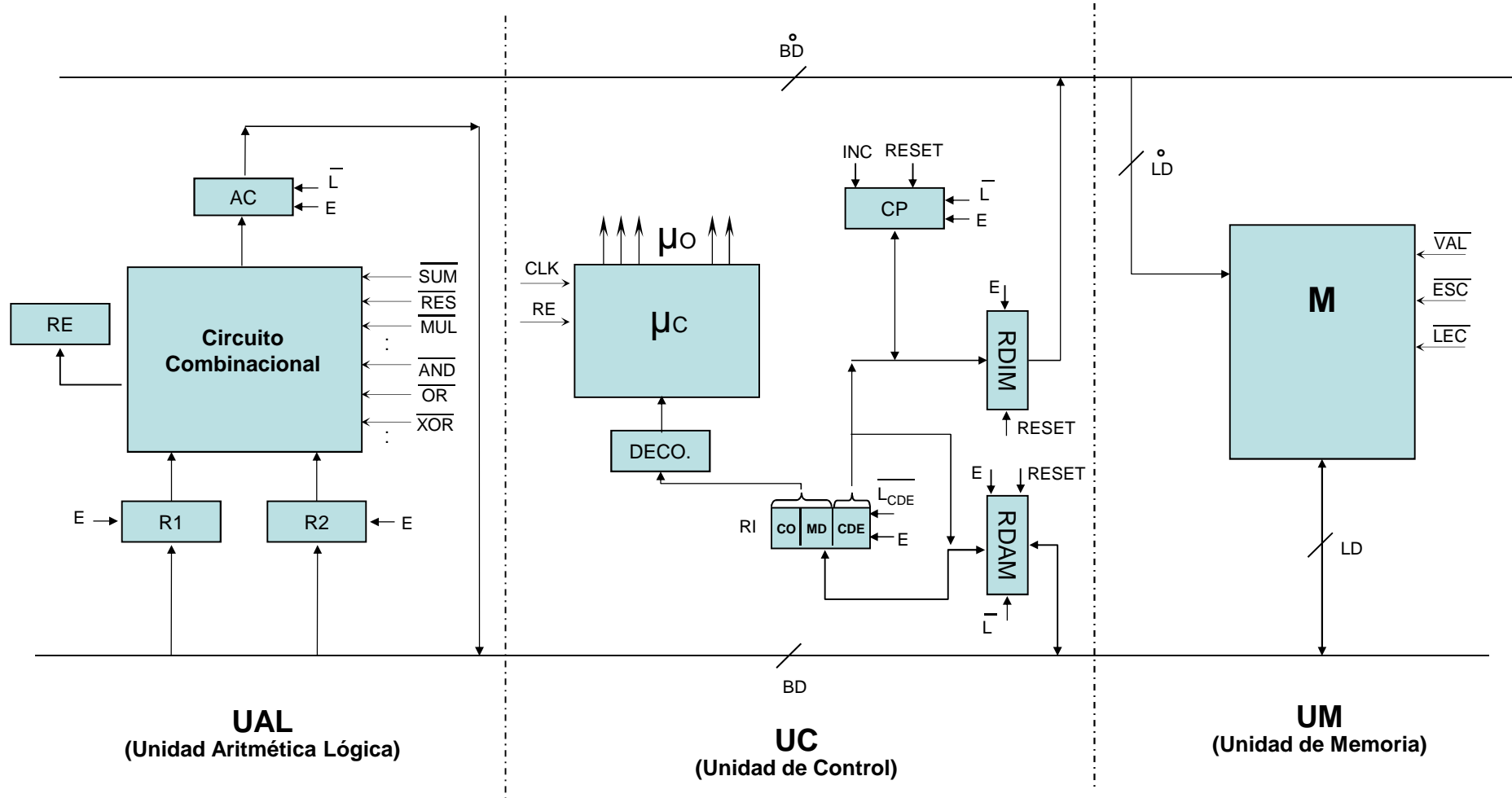
(FI de 1 Palabra, MD Unico)



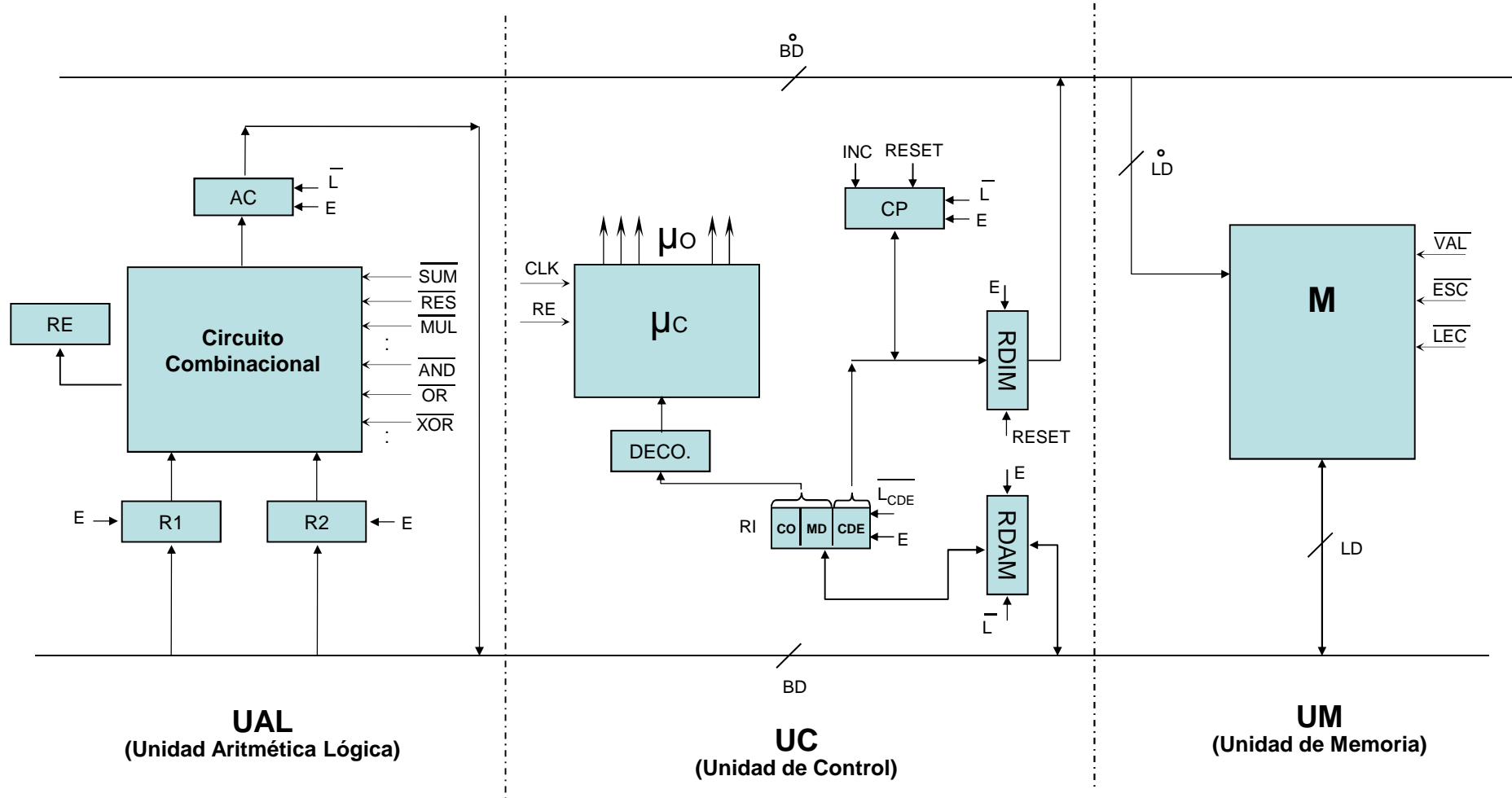
Modos de Direccionamiento

(FI de 1 Palabra)

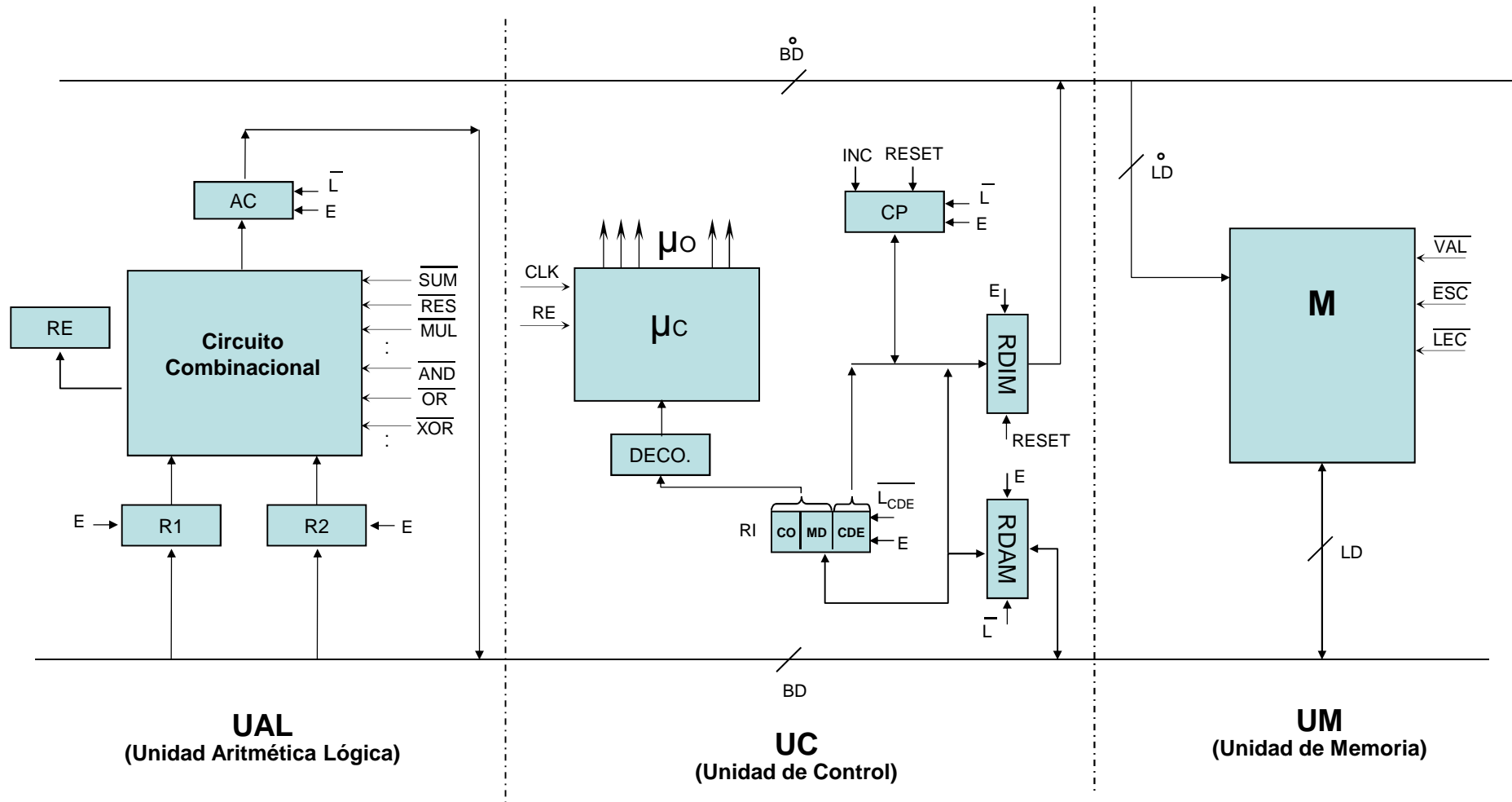
ARQUITECTURA BASICA DEL COMPUTADOR (MD Inmediato)



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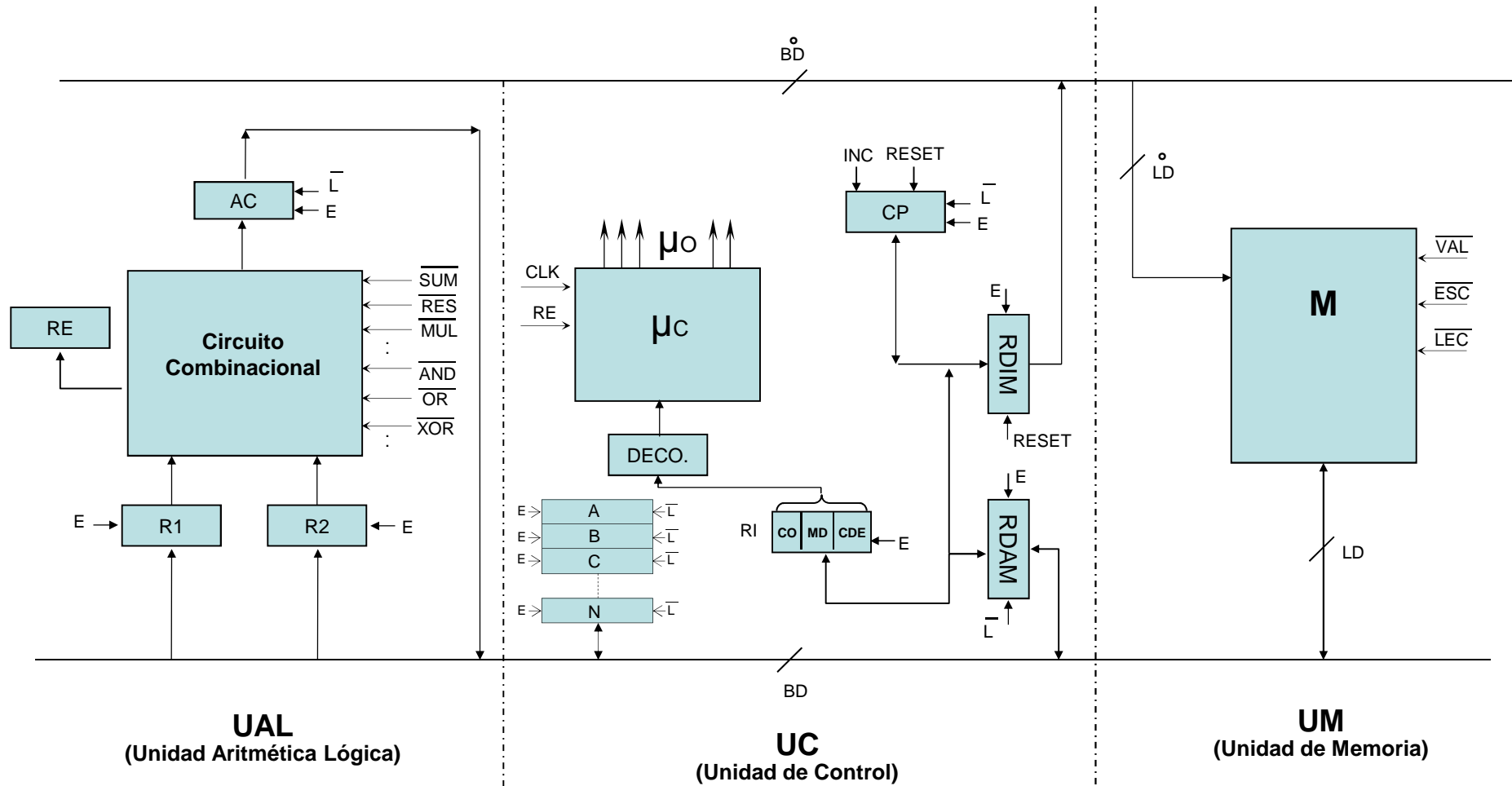


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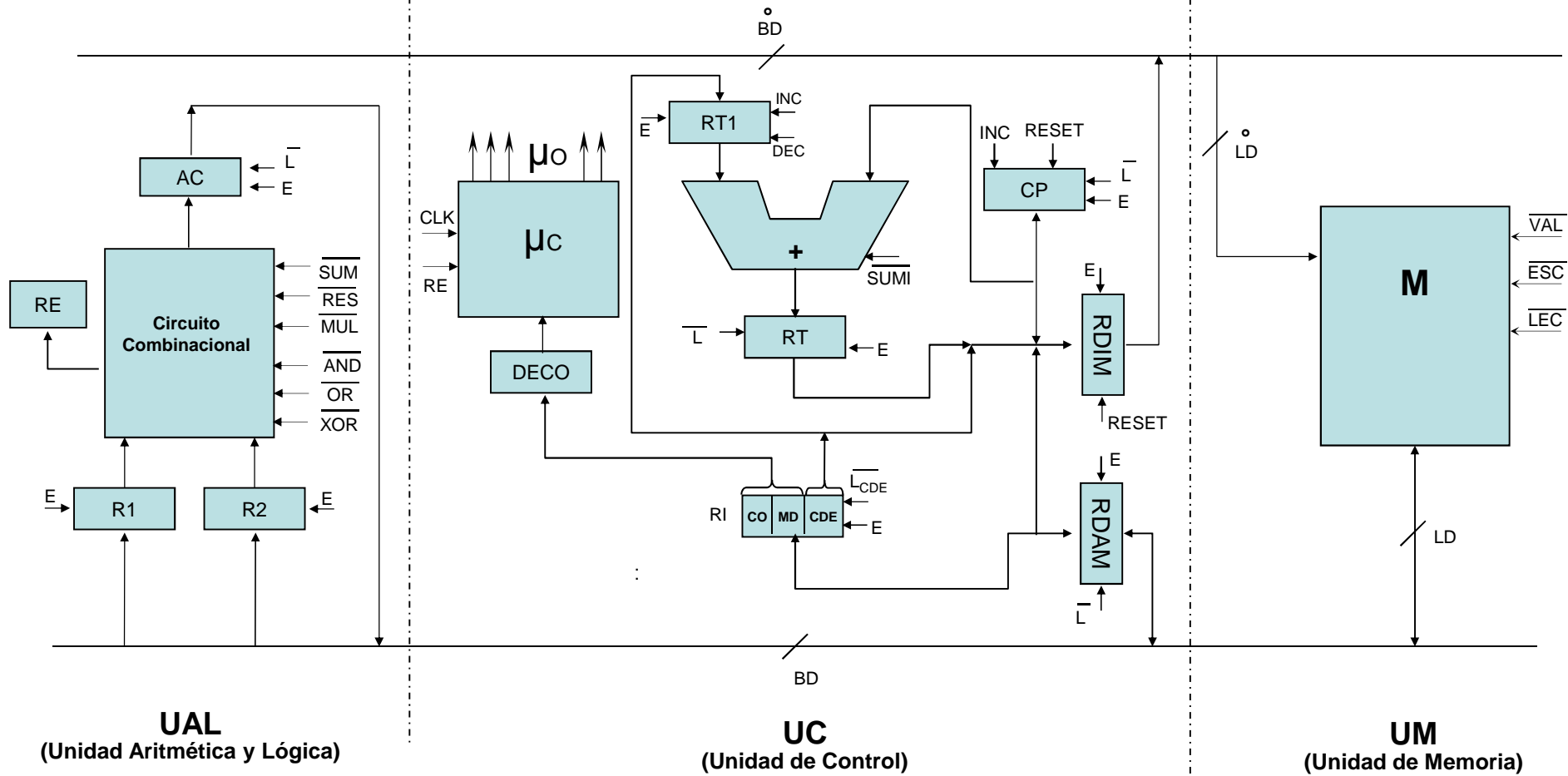


ARQUITECTURA BASICA DEL COMPUTADOR

(MD Mediante Registro “Inmediato-Directo-Indirecto”)



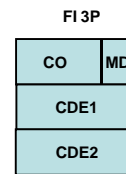
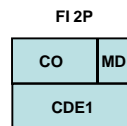
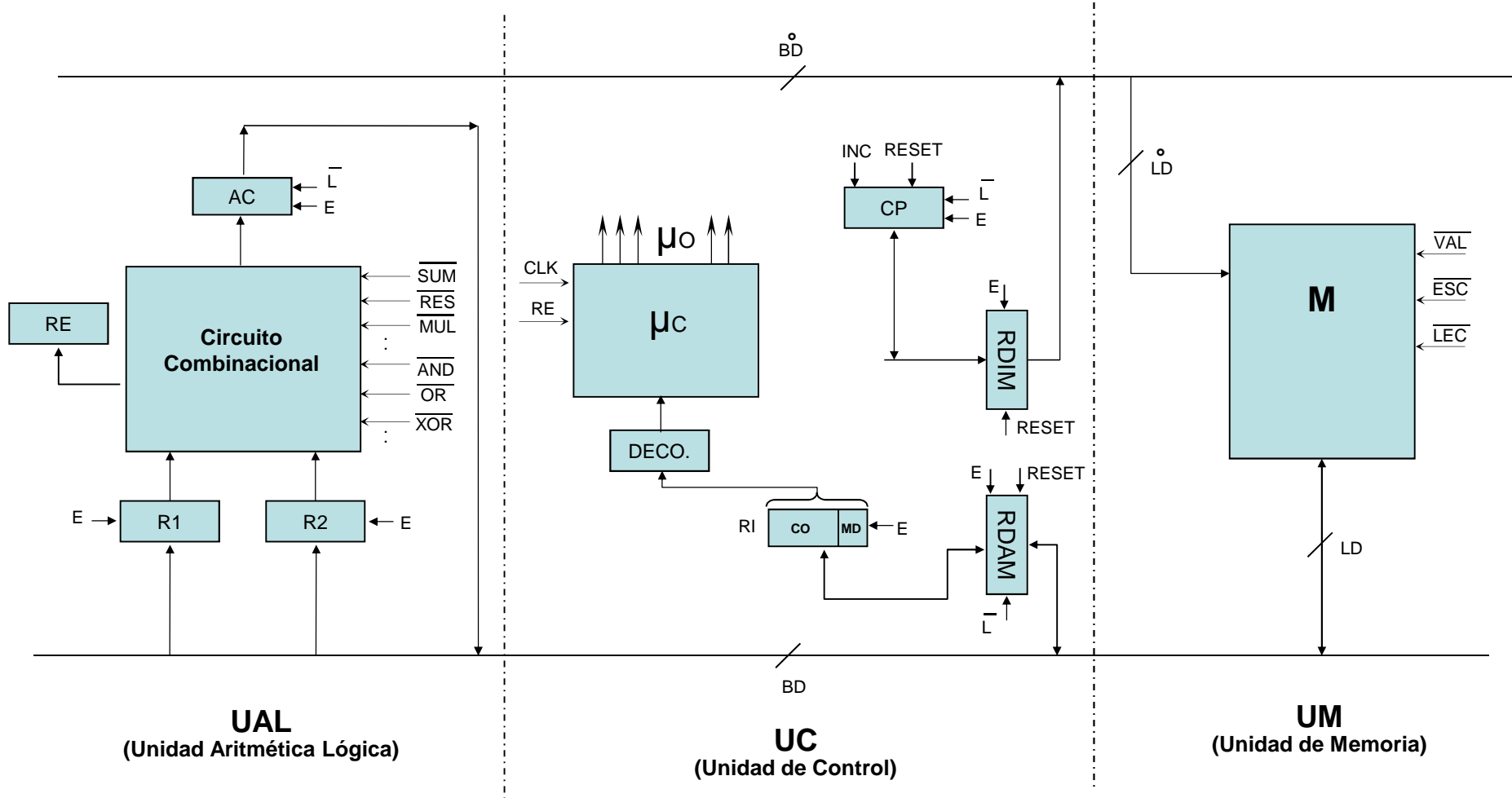
ARQUITECTURA BASICA DEL COMPUTADOR (MD Indexado "Pre-Indexado, Post-Indexado")



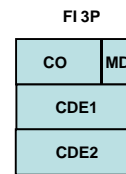
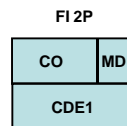
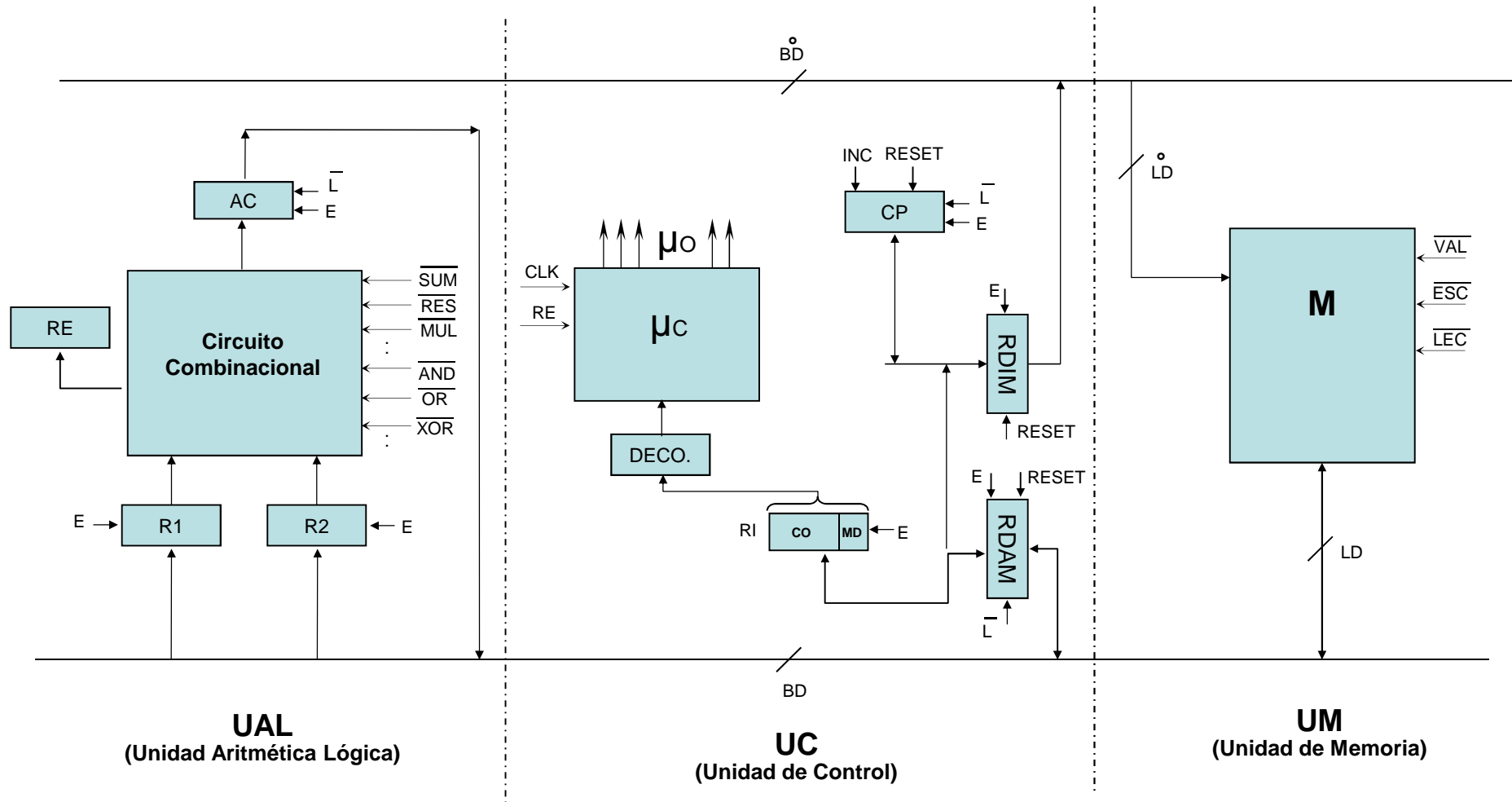
Modos de Direccionamiento

(FI de Variable)

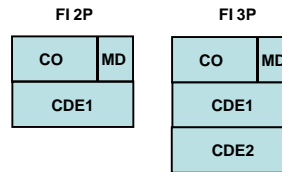
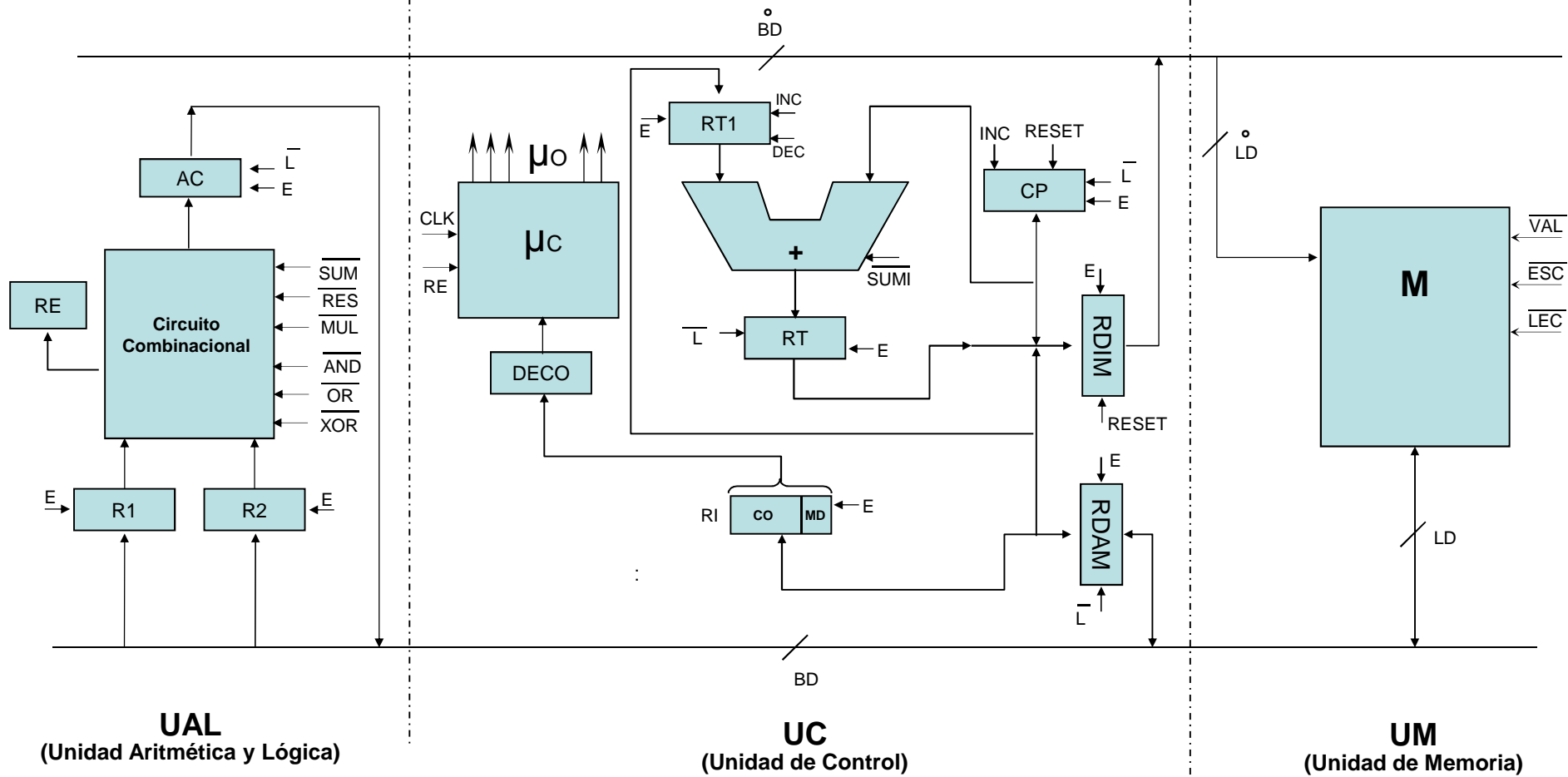
ARQUITECTURA BASICA DEL COMPUTADOR (MD Inmediato)



ARQUITECTURA BASICA DEL COMPUTADOR (MD Directo e Indirecto)



ARQUITECTURA BASICA DEL COMPUTADOR (MD Indexado "Pre-Indexado, Post-Indexado")



ARQUITECTURA BASICA DEL COMPUTADOR

(MD Mediante Registro “Inmediato-Directo-Indirecto”)

