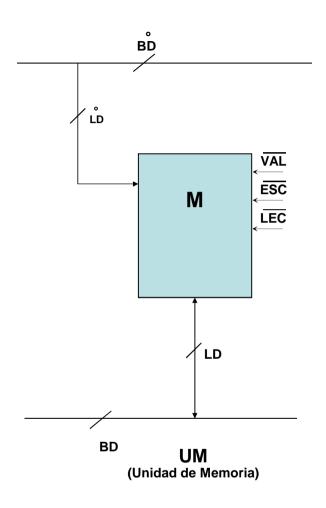
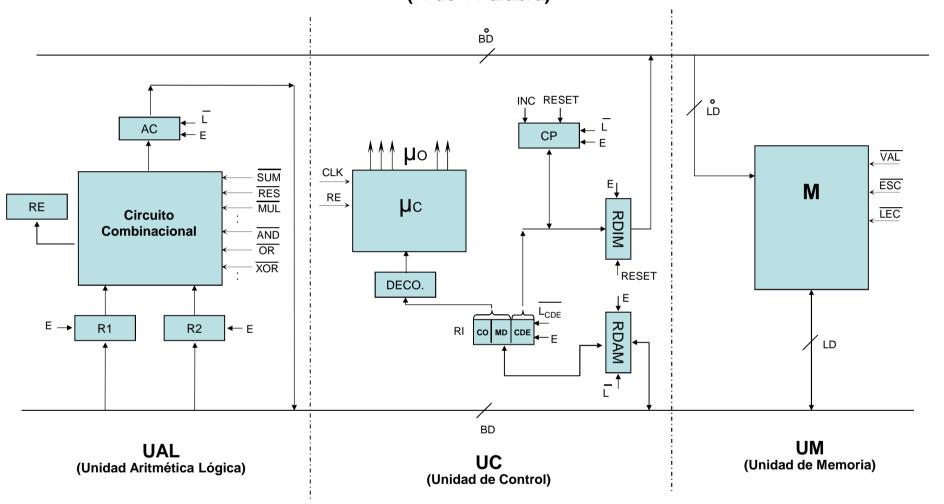


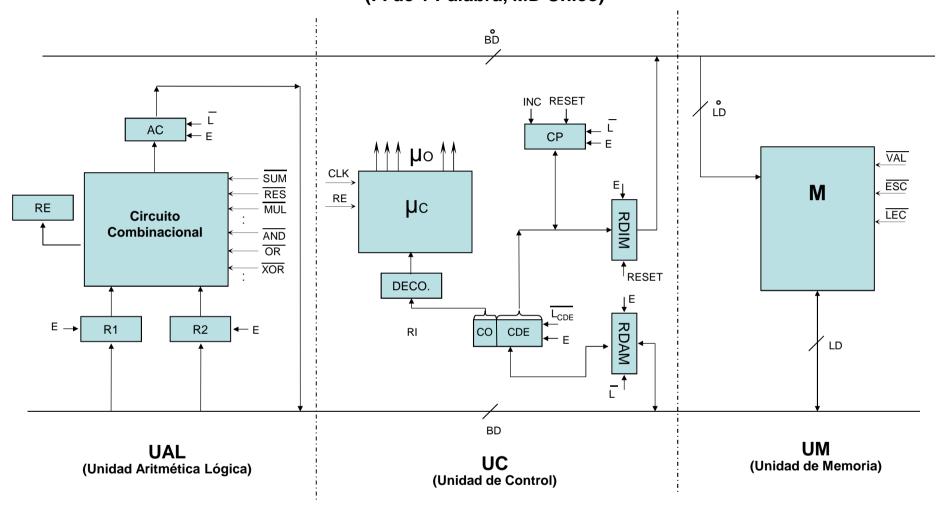
UAL (Unidad Aritmética Lógica)



ARQUITECTURA BASICA DEL COMPUTADOR (FI de 1 Palabra)



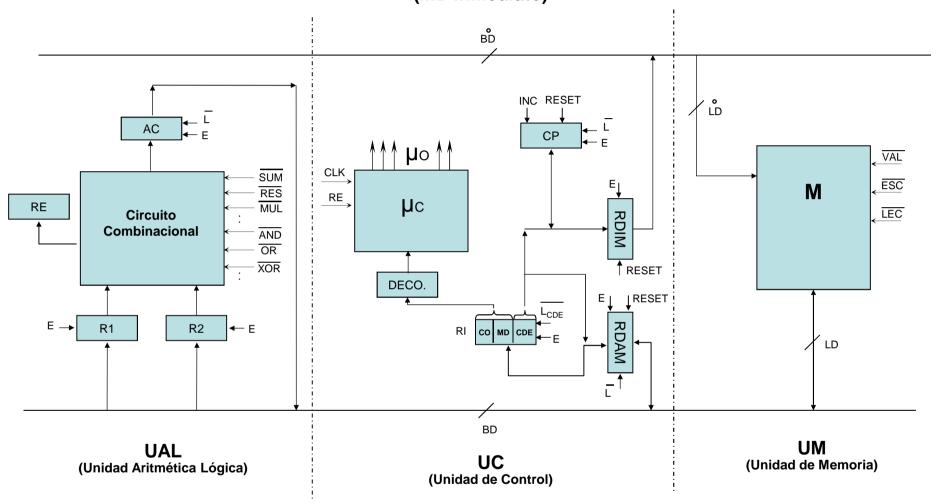
ARQUITECTURA BASICA DEL COMPUTADOR (FI de 1 Palabra, MD Unico)



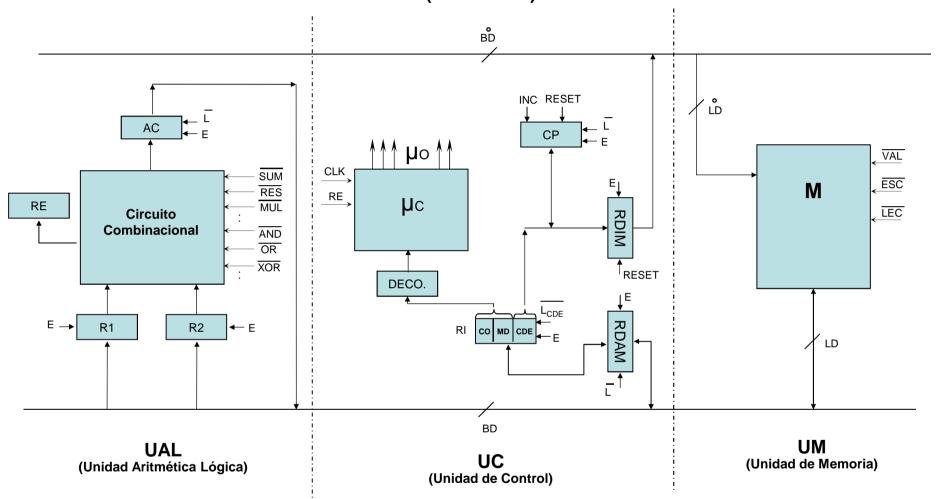
Modos de Direccionamiento

(FI de 1 Palabra)

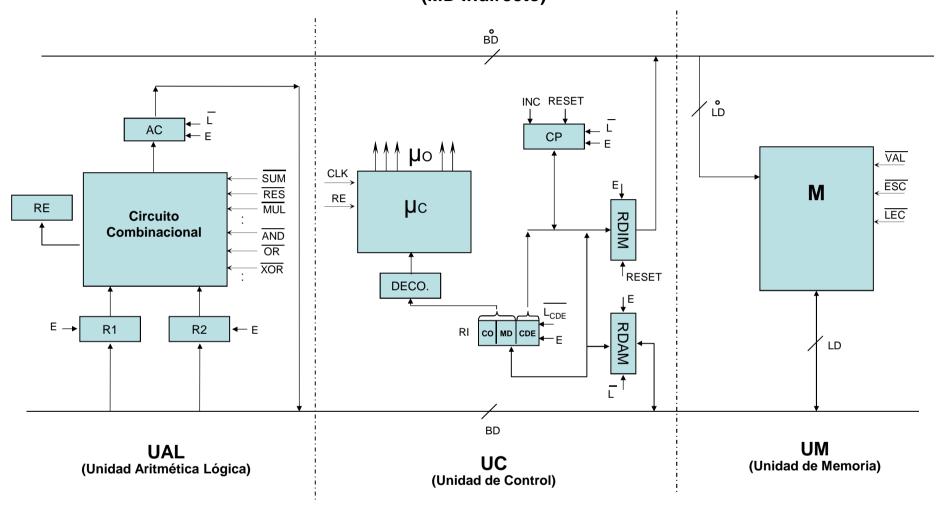
ARQUITECTURA BASICA DEL COMPUTADOR (MD Inmediato)



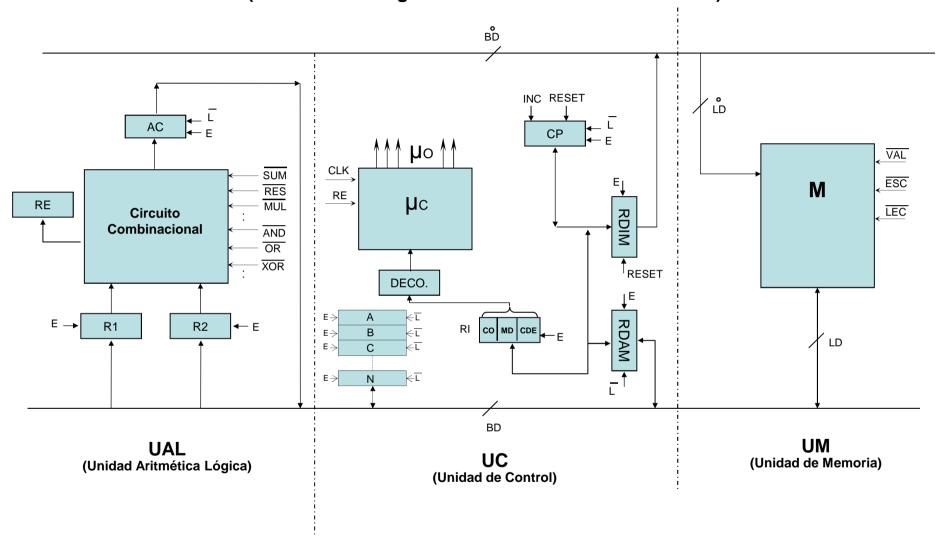
ARQUITECTURA BASICA DEL COMPUTADOR (MD Directo)

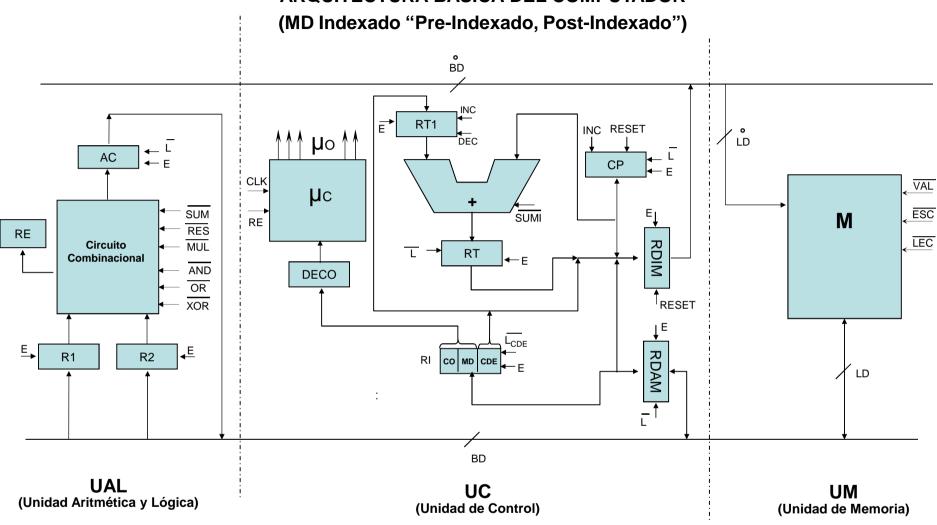


ARQUITECTURA BASICA DEL COMPUTADOR (MD Indirecto)



(MD Mediante Registro "Inmediato-Directo-Indirecto")

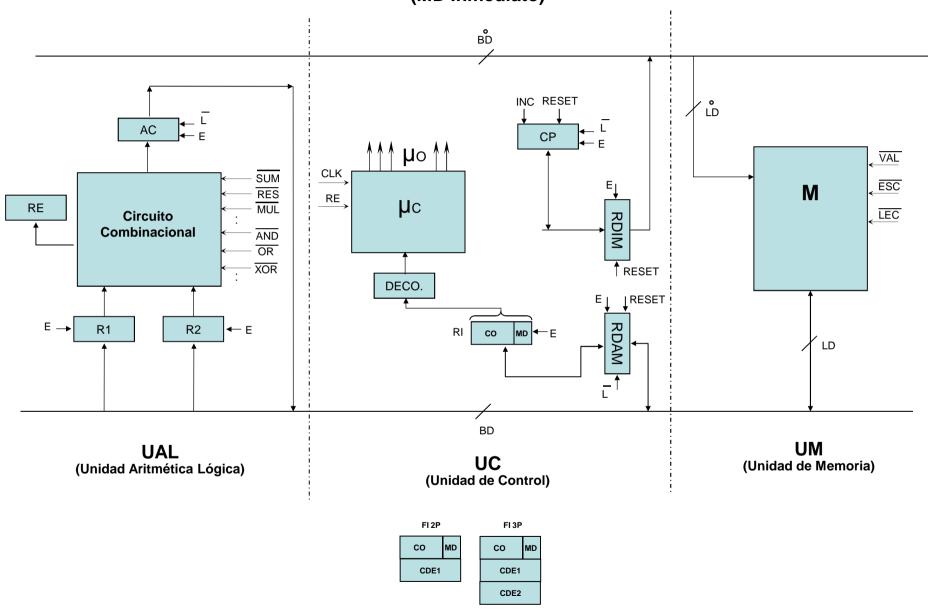




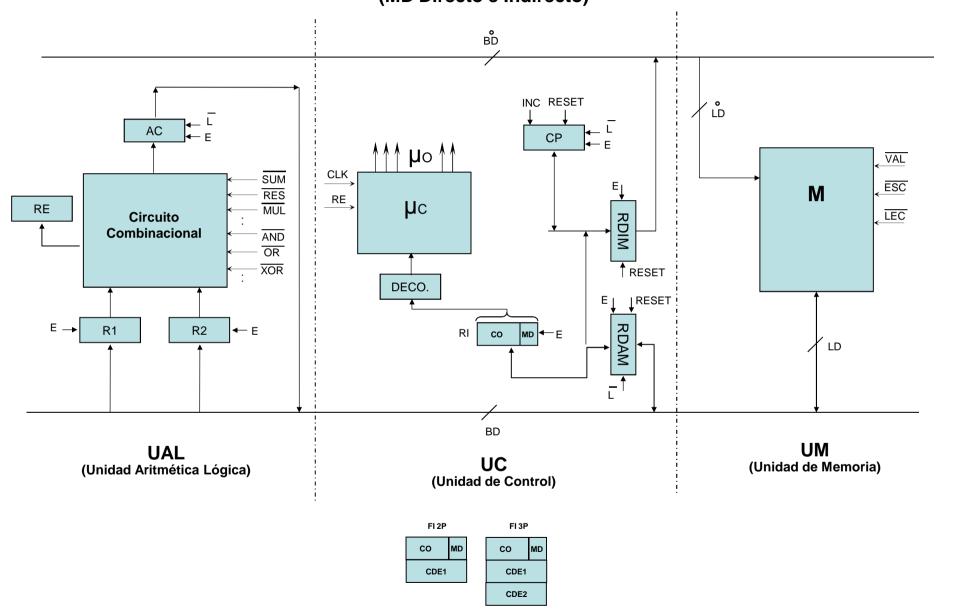
Modos de Direccionamiento

(FI de Variable)

ARQUITECTURA BASICA DEL COMPUTADOR (MD Inmediato)



ARQUITECTURA BASICA DEL COMPUTADOR (MD Directo e Indirecto)



ARQUITECTURA BASICA DEL COMPUTADOR (MD Indexado "Pre-Indexado, Post-Indexado") **o** BD RT1 INC RESET , **o** LD **µ**o ↑↑ DEC AC i CLK) VAL μc ESC SUM SUMI M RE RES RE RDIM LEC MUL Circuito RT Combinacional AND DECO OR XOR RESET RDAM R1 R2 СО LD BD **UAL** UC UM (Unidad Aritmética y Lógica) (Unidad de Control) (Unidad de Memoria) FI 2P FI 3P СО СО CDE1 CDE1

CDE2

(MD Mediante Registro "Inmediato-Directo-Indirecto")

