

HelioMote - a pioneer demo

ElectroMagnetic Interference (EMI) consideration

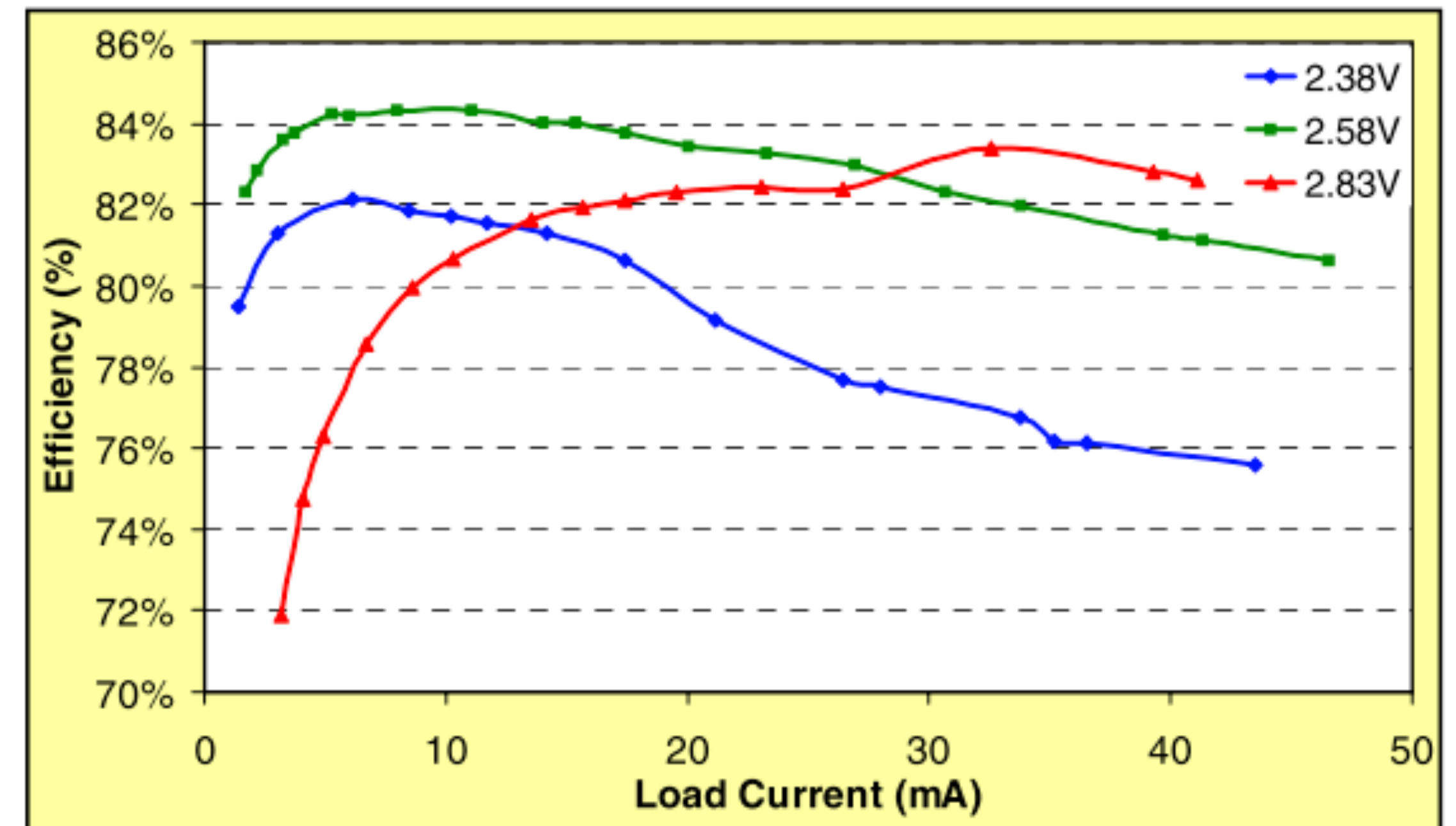
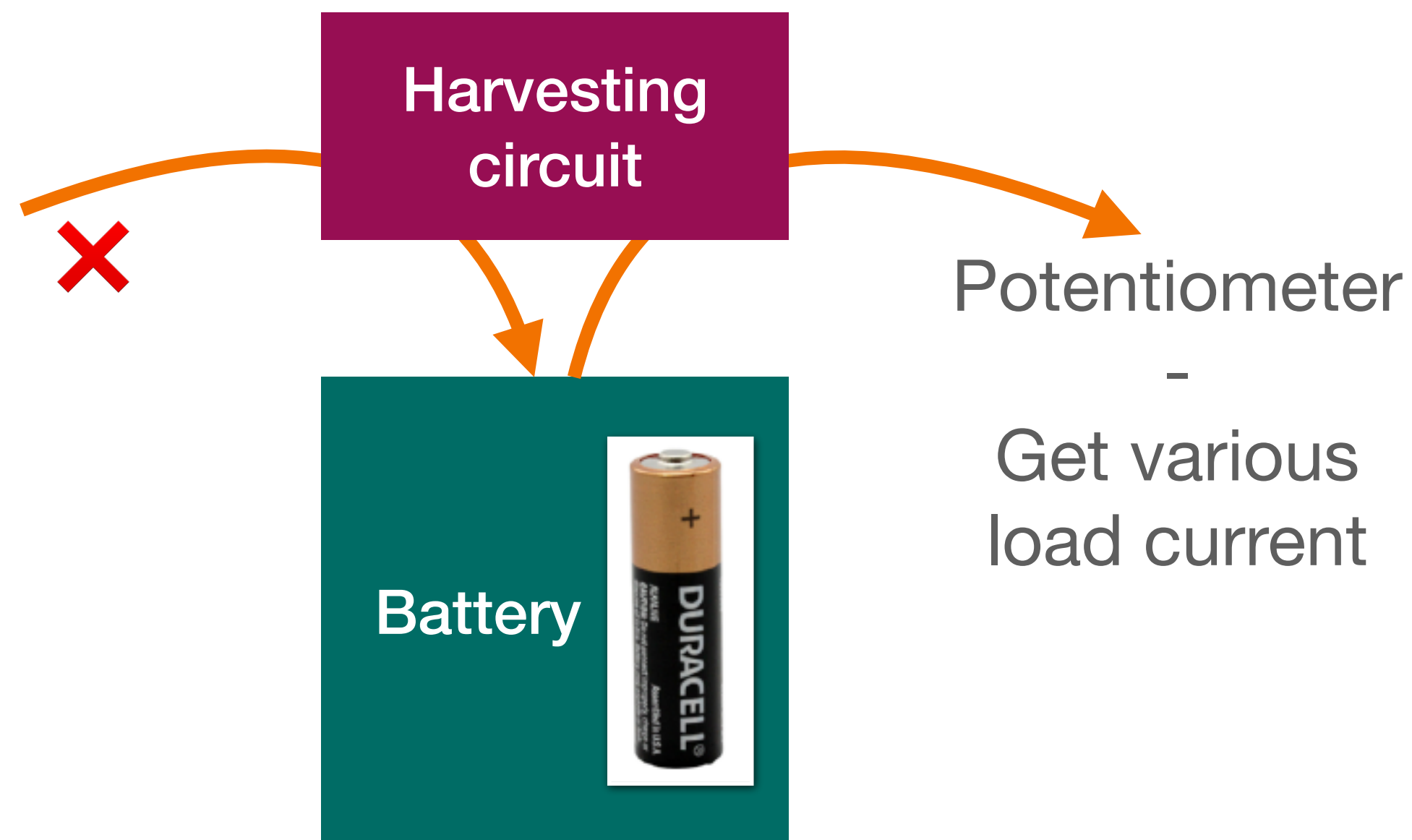
- Some good design practice
 - Place the GND plane low in the board stack to protect other circuits*
 - Using higher freq PWM for the DC-DC converter to enable smaller filter
 - Moving EMI sourcing components away from the RF circuits

* not clearly illustrated in the paper

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Performance evaluation

- Harvesting circuit efficiency



DC-DC converter by itself is over 90%
Yet other parts consume extra power