A single case
Power comparison

	Traditional cache	Temporal/spatial split cache	Comparison
mem_power	0.832W	0.67W	>
cache_power	access_num x energy_per_access	access_num x energy_per_access	> (ignored in the paper)
extra_overhead	0	cache_ctrl_overhead(0.0025W)	< but pretty subtle
Sum	0.832W	0.6725W	>

Conclusion: 0.16W power saving with same performance(hit rate)

The PaLM Algorithm
Lets generalize it - an algorithm flow

C program

Amount of available memory

PaLM Algorithm

Detailed Setup of cache

Which variable stores in which cache