

Access Pattern based Local Memory Customization for Low Power Embedded Systems

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Big background

The theme

- Memory wall - mismatch between cpu development and memory BW
- Memory bandwidth is the bottleneck of improving processor performance

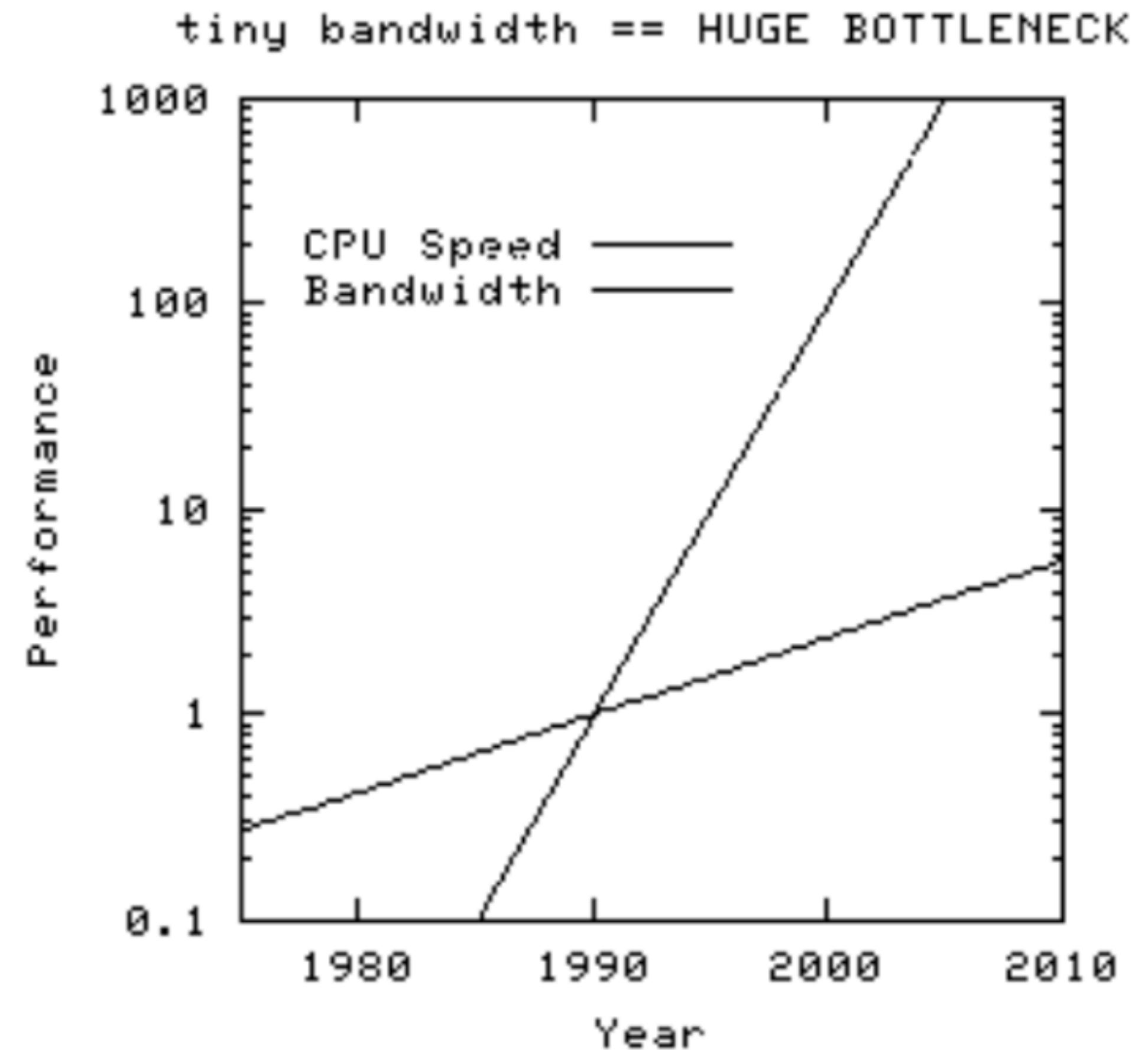


Figure 5: McCalpin's CPU Speed vs. Bandwidth

McKee, Sally A. "Reflections on the memory wall." *Proceedings of the 1st conference on Computing frontiers*. 2004.