SER502-Spring2018-Team2

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Outline

- Language Design
- Grammar
- Intermediate Code
- Compiler Architecture
- Frontend
- Intermediate Code Generator
- Runtime
- Sample

Language Design

- Paradigm: Imperative
- Name: Godfather
- Features
 - o Primitives: integer, boolean value
 - o Flow control: if-else, while
 - Assignment operator: "="
 - Arithmetic operators: "+", "-", "*", "/", "%"
 - Relational operators: "==", "≠", "≥", > ≤ <
 - Supports parentheses to override arithmetic precedence: "(" arithmetic expression ")"
 - A special statement "print" used to output an arithmetic expression to console

Constrains

- Do not support function
- Declarations can only be placed at the top of a program

Grammar

```
program → decls stmts
decls \rightarrow decl decls rest | \epsilon
decls rest \rightarrow decl decls rest | \epsilon
decl → type id ':'
stmts \rightarrow stmt | stmts rest | \epsilon
stmts rest \rightarrow stmt stmts rest | \epsilon
stmt → id '=' arith expr ';'
       | id '=' bool expr ';'
       | 'if' '(' bool expr ')' '{' stmts '}'
       | 'if' '(' bool expr ')' '{' stmts '}' 'else' '{' stmts '}'
        'while' '(' bool_expr ')' '{' stmts '}'
        'print' '(' arith_expr ')'
bool_expr → arith_expr '==' arith_expr
              arith_expr '!=' arith_expr
              arith expr'>' arith expr
              arith_expr '<' arith_expr
              arith_expr '>=' arith_expr
              arith expr '<=' arith expr
              bool value
```

```
arith_expr \rightarrow term airth_expr_rest airth_expr_rest \rightarrow '+' term airth_expr_rest | '-' term airth_expr_rest | \epsilon term \rightarrow factor term_rest term_rest \rightarrow '*' factor term_rest | '/' factor term_rest | '%' factor term_rest | \epsilon factor \epsilon number | '(' airth_expr ')' | id type \epsilon 'int' | 'bool' id \epsilon [a-z|A-Z]+ num \epsilon [0-9]+ bool_value \epsilon 'true' | 'false'
```

Intermedia Code — Three-Address Code

An three-address code has one operator and most three operands. An expression has more than one operator will be translated to multiple instructions.

E.g:
$$y = x + 1 + 2$$
 ----> $t1 = x + 1$; $y = t1 + 2$;

In Three-Address Instructions. An address can be a:

- Name. usually is an identifier, as a pointer to its symbol table entry.
- o Constant. can be an integer or boolean value.
- Compiler-generated temporary. is a temporary identifier used to save partial result and will be reused or combined later.

Intermedia Code —— Instruction Design

Our intermediate code is in MIPS Style. The operator is on the left side and operands are on the right side.

Assign

$$\blacksquare$$
 Rd = Rs ----> move Rd Rs

Arithmetic

$$\blacksquare$$
 Rd = Rs + Rt ----> add Rd Rs Rt

$$\blacksquare$$
 Rd = Rs / Rt ----> div Rd Rs Rt

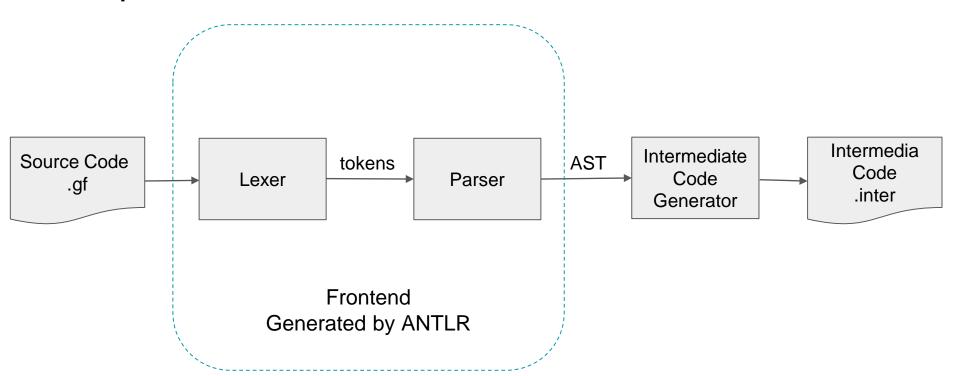
$$\blacksquare$$
 Rd = Rs % Rt ----> rem Rd Rs Rt

Intermedia Code —— Instruction Design

```
Branch
         Branch to Label iffalse(Rs == Rt)
                                                            bneg Rs Rt Label
                                                 ---->
         Branch to Label iffalse(Rs != Rt)
                                                            beg Rs Rt Label
                                                 --->
         Branch to Label iffalse(Rs < Rt)
                                                            bnlt Rs Rt Label
                                                 ---->
         Branch to Label iffalse(Rs > Rt)
                                                            bngt Rs Rt Label
                                                 --->
         Branch to Label iffalse(Rs <= Rt)
                                                            bnle Rs Rt Label
                                                 --->
         Branch to Label iffalse(Rs >= Rt)
                                                            bnge Rs Rt Label
                                                 ---->
   Jump
0
         Jump to Label unconditionally
                                                            j Label
                                                 ---->
   Extension
         Print Rs to Console
                                                            print Rs
```

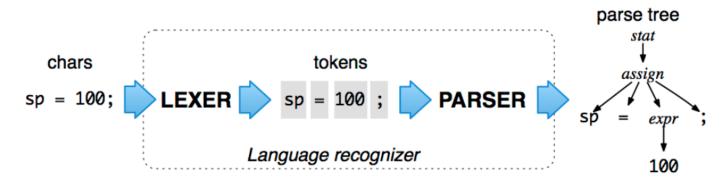
An instruction can be marked with an label, E.g. LB1: add rd rs rt.

Compiler Architecture



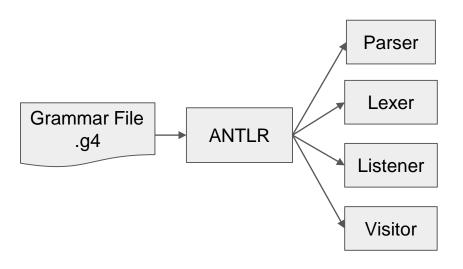
Frontend (Lexer and Parser)

- Lexer: sperate a stream of characters into different words, which is the token list.
- Parser: generate a parse tree with the token list from the Lexer.
- Antir makes use of the grammar file we defined to generate a lexer for lexical analysis and a parser for parse tree.



Frontend (Lexer and Parser)

- ANTLR accepts a grammar file to generate the parse and lexer.
- ANTLR also genereates a Listener and Visitor which can be used to traverse the parse tree.



- Listener: is called by the ANTLR-provided walker object, it cannot be controlled by us.
- Visitor: walk their children with explicit visit calls and we can control its path. Developers can define a return type for visitor to implement more complex manipulations.

Frontend —— Grammar File for ANTLR

```
grammar GodFather;
program: decls stmts;
decls: (decl)*;
decl: type=('int' | 'bool') ID ';';
stmts: (stmt)*;
stmt : ID '=' arith_expr ';' # stmtArithAssign
     | ID '=' bool expr ';' # stmtBoolAssign
     | 'if' '(' bool expr ')' '{' stmts '}' # stmtlf
     | 'if' '(' bool_expr ')' '{' stmts '}' 'else' '{' stmts '}' # stmtlfElse
     | 'while' '(' bool_expr ')' '{' stmts '}' # stmtWhile
     | 'print' '(' arith expr ')' ';' # stmtPrint
bool expr : arith expr op=('==' | '!=' | '>' | '<' | '>=' | arith expr #boolExprCmp
           | value=('true' | 'false') #boolExprValue
arith expr: term (op=('+' | '-') term)*;
term : factor (op=('*' | '/' | '%') factor)*;
factor: NUMBER | '(' arith_expr ')' | ID;
```

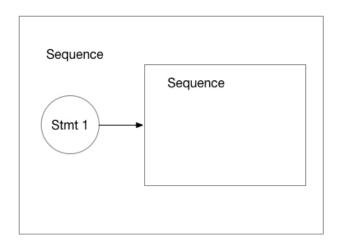
Frontend —— Grammar File for ANTLR

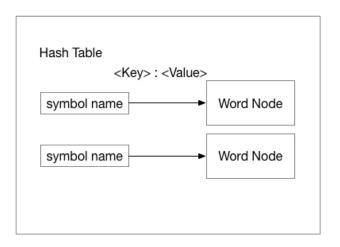
```
LE : '<=';
SEMI: ';';
OR : '||';
GT: '>';
ASSIGN: '=';
GE: '>=';
EQ: '==';
PLUS: '+';
MINUS: '-';
NE: '!=';
MUL: '*';
LT : '<';
DIV: '/';
INT: 'int';
BOOL: 'bool';
NUMBER: [0-9]+;
TRUE: 'true';
FALSE: 'false';
ID: [a-z|A-Z]+;
WS: [ t\rangle - skip ;
```

Intermedia Code Generator — Data Structure

LinkedList: used to express the execution order.

Hash Table: used to save global declared id as a symbol table.



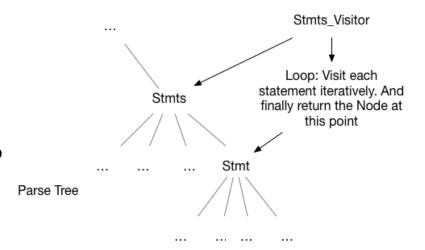


Intermedia Code Generator —— Translation Process

Intermediate code translation is based on the ANTLR Visitor interfaces. There are two steps for translation.

Step 1: Traverse

Use Visitor to access each node on the parse tree and generate corresponding Node at this point and return it to higher-level.



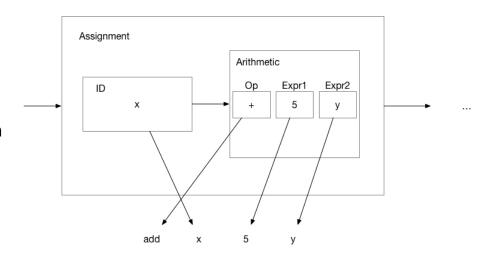
Intermedia Code Generator —— Translation Process

• Step 2: Print

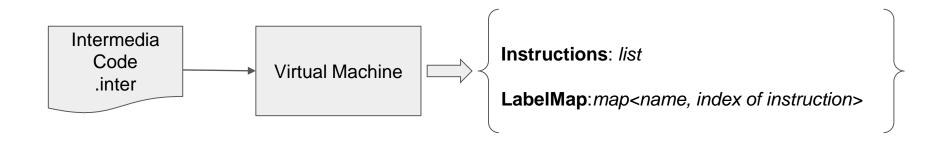
After Step 1, we have will have a generated linked list to store all instructions. Then, we will generate intermediate recursively for each node.

When we try to generate intermediate code for Assignment node. Since there is an id node and an arithmetic node inside, we go deep in those two nodes and generate intermediate code for them, firstly.

Finally, A file with '.inter' file will be created at the same location as provided '.gf' file.



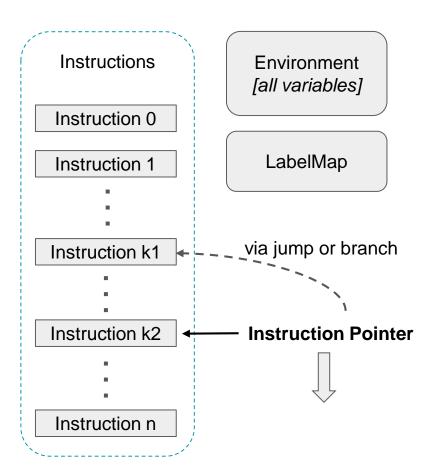
Runtime —— Parsing Phase



Each line in the intermediate code file will be parsed to an instruction.

The LabelMap is used to store the mapping relationship between a label and the index of an instruction.

Runtime — Execution Phase



Core Data Structures

- **Instruction Pointer**: *int*. Used to indicate the next instruction.
- **Environment**: map<string, integer>. Used to store all the variables at runtime. All the variables will be updated and queried from the environment. There're no mutiple environments because we do not support nested declarations.

Running Process

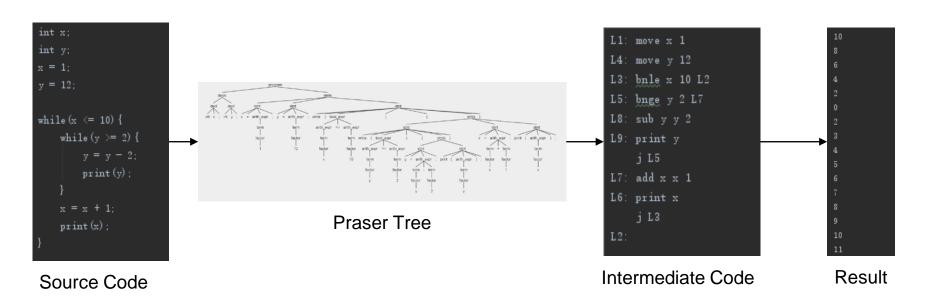
- Instructions are executed one by one from the very first instruction.
- A program completes when the Instruction Pointer points the n + 1 instruction.
- The Instruction Pointer can be changed by an jump or branch instruction.

Sample

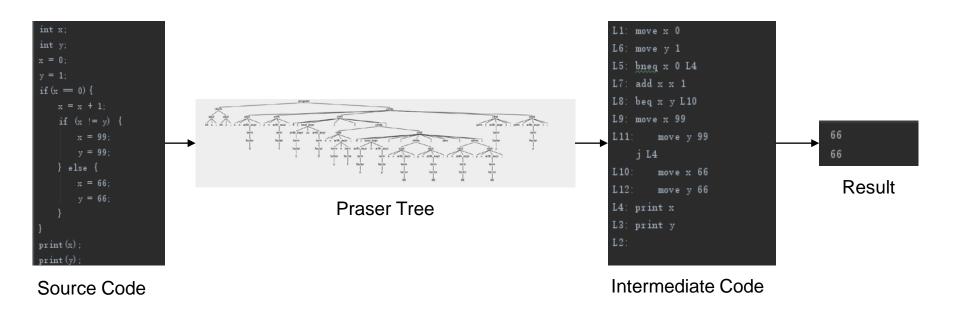
Four Samples to show the accuracy of language:

- While loop sample
- If statement sample
- Compound expression sample
- Factorial sample

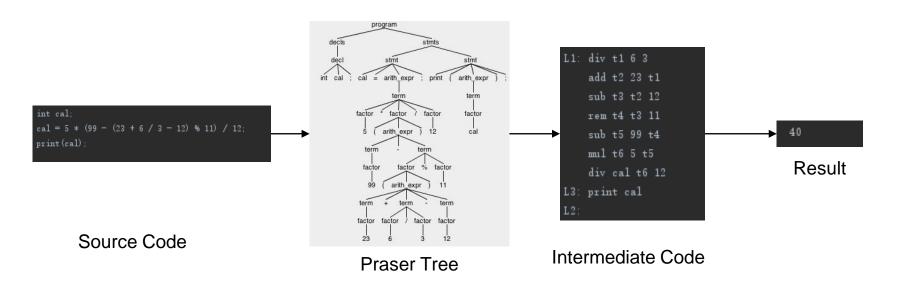
While Loop Sample



If Statement Sample



Compound Expression Sample



Factorial Sample

