

Jixun Liu

Date of Birth: 11th April 2001 E-mail: liujx2019@mail.sustech.edu.cn
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EDUCATION

UnderGraduate pursuing a B.Eng., 09/2019 - (expected)07/2023
Microelectronic Science and Engineering
Southern University Of Science And Technology(SUSTech), Shenzhen, Guangdong, China
Overall GPA: 3.71/4.00
Relevant Courses:

- CMOS Analog Integrated Circuit Design
- Advanced Digital CMOS IC Design
- VLSI Fabrication Technology
- The Foundation of IC Layout Design
- Electronic Design Automation (EDA) Basics

RESEARCH & PROGRAMS

A Reference System Used For IoT Applications

- Combining three separate voltage references(Current Controlled Voltage Reference & Bandgap Voltage Reference) into a system in order to provide several low voltage power supplies in the mean time. Three voltage references could be adjusted with digital inputs.
- Program Member-Responsible for the final layout design.
- Supervisor: Prof. Chenchang Zhan

A 2M-Hz RC Relaxation Oscillator Based on Self-Regulated Voltage Reference

- Using the technique of self-regulated voltage supply(input:2.5-5.5V & output:2.2V) to implement a low-power intermediate frequency reference with high precision of frequency(Variation < 0.25%) and duty cycle(Variation < 0.5%).
- Leader-Responsible for part of schematic design and the whole layout design
- Supervisor: Prof. Chenchang Zhan

INTERNSHIP & VOLUNTEERING EXPERIENCE

SUSTech Volunteering Association 09/2019 - present

- 41.5 hours of Volunteering Time.
- Excellent Volunteer in 10th Anniversary of University.

STEAM+ Education Volunteering Association 10/2020 - 12/2020

- Teaching Assistant

Summer Internship of Sense Lab 2022 07/2022-09/2022

- Directed by iViP Lab of Tsinghua University
- Researching on an effective Speech Identification Platform which could be applied on embedded system with an accuracy larger than 95%.
- Responsible for algorithm design(Python + TensorFlow)

SCHOLARSHIPS & AWARDS

<i>Third Prize(as a team) in RoboMaster University Championship Competition 2020</i>	08/2020
<ul style="list-style-type: none">• Designing the structure and circuits to build a group of robots for competitions with remote control.• Directed by DJI Co. Ltd.	
<i>Third Prize Scholarship, SUSTech</i>	09/2020
<i>Third Prize Scholarship, SUSTech</i>	09/2021
<i>Winner(as a team) in Mathematical Contest In Modeling of Guangdong Province in China</i>	10/2021
<i>Third Prize Scholarship, SUSTech</i>	09/2022

ACTIVITIES

<i>Librarian Assistant in College Library</i>	09/2020 - 07/2021
<i>The leader in Graduation & Employment Department of Student Union</i>	02/2021 - present
<ul style="list-style-type: none">• Experience Sharing Session of Postgraduate Planning• Career Talk Activities	
<i>One of the Organizers in Domestic Food Festival On Campus</i>	04/2021
<i>Global Undergraduate Engineer Research and Innovation Summer School</i>	08/2021
<ul style="list-style-type: none">• Directed by Rapidamic Lab and SUSTech	
<i>Berkeley International Study Program at UC Berkeley</i>	(Expected)01/2023 - 05/2023

STANDARDIZED TEST

IELTS

- Overall 7 (Reading 8.5, Listening 7.5, Speaking 5.5, Writing 6.5)

GRE

- Overall 324 (Verbal 154, Quantitative 170) + 4.0 (Writing)

SKILLS & INTERESTS

Skills

- Programming - Verilog(and C, C++, Python, Java).
- PCB Design - Altium(Robots design; FM radio design project).
- Integrated Circuit Design - Cadence(0.18um technology; from schematic design to tape-out).

Interests

- Swimming(Get an A in Course Learning), Running, Table Tennis, Badminton, Music Theory.