Jixun Liu

Date of Birth: 11th April 2001 E-mail: liujx2019@mail.sustech.edu.cn
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EDUCATION

UnderGraduate pursuing a B.Eng.,   09/2019 - (expected)07/2023
Microelectronic Science and Engineering
Southern University Of Science And Technology(SUSTech), Shenzhen, Guangdong, China
Relevant Courses:

- CMOS Analog Integrated Circuit Design
- Advanced Digital CMOS IC Design
- VLSI Fabrication Technology
- The Foundation of IC Layout Design
- Electronic Design Automation (EDA) Basics

RESEARCH & PROGRAMS

A Reference System Used For Multiple Applications
- Team work supervised by professor Chenchang Zhan

- Responsible for layout design

A 2M-Hz RC Relaxation Oscillator Based on Self-Regulated Voltage Reference
- Team work supervised by professor Chenchang Zhan

- Responsible for schematic and layout design

INTERNSHIP & VOLUNTEERING EXPERIENCE

SUSTech Volunteering Association   09/2019 - present
- 41.5 hours of Volunteering Time.
- Excellent Volunteer in 10th Anniversary of University.

STEAM+ Education Volunteering Association   10/2020 - 12/2020
- Teaching Assistant

iViP of Tsinghua University   07/2022-09/2022
- Working on the Project of Speech Identification Platform.
- Responsible for algorithm design(Python + TensorFlow)

SCHOLARSHIPS & AWARDS

Third Prize(as a team) in RoboMaster University Championship Competition 2020   08/2020
- Directed by DJI Co. Ltd.

Third Prize Scholarship, SUSTech   09/2020

Third Prize Scholarship, SUSTech	09/2021
Winner(as a team) in Mathematical Contest In Modeling of Guangdong Province in China	10/2021
Third Prize Scholarship, SUSTech	09/2022

ACTIVITIES

Librarian Assistant in College Library	09/2020 - 07/2021
The leader in Graduation & Employment Department of Student Union	02/2021 - present

- Experience Sharing Session of Postgraduate Planning
- Career Talk Activities

One of the Organizers in Domestic Food Festival On Campus	04/2021
Global Undergraduate Engineer Research and Innovation Summer School	08/2021

- Directed by Rapidamic Lab and SUSTech

Berkeley International Study Program at UC Berkeley	(Expected)01/2023 - 05/2023

STANDARDIZED TEST

IELTS
- Overall 7 (Reading 8.5, Listening 7.5, Speaking 5.5, Writing 6.5)

GRE
- Overall 324 (Verbal 154, Quantitative 170) + 4.0 (Writing)

SKILLS & INTERESTS

Skills
- Programming - Verilog(and C, C++, Python, Java).
- PCB Design - Altium(Robots design; FM radio design project).
- Integrated Circuit Design - Cadence(0.18um technology; from schematic design to tape-out).

Interests
- Swimming(Get an A in Course Learning), Running, Table Tennis, Badminton, Music Theory.