

Quiz3

Due No due date

Points 2

Questions 8

Available Apr 20 at 3:38pm - Apr 20 at 4:10pm 32 minutes

Time Limit 30 Minutes

Instructions

Covers Chapter 7 and 8

This quiz was locked Apr 20 at 4:10pm.

Attempt History

	Attempt	Time	Score
LATEST	Attempt 1	7 minutes	1.24 out of 2

Score for this quiz: **1.24** out of 2
Submitted Apr 18 at 1:22pm
This attempt took 7 minutes.

Correct!

Question 1**0.15 / 0.15 pts**

Given N , the number of tasks that are either waiting in the queue or being processed by the CPU, arrival rate Y , and response time R , what is the relationship between N , Y , R in a stable system?

☒ $N=YR$

☐ $N=Y/R$

☐ $N=Y+R$

☐ $N=YR^2$

Question 2**0 / 0.15 pts**

Given 60% utilization, the ratio of response time R and execution time S is:

Correct Answer☐ 2.5**You Answered**☒ 1.25☐ 1.33☐ 1.67**Question 3****0.54 / 1 pts**

Given three tasks A, B, C with service time of 6ms, 4ms, and 4ms, respectively. Assume that all three tasks arrive all at the same time but A comes slightly before B and B comes slightly before C. Please show the detailed schedule for the following algorithms:

- FIFO: the order of scheduling is -

- with completion time of

ms, ms, and

ms, respectively.

- SJF: the order of scheduling is -

- with completion time of

ms, ms, and

ms, respectively.

- Round robin with time slice of 4ms: the completion time of A, B, C are

ms,

ms, and

ms, respectively.

- LIFO: the order of scheduling is -

-

with completion time of

ms,

ms, and

ms, respectively.

- Round robin with time slice of 1ms: the completion time of A, B, C are

ms,

ms, and

ms, respectively.

Answer 1:**Correct!**

Answer 2:**Correct!**

Answer 3:**Correct!**

Answer 4:**Correct!**

Answer 5:**ou Answered****orrect Answer**

Answer 6:

You Answered

4

Correct Answer

14

Answer 7:

Correct!

B

Answer 8:

Correct!

C

Answer 9:

Correct!

A

Answer 10:

Correct!

4

Answer 11:

You Answered

4

Correct Answer

8

Answer 12:

You Answered

6

Correct Answer

14

Answer 13:

You Answered

8

Correct Answer

14

Answer 14:

Correct!

8

Answer 15:

You Answered

10

Correct Answer

12

Answer 16:

Correct!

C

Answer 17:

Correct!

B

Answer 18:

Correct!

A

Answer 19:

Correct!

4

Answer 20:

You Answered

4

Correct Answer

8

Answer 21:

You Answered

6

Correct Answer

14

Answer 22:

You Answered

7

Correct Answer

14

Answer 23:

You Answered

5

Correct Answer

11

Answer 24:

You Answered

5

Correct Answer

12

Question 4

0 / 0.15 pts

Which memory address translation strategy is used as the lowest level of all multi-level address translation systems?

Correct Answer

☐ paging

You Answered

☒ segmentation☐ static base+bound☐ swapping**Question 5**

0.15 / 0.15 pts

Which of the following strategy causes internal fragmentation?

Correct!

☒ paging☐ segmentation☐ caching☐ memory bitmap

Question 6**0.15 / 0.15 pts**

Which of the following does Multi-level feedback CPU scheduling does not achieve?

Correct!

- ☐ Responsiveness
- ☒ Deadline Assurance
- ☐ Starvation-Freedom
- ☐ Fairness

Question 7**0.15 / 0.15 pts**

With regard to minimizing queuing delay, which scenario is best case?

Correct!

- ☐ bursty arrival
- ☐ exponential arrivals
- ☒ evenly spaced arrivals
- ☐ poisson arrivals

Question 8**0.1 / 0.1 pts**

Every memory segment still uses an array of pairs of base and bound registers for its organization.

Correct!

- ☒ True

☐ False

Quiz Score: **1.24** out of 2