Quiz3

Due No due datePoints 2Questions 8

Available Apr 20 at 3:38pm - Apr 20 at 4:10pm 32 minutes Time Limit 30 Minutes

Instructions

Covers Chapter 7 and 8

This quiz was locked Apr 20 at 4:10pm.

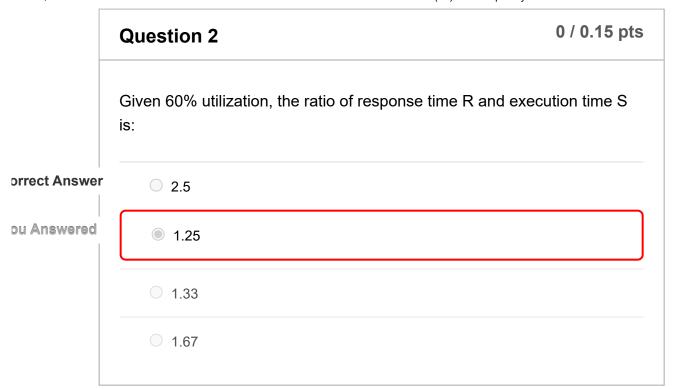
Attempt History

	Attempt	Time	Score
LATEST	Attempt 1	7 minutes	1.24 out of 2

Score for this quiz: 1.24 out of 2

Submitted Apr 18 at 1:22pm This attempt took 7 minutes.

	Question 1 0.15 / 0.15 pts	
	Given <i>N</i> , the number of tasks that are either waiting in the queue or being processed by the CPU, arrival rate Y, and response time R, what is the relationship between N, Y, R in a stable system?	
Correct!	N=YR	
	○ N=Y/R	
	○ N=Y+R	
	○ N=YR^2	



Question 3 0.54 / 1 pts

Given three tasks A, B, C with service time of 6ms, 4ms, and 4ms, respectively. Assume that all three tasks arrive all at the same time but A comes slightly before B and B comes slightly before C. Please show the detailed schedule for the following algorithms:

- FIFO: the order of scheduling is
 A
 -
 - B with completion time of ms, 4 ms, and
 - 4 ms, respectively.
- SJF: the order of scheduling is B
 - C with completion time of
 - ms, 4 ms, and
 - 6 ms, respectively.

	Round robin with	th time slice of 4ms: th	ne completion time of A, B, C are
	8	ms, 8	ms, and
	10	ms, respectively.	
	LIFO: the order	of scheduling is C	-
	В	- A	with completion time of
	4	ms, 4	ms, and
	6	ms, respectively.	
	Round robin with	th time slice of 1ms: th	ne completion time of A, B, C are
	7	ms, 5	ms, and
	5	ms, respectively.	
	Answer 1:		
Correct!	Α		
	Answer 2:		
Correct!	В		
	Answer 3:		
Correct!	С		
	Answer 4:		
Correct!	6		
	Answer 5:		
ou Answered	4		
orrect Answer	10		
	Answer 6:		

	Question 6	0.15 / 0.15 pts
	Which of the following does Multi-level feedback CPU not achieve?	scheduling does
	Responsiveness	
Correct!	Deadline Assurance	
	Starvation-Freedom	
	Fairness	
	Question 7	0.15 / 0.15 pts
	With regard to minimizing queuing delay, which scena	rio is best case?
	o bursty arrival	
	exponential arrivals	
Correct!	evenly spaced arrivals	
	opoisson arrivals	

Every memory segment still uses an array of pairs of base and bound registers for its organization. Correct! True

○ False

Quiz Score: 1.24 out of 2