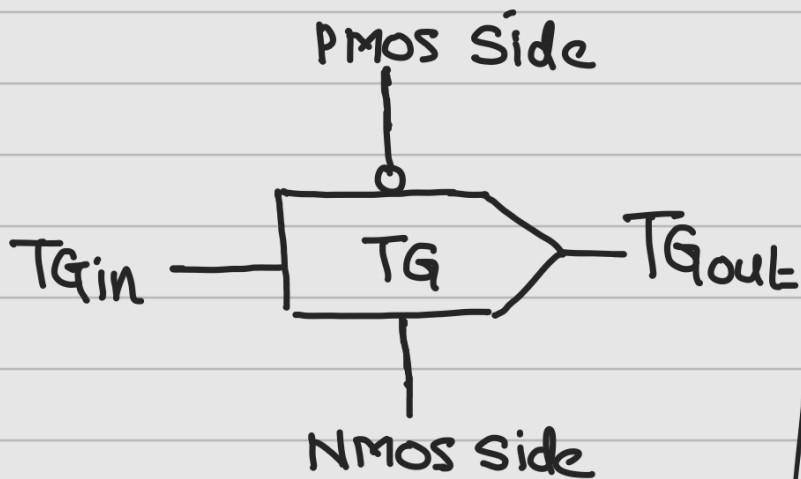
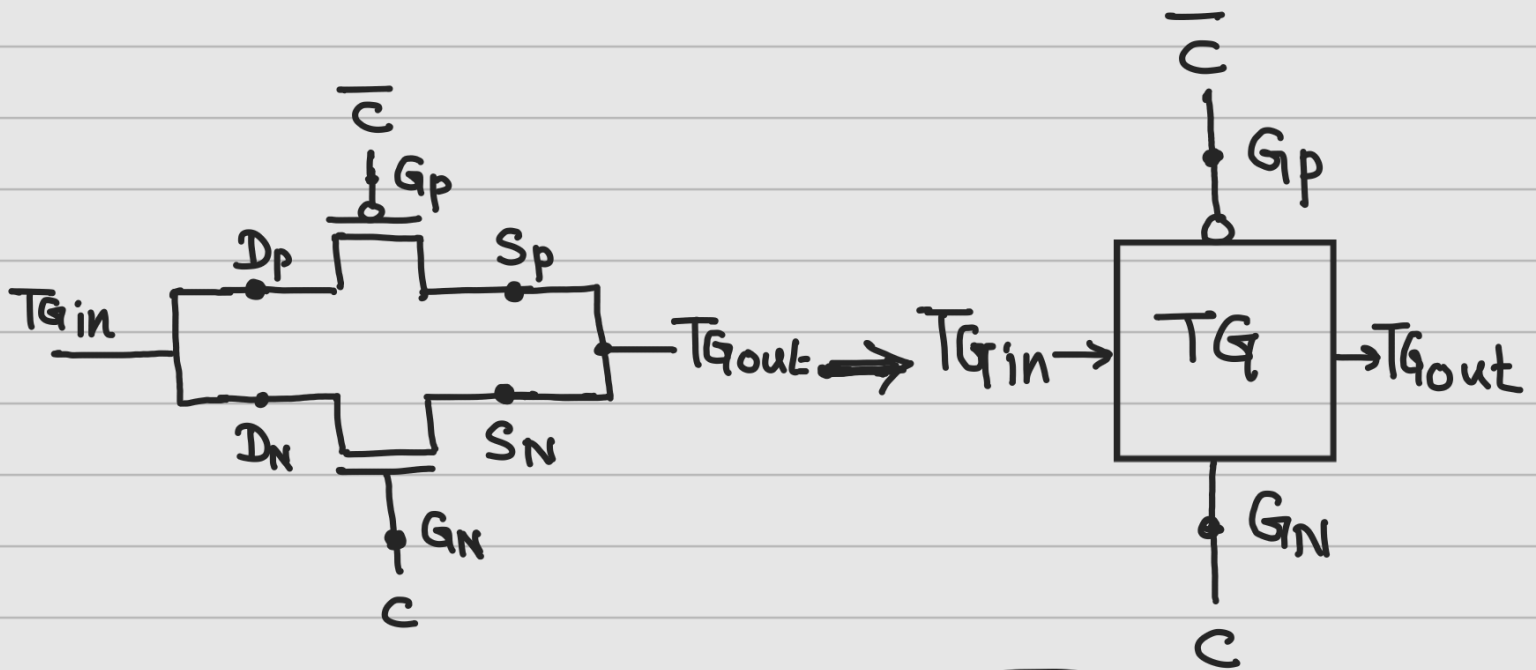


## CONSTRUCTION & SYMBOL(S):

Transmission Gate (TG) = 1 PMOS // 1 NMOS



Operation :-

$C = 0$  (NMOS Side)

$\overline{C} = 1$  (PMOS Side)

$TG = \text{OFF (O.C)}$

$C = 1$  (NMOS side)

$\overline{C} = 0$  (PMOS side)

$TG = \text{ON (S.C)}$