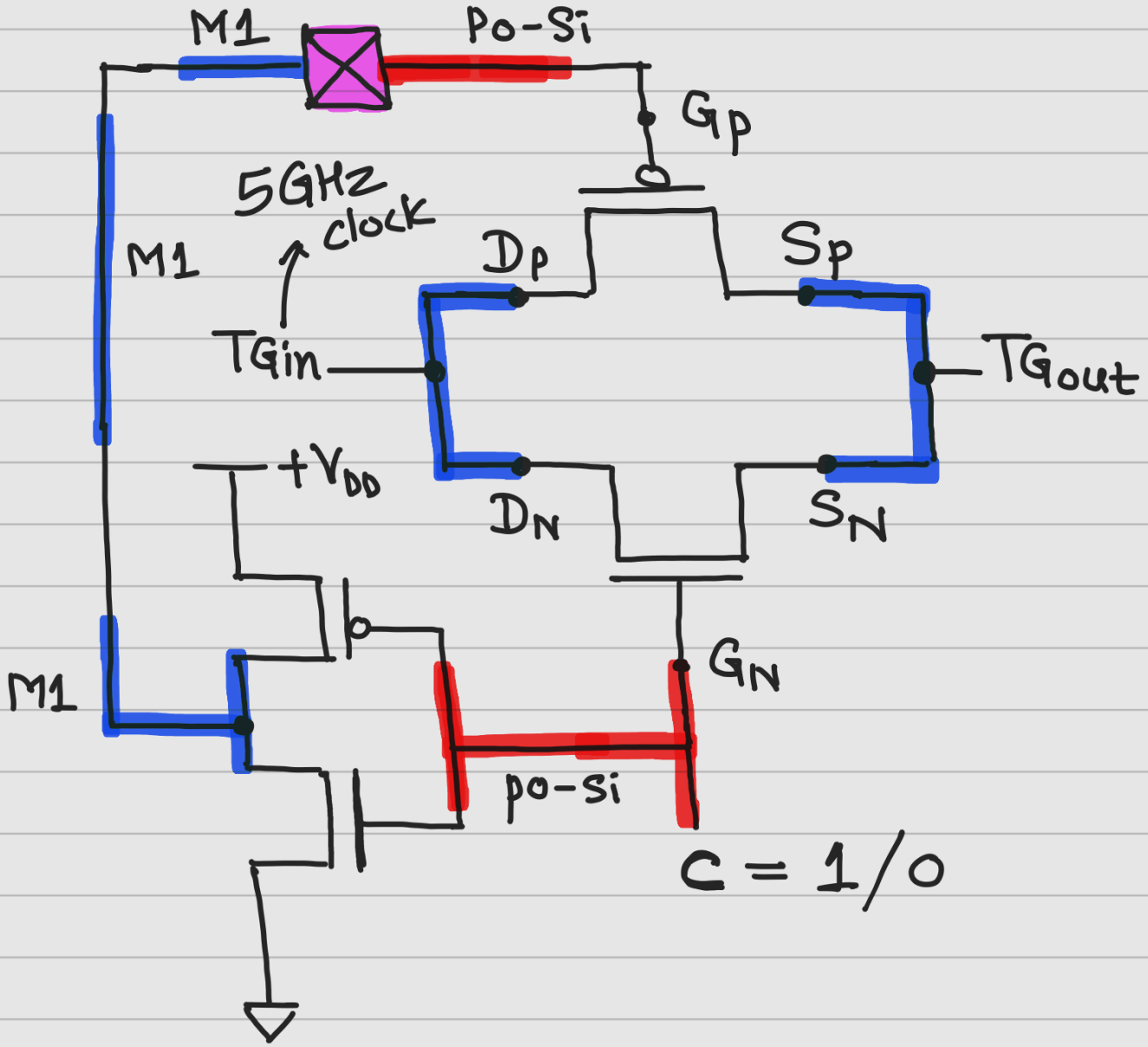
# MOSFET-LEVEL SCHEMATIC OF TG




PMOS:. $\frac{W_P}{L_P} = \left(\frac{500}{100}\right)nm = 5 = p$

NMOS: $\frac{W_N}{L_N} = \left(\frac{500}{100}\right)nm = 5 = n$

$\left.\begin{matrix} \\ \\ \end{matrix}\right\} \frac{p}{n} = 1$

Metal-1 Layer (M1)

Polysilicon Layer (po-si)

Metal-1 To polysilicon contact

closed s/w

For C = 1, TGout = TGin (S-1, S-0)

C = 0, TGout ≠ TGin (open s/w)