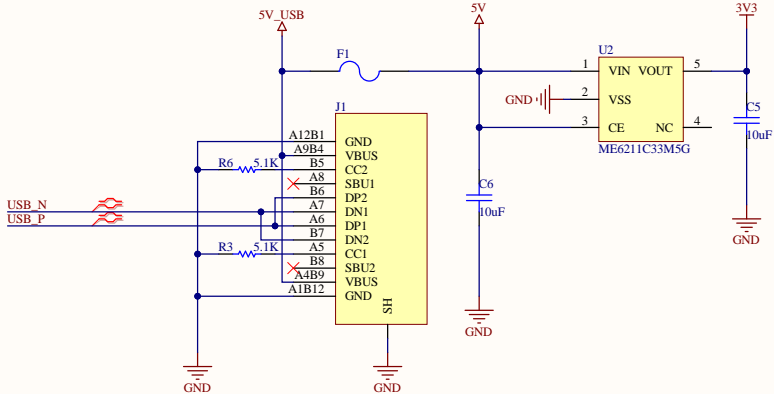
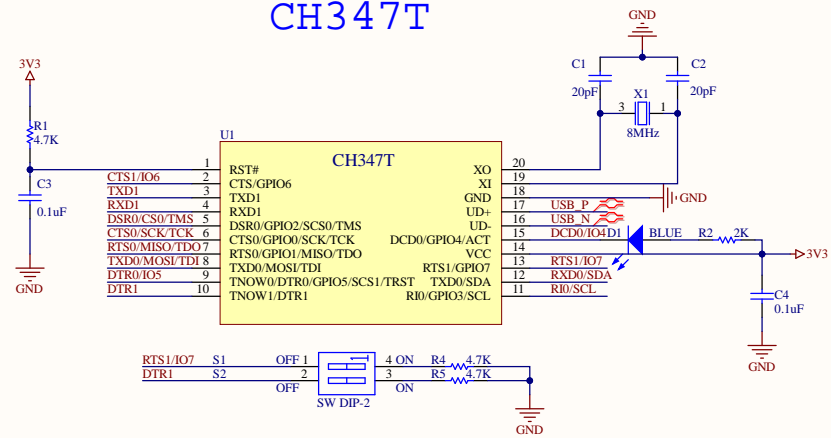


USB-HS-Bridge v1.0

POWER



CH347T



S1	S2	DTR1	RTS1	MODE
OFF	OFF	1	1	0
ON	OFF	1	0	1
OFF	ON	0	1	2
ON	ON	0	0	3

UART0

CTS0/SCK/TCK	U0_CTS
TXD0/MOSI/TDI	U0_TXD
RXD0/SDA	U0_RXD
RTS0/MISO/TDO	U0_RTS
DTR0/IO5	U0_DTR
DSR0/CS0/TMS	U0_DSR
RI0/SCL	U0_RI
DCD0/IO4	U0_DCD

I2C

<u>RI0/SCL</u>	<u>I2C_SCL</u>
<u>RXD0/SDA</u>	<u>I2C_SDA</u>

SPI

DSR0/CS0/TMS	SPI_CS0
DTR0/IO5	SPI_CS1
CTS0/SCK/TCK	SPI_CLK
RTS0/MISO/TDO	SPI_MISO
TXD0/MOSI/TDI	SPI_MOSI

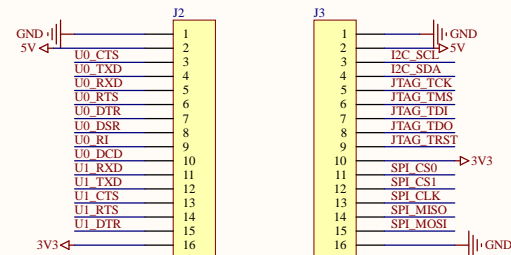
UART1

CTS1/IO6	U1_CTS
TXD1	U1_TXD
RXD1	U1_RXD
RTS1/IO7	U1_RTS
DTR1	U1_DTR

JTAG

CTS0/SCK/TCK	JTAG_TCK
DSR0/CS0/TMS	JTAG_TMS
TXD0/MOSI/TDI	JTAG_TDI
RTS0/MISO/TDO	JTAG_TDO
DTR0/IO5	JTAG_TRST

HEADER



Title		
Size A3	Number	Revision
Date:	8/29/2022	Sheet of
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