ĐẠI HỌC QUỐC GIA THÀNH PHỐ HỒ CHÍ MINH TRƯỜNG ĐẠI HỌC BÁCH KHOA KHOA KHOA HỌC - KỸ THUẬT MÁY TÍNH



Report: Assignment 2 - Operating System

Topic

Simple Operating System

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1 Scheduler

1.1 Question & Answer

Question: What is the advantage of using priority feedback queue in comparison with other scheduling algorithms you have learned?

Answer:

- Thời gian đợi trung bình giảm so với giải thuật First Come First Served
- CPU chạy các process theo Round-Robin "style" nên đảm bảo tính công về thời gian thực thi của các process. Không có process nào được chạy quá thời gian mà OS cho phép, từ đó dẫn đến tránh được tình trạng trì hoãn vô hạn định xảy ra ở một số giải thuật định thời khác như: Priority scheduling hay Multilevel Queue Scheduling
- Sử dụng hai hàng đợi $ready_queue$ và run_queue , các process được chuyển qua lại giữa hai hàng đợi này đến khi các process được hoàn tất, tăng thời gian đáp ứng cho các process do các process có độ ưu tiên thấp đến sau vẫn có thể được thực thi trước các process có độ ưu tiên cao hơn (vì có thể lúc này process có độ ưu tiên cao hơn đang ở run queue).

1.2 Gantt chart

Requirement: Draw Gantt diagram describing how processes are executed by the CPU

1.2.1 Test sched 0

• sched 0 log

```
1 Time slot
             Loaded a process at input/proc/s0, PID: 1
             CPU 0: Dispatched process
5 Time slot
6 Time slot
              \begin{tabular}{lll} {\tt CPU} & 0 \colon {\tt Put process} & 1 & {\tt to run queue} \\ \end{tabular} 
             CPU 0: Dispatched process
9 Time slot 4
             Loaded a process at input/proc/s1, PID: 2
11 Time slot
             CPU 0: Put process 1 to run queue
12
             CPU 0: Dispatched process 2
14 Time slot
15 Time slot
             CPU 0: Put process 2 to run queue
             CPU 0: Dispatched process 2
17
18 Time slot
19 Time slot
              \begin{tabular}{lll} {\tt CPU} & 0 \colon {\tt Put process} & 2 & {\tt to run queue} \\ \end{tabular} 
20
             CPU 0: Dispatched process
21
_{22} Time slot 10
23 Time slot 11
             CPU 0: Put process 1 to run queue
             CPU 0: Dispatched process 2
25
\begin{array}{cccc} {\tt 26} & {\tt Time} & {\tt slot} & 12 \\ {\tt 27} & {\tt Time} & {\tt slot} & 13 \\ \end{array}
             CPU 0: Put process 2 to run queue
28
             CPU 0: Dispatched process 1
30 Time slot
_{\rm 31} Time slot 15
             CPU 0: Put process 1 to run queue
             CPU 0: Dispatched process 2
33
_{34} Time slot 16
             CPU 0: Processed 2 has finished
              {\tt CPU} \ 0 \colon {\tt Dispatched process} \quad 1 \\
36
_{
m 37} Time slot 17
_{38} Time slot _{18}
             39
             {\tt CPU} \ 0 \colon {\tt Dispatched process}
```



```
_{\rm 41} Time slot 19
_{\rm 42} Time slot 20
                 CPU 0: Put process 1 to run queue
43
                  CPU 0: Dispatched process 1
44
\begin{array}{cccc} {}_{45} \text{ Time slot} & 21 \\ {}_{46} \text{ Time slot} & 22 \end{array}
                   \label{eq:cpu} \text{CPU} \ \ 0\colon \ \text{Put process} \quad 1 \ \ \text{to run queue}
47
                   CPU 0: Dispatched process 1
_{49} Time slot 23
                   {\tt CPU} \ 0 \colon {\tt Processed} \quad 1 \ {\tt has} \ {\tt finished}
50
                   \mathtt{CPU} \ 0 \ \mathtt{stopped}
52
53 MEMORY CONTENT:
```

• Gantt chart

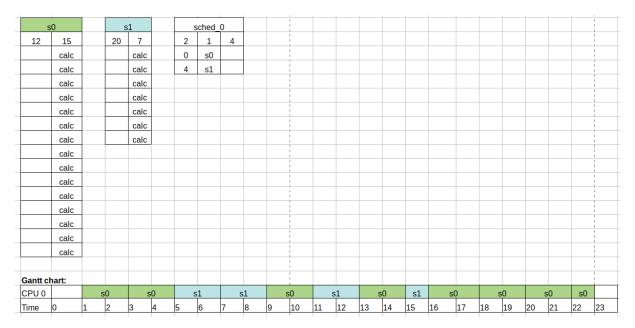


Figure 1: Gantt chart CPU thực thi các process trong sched 0

1.2.2 Test sched 1

• sched 1 log

```
_{1} Time slot _{0}
           Loaded a process at input/proc/s0, PID: 1
2
          CPU 0: Dispatched process 1
_{5} Time slot _{2}
6 Time slot
               3
          CPU 0: Put process 1 to run queue
7
           CPU 0: Dispatched process 1
_{9} Time slot _{4}
          Loaded a process at input/proc/s1, PID: 2
10
_{11} Time slot _{5}
          CPU 0: Put process 1 to run queue
12
           CPU 0: Dispatched process 2
13
_{\rm 14} Time slot _{\rm 6}
          Loaded a process at input/proc/s2, PID: 3
15
_{16} Time slot _{7}
           CPU 0: Put process 2 to run queue
           {\tt CPU}\ 0\colon {\tt Dispatched\ process} \quad 3
18
           Loaded a process at input/proc/s3, PID: 4
_{20} Time slot _{8}
21 Time slot
        CPU 0: Put process 3 to run queue
```



```
^{23} CPU 0: Dispatched process ^4
_{\rm 24} Time slot 10
25 Time slot
               11
           CPU 0: Put process 4 to run queue
26
            CPU 0: Dispatched process 2
\begin{array}{cccc} {\rm 28} & {\rm Time} & {\rm slot} & 12 \\ {\rm 29} & {\rm Time} & {\rm slot} & 13 \end{array}
            CPU 0: Put process 2 to run queue
            CPU 0: Dispatched process 3
31
_{\rm 32} Time slot -14
_{\rm 33} Time slot _{\rm 15}
            CPU 0: Put process 3 to run queue
34
            CPU 0: Dispatched process 1
_{36} Time slot _{16}
37 Time slot 17
            CPU 0: Put process 1 to run queue
            CPU 0: Dispatched process 4
39
\begin{array}{cccc} {}_{40} \text{ Time slot} & 18 \\ {}_{41} \text{ Time slot} & 19 \end{array}
            CPU 0: Put process 4 to run queue
42
            CPU 0: Dispatched process 2
CPU 0: Put process 2 to run queue
            CPU 0: Dispatched process 3
47
_{\rm 48} Time slot 22
_{\rm 49} Time slot -23
         CPU 0: Put process 3 to run queue
50
51
            CPU 0: Dispatched process 1
_{52} Time slot 24
53 Time slot 25
            CPU 0: Put process 1 to run queue
            CPU 0: Dispatched process 4
55
^{56} Time slot 26 ^{57} Time slot 27
           CPU 0: Put process 4 to run queue
            CPU 0: Dispatched process 2
60 Time slot 28
           CPU 0: Processed 2 has finished
61
            CPU 0: Dispatched process 3
\begin{array}{cccc} \mathbf{63} & \texttt{Time slot} & \mathbf{29} \\ \mathbf{64} & \texttt{Time slot} & \mathbf{30} \end{array}
           CPU 0: Put process 3 to run queue
            CPU 0: Dispatched process 1
67 Time slot 31
68 Time slot 32
           CPU 0: Put process 1 to run queue
69
70
            CPU 0: Dispatched process 4
_{71} Time slot 33
72 Time slot 34
            CPU 0: Put process 4 to run queue
            CPU 0: Dispatched process 3
74
^{75} Time slot 35 ^{76} Time slot 36
           CPU 0: Put process 3 to run queue
            CPU 0: Dispatched process 1
^{79} Time slot 37 _{80} Time slot 38
           CPU 0: Put process 1 to run queue
            CPU 0: Dispatched process 4
82
83 Time slot 39
_{84} Time slot \ 40
            CPU 0: Put process 4 to run queue
85
            CPU 0: Dispatched process 3
86
87 Time slot 41
88 Time slot 42
            CPU 0: Processed 3 has finished
            CPU 0: Dispatched process 1
90
CPU 0: Put process 1 to run queue
93
            CPU 0: Dispatched process 4
95 Time slot 45
```



```
CPU 0: Processed 4 has finished
CPU 0: Dispatched process 1

Time slot 46

CPU 0: Processed 1 has finished
CPU 0 stopped

CPU 0 stopped

MEMORY CONTENT:
```

• Gantt chart

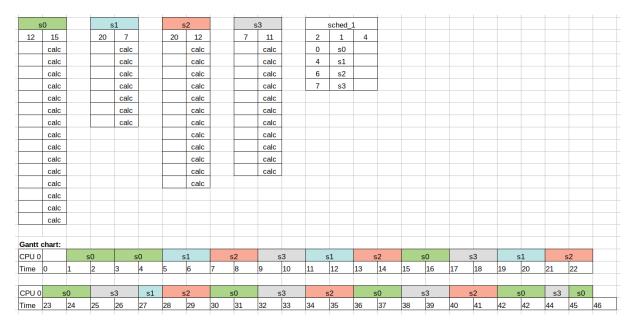


Figure 2: Gantt chart CPU thực thi các process trong sched 1



2 Memory management

2.1 Question & Answer

Question: What is the advantage and disadvantage of segmentation with paging? **Answer**:

- Advantage:
 - Tiết kiệm bộ nhớ, sử dụng bộ nhớ hiệu quả, khắc phục việc kích thước bảng phân trang quá lớn
 - Khắc phục được phân mảnh ngoại
- Disadvantage:
 - Chưa khắc phục được hiện tượng phân mảnh nội

2.2 Show status of RAM

Requirement: Show the status of RAM after each memory allocation and deallocation function call

2.2.1 Test m0

• Below is content of file m0

```
1 1 7
2 alloc 13535 0
3 alloc 1568 1
4 free 0
5 alloc 1386 2
6 alloc 4564 4
7 write 102 1 20
8 write 21 2 1000
```

• Status of RAM after each allocation and deallocation function call in m0

```
1 alloc 13535 0:
_{2} 000: 00000-003ff - PID: 01
                                     (idx 000, nxt:
                                      (idx 001, nxt:
  001 \colon \ 00400 \! - \! 007 \mathtt{ff} \ - \ \mathtt{PID} \colon \ 01
  002: 00800 - 00 \, \text{bff} - \, \text{PID}: \, 01
                                      (idx
                                           002,
                                                 nxt:
5\ 003:\ 00\,\text{c00}-00\,\text{fff}-\text{PID}:\ 01
                                     (idx 003, nxt:
                                                         004
  (idx 004, nxt:
                                                         005
                                           005, nxt:
                                      idx
  006: 01800 - 01 \text{bff} - \text{PID}: 01
                                     (idx 006, nxt:
  007: 01c00-01fff - PID: 01
                                      (idx
                                           007, \mathtt{nxt}:
         02000 - 023 \mathtt{ff} \ - \ \mathtt{PID} \colon \ 01
                                      (idx
                                           008, nxt:
11 009: 02400 - 027 ff - PID: 01
                                      (idx 009, nxt:
                                                         010
  010: 02800 - 02 \, \text{bff} - PID: 01
                                      (idx 010, nxt:
                                                        011)
  011: 02c00-02fff - PID: 01
                                      (idx 011, nxt:
                                                         012
14 \ 012: \ 03000 - 033 ff - PID: \ 01
                                      (idx 012, nxt:
  013: 03400 - 037 ff - PID: 01
                                     (idx 013, nxt:
16
  alloc 1568 1:
_{18}\ 000:\ 00000-003\mathtt{ff}\ -\ \mathtt{PID}:\ 01
                                     (idx 000, nxt:
  (idx 001, nxt:
19
                                      (idx
                                           002,
                                                 nxt:
003: 00c00-00fff - PID: 01
                                     (idx 003, nxt:
  004: 01000 - 013ff - PID: 01
                                      (idx
                                           004, nxt:
         01400\!-\!017\mathtt{ff} - \mathtt{PID} \colon \ 01
   005:
                                      idx
                                           005, nxt:
006: 01800 - 01bff - PID: 01
                                     (idx 006, nxt:
                                                         007
  007: 01c00-01fff - PID: 01
                                      (idx
                                           007, \mathtt{nxt}:
                                                         008)
         02000 - 023 {
m ff} - {
m PID} \colon 01
  008:
                                      idx
                                           008, nxt:
27 009: 02400 - 027ff - PID: 01
                                      (idx 009, nxt:
  010: 02800 - 02 \text{bff} - \text{PID}: 01
                                      (idx 010, nxt:
                                                         011
  011: 02c00-02fff - PID: 01
                                      (idx 011, nxt:
                                                         012
  012: 03000 - 033ff - PID: 01
                                     (idx 012, nxt:
                                                         013)
                                     (idx 013, nxt: -01)
_{\rm 31}\ 013\colon\ 03400\!-\!037{\rm ff}\ -\ {\rm PID}\colon\ 01
```



```
014: 03800-03bff - PID: 01 (idx 000, nxt: 015)
33 015: 03c00-03fff - PID: 01 (idx 001, nxt: -01)
35 free 0:
36\ 014:\ 03800-03bff-PID:\ 01\ (idx\ 000,\ nxt:\ 015)
37 015: 03c00-03fff - PID: 01 (idx 001, nxt: -01)
39 alloc 1386 2:
_{40} 000: 00000-003ff - PID: 01 (idx 000, nxt: 001)
_{41} 001: 00400-007ff - PID: 01
                                     (idx 001, nxt: -01)
^{42} 014: 03800-03bff - PID: 01
                                    (idx 000, nxt: 015)
43 015: 03c00-03fff - PID: 01 (idx 001, nxt: -01)
_{45} alloc 4564 4:
^{46} 000: 00000-003 {\rm ff} - PID: 01 (idx 000, nxt: 001) _{47} 001: 00400-007 {\rm ff} - PID: 01 (idx 001, nxt: -01)
002: 00800 - 00 \text{bff} - PID: 01 \text{ (idx } 000, \text{ nxt: } 003)
_{49} 003\colon 00\,\mathrm{c00}\!-\!00\mathrm{fff} – PID: 01
                                    (idx 001, nxt: 004)
  004: 01000 - 013 \text{ff} - \text{PID}: 01
                                     (idx 002, nxt: 005)
51 \ 005: \ 01400-017 ff - PID: \ 01
                                    (idx 003, nxt: 006)
_{52} 006: 01800-01bff - PID: 01
                                    (idx 004, nxt: -01)
014: 03800 - 03bff - PID: 01
                                    (idx 000, nxt: 015)
_{54} 015: 03c00-03fff - PID: 01 (idx 001, nxt: -01)
```

2.2.2 Test *m*1

• Below is content of file m1

```
1 1 8
2 alloc 13535 0
3 alloc 1568 1
4 free 0
5 alloc 1386 2
6 alloc 4564 4
7 free 2
8 free 4
9 free 1
```

 \bullet Status of RAM after each allocation and deallocation function call in m1

```
1 alloc 13535 0:
_{2} 000: 00000-003ff - PID: 01 (idx 000, nxt: 001)
3 001: 00400-007ff - PID: 01 (idx 001, nxt: 002)
4 002: 00800-00bff - PID: 01 (idx 002, nxt: 003)
_{5} 003: 00c00-00fff - PID: 01
                                       (idx 003, nxt:
 _{6} 004\colon 01000\!-\!013\mathtt{ff} - \mathtt{PID}\colon 01
                                       (idx 004, nxt: 005)
   005: 01400 - 017ff - PID: 01
                                        (idx 005, nxt:
   006: 01800 - 01 \text{bff} - \text{PID}: 01
                                       (idx 006, nxt: 007)
   007\colon\ 01\mathsf{c00}\!-\!01\mathsf{fff}\ -\ \mathsf{PID}\colon\ 01
                                       (idx 007, nxt: 008)
_{10} 008: 02000-023ff - PID: 01
                                        (idx 008, nxt:
11 009: 02400-027ff - PID: 01
                                       (idx 009, nxt: 010)
                                       (idx 010, nxt: 011)
_{12} 010: 02800-02 \, \mathrm{bff} - PID: 01
^{13} 011: 02c00-02fff-PID: 01
                                       (idx 011, nxt: 012)
_{14} 012: 03000-033ff - PID: 01
                                       (idx 012, nxt: 013)
15 013: 03400-037 ff - PID: 01 (idx 013, nxt: -01)
16 =
_{\rm 17} alloc 1568\ 1\colon
18 \ 000: \ 00000-003 ff - PID: \ 01 \ (idx \ 000, \ nxt: \ 001)
^{19}\ 001:\ 00400-007 {\tt ff}\ -\ {\tt PID}\colon\ 01\ ({\tt idx}\ 001,\ {\tt nxt}\colon\ 002)
   002: 00800 - 00bff - PID: 01
                                        (idx 002, nxt:
_{21} 003: 00c00-00fff - PID: 01
                                       (idx 003, nxt: 004)
_{22} 004\colon 01000\!-\!013\mathtt{ff} - \mathtt{PID}\colon 01
                                       (\mathrm{idx}\ 004\,,\ \mathrm{nxt}\colon\ 005)
  005: 01400 - 017ff - PID: 01
                                        (idx 005, nxt:
006: 01800 - 01bff - PID: 01
                                       (idx 006, nxt:
_{25} 007\colon 01\text{c00-}01\text{fff} - PID: 01
                                       (idx 007, nxt:
   008: 02000 - 023 ff - PID: 01
                                       (idx 008, nxt:
_{27} 009: 02400-027ff - PID: 01 (idx 009, nxt: 010)
_{28} 010: 02800-02bff - PID: 01 (idx 010, nxt: 011)
```



```
_{29} 011: 02c00-02fff - PID: 01 (idx 011, nxt: 012)
_{30} 012\colon 03000-033 \mathrm{ff} - PID: 01 (idx 012\,, nxt:
_{\rm 31}\ 013\colon\ 03400\!-\!037{\rm ff}\ -\ {\rm PID}\colon\ 01
                                       (idx 013, nxt:
014: 03800 - 03bff - PID: 01
                                      (idx 000, nxt: 015)
33 015: 03c00-03fff - PID: 01 (idx 001, nxt: -01)
34
35 free 0:
{\tt 36}\ 014\colon\ 03800 - 03{\tt bff}\ -\ {\tt PID}\colon\ 01\ ({\tt idx}\ 000\,,\ {\tt nxt}\colon\ 015)
_{37} 015\colon 03\,\mathtt{c00}-03\mathtt{fff} - PID: 01 (idx 001\,, nxt: -01)
_{\rm 39} alloc 1386 2\colon
{\tt 40~000:~00000-003ff-PID:~01~(idx~000,~nxt:~001)}
_{41} 001: 00400-007 {\tt ff} - PID: 01
                                       (idx 001, nxt:
42\ 014:\ 03800-03bff-PID:\ 01\ (idx\ 000,\ nxt:\ 015)
43 015: 03c00-03fff - PID: 01 (idx 001, nxt: -01)
_{45} alloc 4564 4
46 000: 00000-003ff - PID: 01 (idx 000, nxt: 001)
   001: 00400 - 007 ff - PID: 01
                                       (idx 001, nxt:
48 \ 002: \ 00800 - 00 \, \text{bff} - \, \text{PID}: \ 01
                                       (idx 000, nxt:
                                                           003)
49 003: 00c00-00fff - PID: 01
                                       (idx 001, nxt:
50\ 004:\ 01000-013 {\tt ff}\ -\ {\tt PID}:\ 01
                                       (idx 002, nxt:
                                                           005)
005: 01400 - 017ff - PID: 01
                                       (idx 003, nxt:
                                                           006)
_{52} 006: 01800-01bff - PID: 01
                                       (idx 004, nxt:
_{53} 014\colon 03800\!-\!03\,\mathrm{bff} — PID: 01
                                       (\,\mathtt{idx}\ 000\,,\ \mathtt{nxt}\colon\ 015)
_{54} 015: 03c00-03fff - PID: 01
                                       (idx 001, nxt: -01)
56 free 2:
57\ 002:\ 00800-00bff-PID:\ 01\ (idx\ 000,\ nxt:
003: 0000-00fff - PID: 01
                                       (idx 001, nxt: 004)
_{59} 004\colon 01000\!-\!013\mathtt{ff} — \mathtt{PID}\colon 01
                                       (idx 002, nxt: 005)
  005: 01400 - 017 ff - PID: 01
                                       (idx 003, nxt:
61 \ 006: \ 01800-01 \, \text{bff} - \, \text{PID}: \ 01
                                       (\mathtt{idx}\ 004\,,\ \mathtt{nxt}\colon\ -01)
62\ 014\colon\ 03800\!-\!03\,\text{bff} - PID: 01
                                       (idx 000, nxt: 015)
  015: 03c00-03fff - PID: 01
                                      (idx 001, nxt: -01)
63
64 =
66 \ 014: \ 03800 - 03bff - PID: \ 01 \ (idx \ 000, \ nxt: \ 015)
67 015: 03c00-03fff - PID: 01 (idx 001, nxt: -01)
69 free 1:
```

3 Put it all together

3.1 Synchronization

Trong file mem.c, hai hàm $read_mem()$ và $write_mem()$ truy cập vào $[_ram]$ có thể xảy ra bất đồng bộ nên ta cần thêm một $mutex_lock$: ram_lock để giải quyết, khi đó hai hàm trên được chỉnh lại như sau:

• Hàm read mem()

```
int read_mem(addr_t address, struct pcb_t * proc, BYTE * data) {
   addr_t physical_addr;
   if (translate(address, &physical_addr, proc)) {
      pthread_mutex_lock(&ram_lock); // add
      *data = _ram[physical_addr];
      pthread_mutex_unlock(&ram_lock); // add
      return 0;
   } else {
      return 1;
   }
}
```

• Hàm write mem()

```
int write_mem(addr_t address, struct pcb_t * proc, BYTE data) {
```



```
addr_t physical_addr;
if (translate(address, &physical_addr, proc)) {
  pthread_mutex_lock(&ram_lock); // add
  _ram[physical_addr] = data;
  pthread_mutex_unlock(&ram_lock); // add
  return 0;
} else {
  return 1;
}
```

3.2 Result of simulation

• os 0 log

```
1 Time slot
              Loaded a process at input/proc/p0, PID: 1
3 Time slot
               CPU 0: Dispatched process 1
_5 Time slot _2
              Loaded a process at input/proc/p1, PID: 2
7 Time slot 3
              CPU 1: Dispatched process 2
9 Time slot
10 Time slot
11 Time slot
12 Time slot
13
               CPU 0: Put process 1 to run queue
              CPU 0: Dispatched process 1
14
_{15} Time slot _{8}
16 Time slot
                    9
              CPU 1: Put process 2 to run queue
17
              CPU 1: Dispatched process 2
^{19} Time slot ^{10} ^{20} Time slot ^{11}
_{\rm 21} Time slot 12
               \  \, {\tt CPU} \  \, 0 \colon \, {\tt Processed} \quad 1 \  \, {\tt has} \  \, {\tt finished} 
22
23
               \mathtt{CPU} \ 0 \ \mathtt{stopped}
_{24} Time slot 13
               {\tt CPU} \ 1 \colon {\tt Processed} \ 2 \ {\tt has} \ {\tt finished}
25
               \mathtt{CPU}\ 1 stopped
27
28 MEMORY CONTENT:
_{29} 000: 00000-003ff - PID: 02 (idx 000, nxt: 001)
_{30} 001: 00400-007 {
m ff} - PID: 02 (idx 001, nxt: 007)
007: 01c00-01fff - PID: 02 (idx 002, nxt: 008)
               01 \, \mathrm{de7}:~0 \, \mathrm{a}
{\tt 33}\ 008{\tt :}\ 02000 - 023 {\tt ff}\ -\ {\tt PID}{\tt :}\ 02\ ({\tt idx}\ 003\,,\ {\tt nxt}{\tt :}\ 009)
34 009: 02400-027 \text{ff} - \text{PID}: 02 \text{ (idx } 004, \text{ nxt}: -01)
{\tt 35} \ \ 010 \colon \ 02800 - 02 {\tt bff} \ - \ {\tt PID} \colon \ 01 \ \ ({\tt idx} \ \ 000 \, , \ \ {\tt nxt} \colon \ -01)
              02814: 64
015: 03c00-03fff - PID: 02 (idx 000, nxt: 016)
38 016: 04000-043ff - PID: 02 (idx 001, nxt: 017)
39 017: 04400-047ff - PID: 02 (idx 002, nxt: 018)
40 018: 04800-04bff - PID: 02 (idx 003, nxt: -01)
```

\bullet os_1 log

```
Time slot 0
Loaded a process at input/proc/p0, PID: 1
Time slot 1
CPU 2: Dispatched process 1
Time slot 2
Loaded a process at input/proc/s3, PID: 2
Time slot 3
CPU 0: Dispatched process 2
CPU 2: Put process 1 to run queue
CPU 2: Dispatched process 1
Loaded a process at input/proc/m1, PID: 3
```



```
12 Time slot 4
             {\tt CPU} \ 1: \ {\tt Dispatched process} \quad 3 \\
            CPU 2: Put process 1 to run queue
            {\tt CPU} \ 2 \colon \ {\tt Dispatched \ process} \quad 1
15
            CPU 0: Put process 2 to run queue
            CPU 0: Dispatched process 2
17
18 Time slot 5
           Loaded a process at input/proc/s2, PID: 4
20 Time slot 6
            CPU 1: Put process 3 to run queue
            CPU 1: Dispatched process 4
            {\tt CPU} \ 3 \colon \ {\tt Dispatched \ process} \quad 3
23
            CPU 0: Put process 2 to run queue
            Loaded a process at input/proc/m0, PID: 5
            26
            CPU 2\colon Dispatched process 5
28 Time slot
            CPU 0: Dispatched process 2
30 Time slot
           CPU 1: Put process 4 to run queue
31
            CPU 1: Dispatched process 4
            Loaded a process at input/proc/p1, PID: 6
33
            CPU 2: Put process 5 to run queue
34
            CPU 0: Put process 2 to run queue
            CPU 0: Dispatched process 6
36
           CPU 2: Dispatched process
37
38 Time slot 9
           CPU 3: Put process 3 to run queue
39
40
            CPU 3: Dispatched process
^{41} Time slot 10
            {\tt CPU} \ 1\colon {\tt Put process} \ 4 \ {\tt to run queue}
42
            {\tt CPU} \ 1: \ {\tt Dispatched process} \quad 3
43
            CPU 3: Put process 2 to run queue
44
            CPU 2: Put process 1 to run queue
45
46
            CPU 2: Dispatched process 4
           CPU 3: Dispatched process 5
47
            Loaded a process at input/proc/s0, PID: 7
            CPU 0: Put process 6 to run queue
            CPU 0: Dispatched process 7
_{\rm 51} Time slot ~11
52 Time slot 12
            {\tt CPU} \ 1 \colon \ {\tt Put \ process} \quad 3 \ {\tt to \ run \ queue}
53
            CPU 1: Dispatched process 2
            \label{eq:cpu} \operatorname{CPU} \ 3\colon \operatorname{Put} \ \operatorname{process} \quad 5 \ \operatorname{to} \ \operatorname{run} \ \operatorname{queue}
55
56 Time slot 13
            CPU 0: Put process 7 to run queue
            CPU 0: Dispatched process 7
58
59
            CPU 2: Put process 4 to run queue
            CPU 2\colon Dispatched process 3
60
            CPU 3: Dispatched process
62 Time slot 14
           CPU 1: Put process 2 to run queue
63
            CPU 1: Dispatched process 5
            CPU 3: Put process 1 to run queue
65
_{66} Time slot \ 15
            CPU 3\colon Dispatched process 6
            CPU 0: Put process 7 to run queue
68
             \  \, {\tt CPU} \  \, 0 \colon \  \, {\tt Dispatched process} \quad \, 4 \\
69
            CPU 2: Processed 3 has finished
            CPU 2\colon Dispatched process 7
71
            Loaded a process at input/proc/s1, PID: 8
73 Time slot 16
            CPU 1: Put process 5 to run queue
74
            CPU 1: Dispatched process 8
75
            CPU 3: Put process 6 to run queue
76
77 Time slot 17
            CPU 0: Put process 4 to run queue
            CPU 0: Dispatched process 1
79
             {\tt CPU} \ 3 \colon \ {\tt Dispatched \ process} \ 2 \\
            CPU 2: Put process 7 to run queue
81
            {\tt CPU} \ 2 \colon \ {\tt Dispatched \ process} \quad 4
82
            CPU 3: Processed 2 has finished
            CPU 1: Put process 8 to run queue
```



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85
              CPU 1: Dispatched process 6
              CPU 3: Dispatched process 7
              CPU 0: Processed 1 has finished
              CPU 0: Dispatched process 5
 88
 89 Time slot 18
              CPU 2: Put process 4 to run queue
90
              CPU 0: Processed 5 has finished
91
              {\tt CPU} \ 0 \colon {\tt Dispatched process} \quad 4
 93 Time slot 19
              {\tt CPU}\ 2\colon {\tt Dispatched\ process} \quad 8
              CPU 3: Put process 7 to run queue
              CPU 3: Dispatched process 7
96
 97 Time slot 20
              CPU 1: Put process 6 to run queue
98
               \begin{tabular}{ll} {\tt CPU} & 1: & {\tt Dispatched process} & 6 \\ \end{tabular} 
99
100
              CPU 2: Put process 8 to run queue
              CPU 0: Processed 4 has finished
101
              \mathtt{CPU}\ 0 stopped
103 Time slot 21
              CPU 2\colon Dispatched process 8
104
              CPU 3: Put process 7 to run queue
              CPU 3: Dispatched process 7
106
               \begin{tabular}{lll} {\tt CPU} & 1\colon {\tt Put process} & 6 & {\tt to run queue} \\ \end{tabular}
107
              CPU 1: Dispatched process 6
CPU 2: Put process 8 to run queue
              {\tt CPU} \ 2 \colon \ {\tt Dispatched \ process} \ \ 8
112
113 Time slot 24
             CPU 3: Put process 7 to run queue
114
115
              CPU 3: Dispatched process 7
              CPU 1: Processed 6 has finished
116
              CPU 1 stopped
117
              CPU 2: Processed 8 has finished
118
              \mathtt{CPU}\ 2\ \mathtt{stopped}
119
_{\rm 120} Time slot 25
_{121} Time slot 26
              CPU 3: Put process 7 to run queue
122
              CPU 3: Dispatched process 7
123
124 Time slot 27
              CPU 3: Processed 7 has finished
125
              \mathtt{CPU}\ 3 stopped
126
128 MEMORY CONTENT:
000: 00000-003ff - PID: 05 (idx 000, nxt: 001)
              003e8: 15
_{131} 001\colon 00400-007 \mathrm{ff} - PID: 05 (idx 001\,, nxt: -01)
132\ 002:\ 00800-00bff-PID:\ 05\ (idx\ 000,\ nxt:\ 003)
_{133} 003\colon 00\,\mathrm{coo}\!-\!00\mathrm{fff} – PID: 05 (idx 001\,, nxt: 004)
134 004: 01000-013ff - PID: 05 (idx 002, nxt: 005)
135 005: 01400-017ff - PID: 05 (idx 003, nxt: 006)
                                      (idx 003, nxt: 006)
_{136} 006: 01800-01bff - PID: 05 (idx 004, nxt: -01)
011: 02c00-02fff - PID: 06 (idx 000, nxt: 012)
138 012: 03000-033ff - PID: 06 (idx 001, nxt: 013)
_{139} 013: 03400-037ff - PID: 06 (idx 002, nxt: 014)
140 014: 03800 - 03 \, \text{bff} - \, \text{PID} \colon \ 06 \ \ (\text{idx} \ \ 003 \, , \ \ \text{nxt} \colon \ -01)
019: 04c00-04fff - PID: 01 (idx 000, nxt: -01)
142
              04c14:64
024: 06000-063ff - PID: 05 \text{ (idx } 000, \text{ nxt: } 025)
              06014: 66
144
_{145} \ 025 \colon \ 06400 - 067 \mathtt{ff} \ - \ \mathtt{PID} \colon \ 05 \ \ (\mathtt{idx} \ \ 001 \, , \ \ \mathtt{nxt} \colon \ -01)
026: 06800 - 06bff - PID: 06 (idx 000, nxt: 027)
147 027: 06c00-06fff - PID: 06 (idx 001, nxt: 028)
148 028: 07000-073ff - PID: 06 (idx 002, nxt: 029)
              071\,\mathrm{e}7:~0\mathrm{a}
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