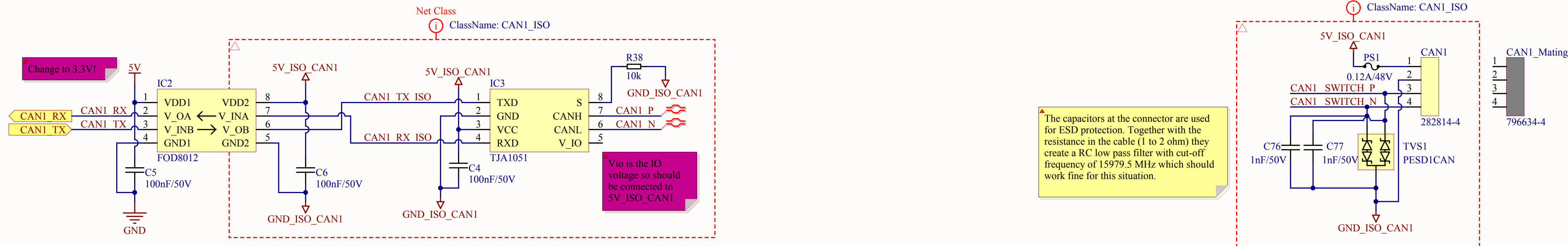
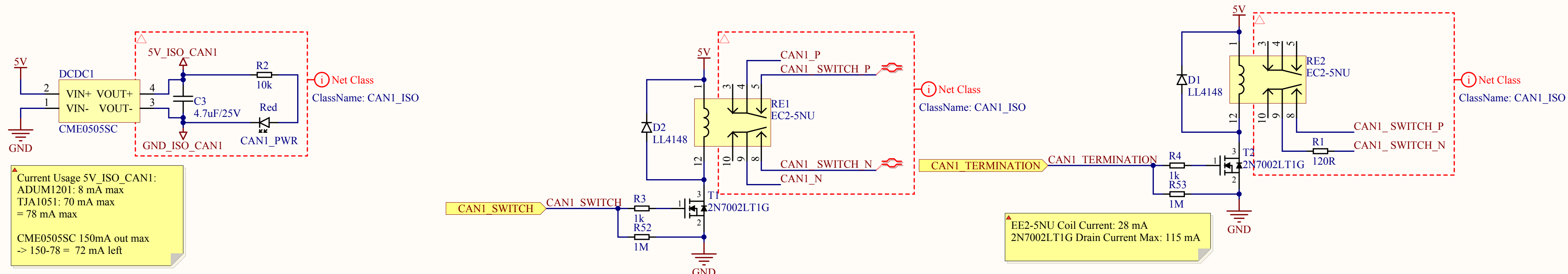
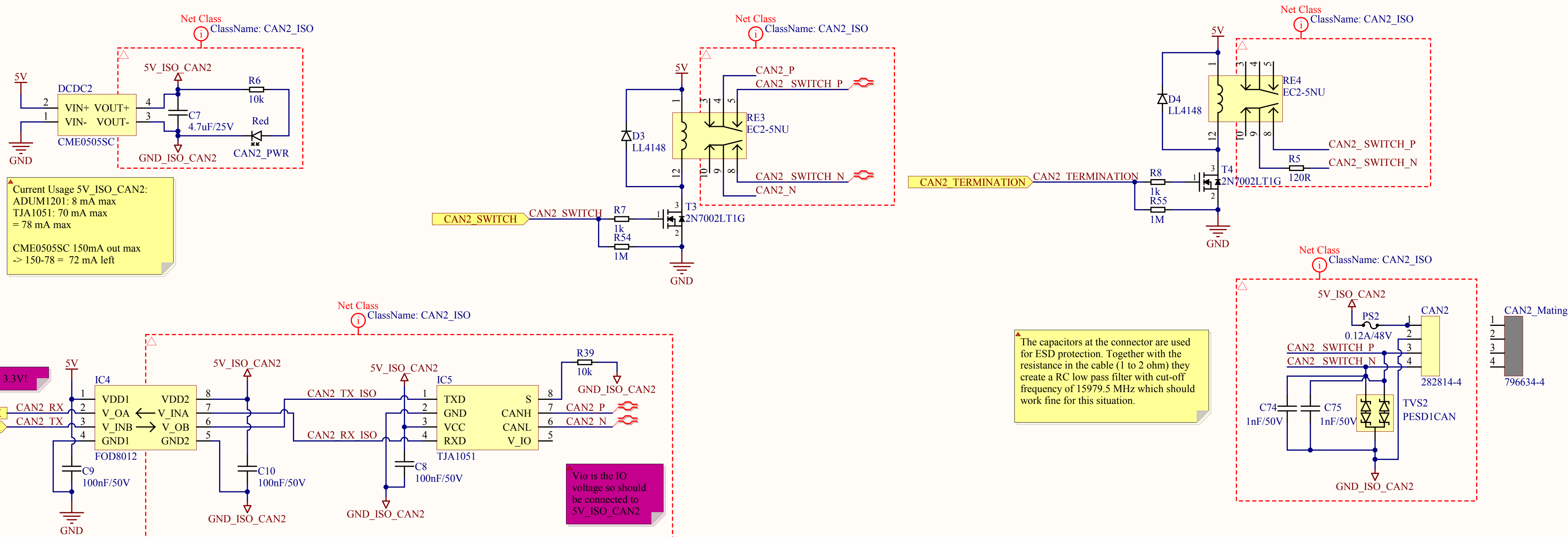


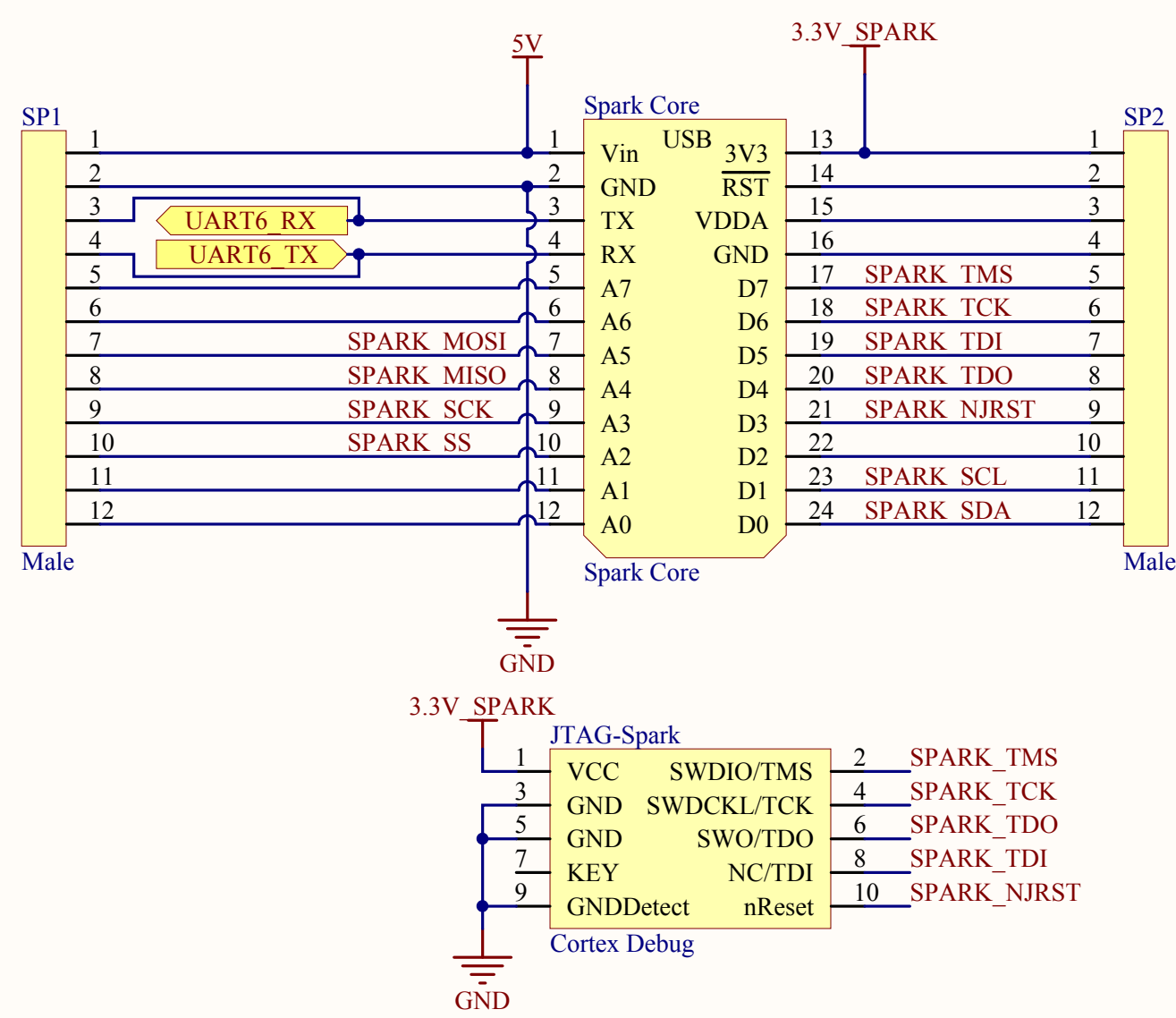
CAN1



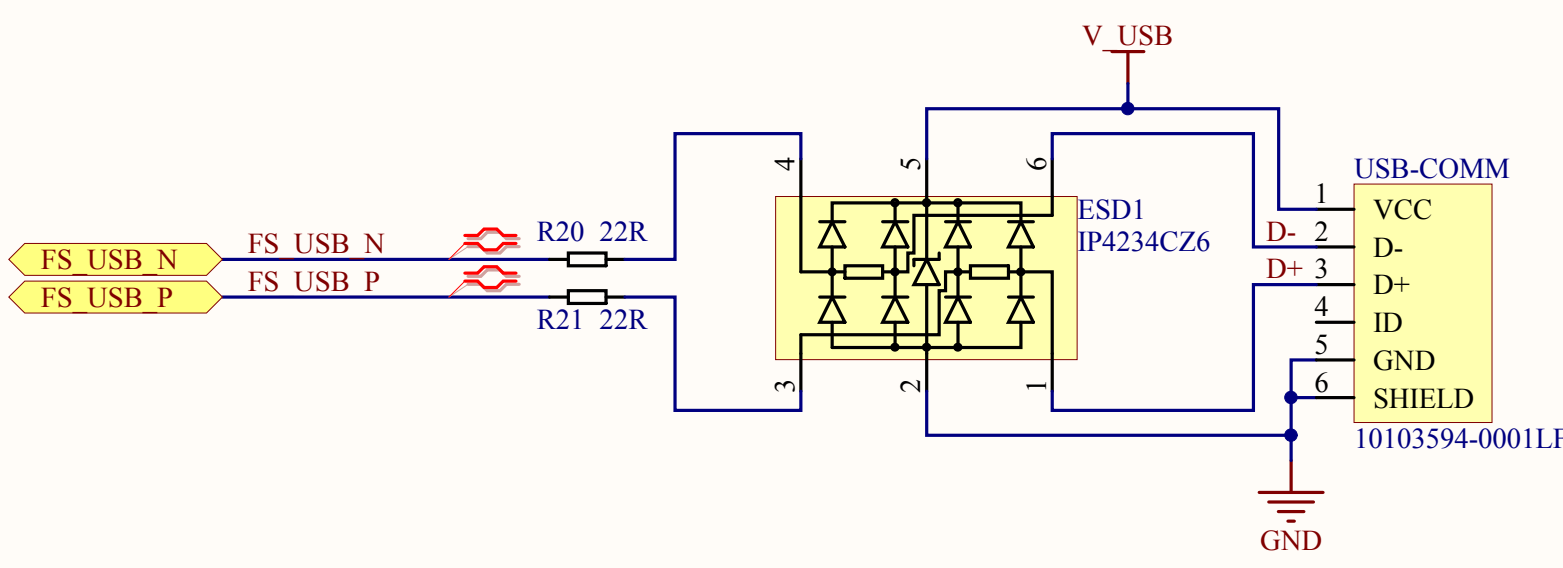
CAN2



Spark Core



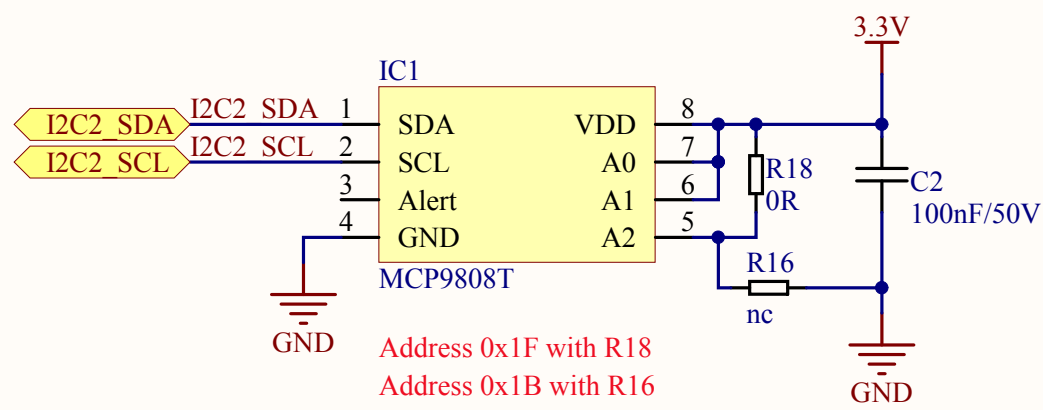
USB Peripheral



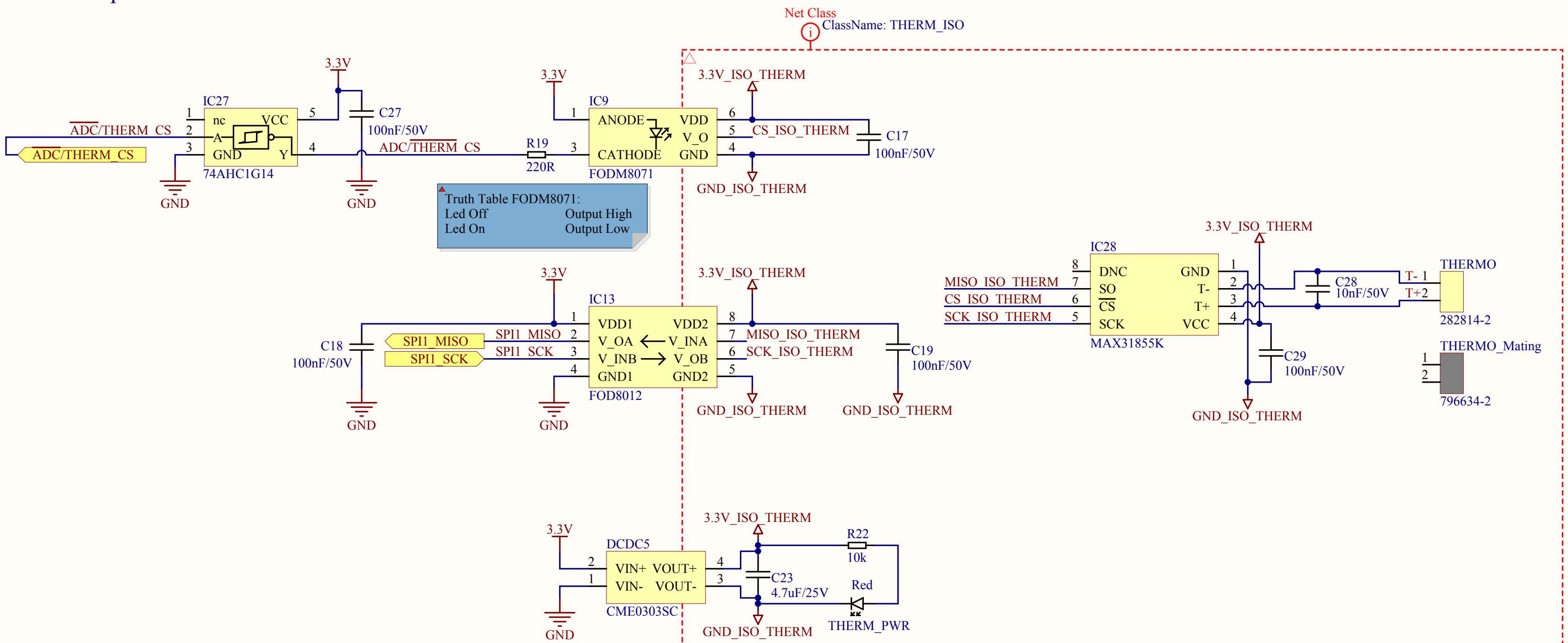
The OTG_FS_VBUS pin (PA9) is used for UART1 so we have to disable it in software in order for the USB interface to work. From the reference manual (rev 7) p. 1229:

"The VBUS pin can be freed by disabling the VBUS sensing option. This is done by setting the NOVBUSSENS bit in the OTG_FS_GCCFG register. In this case the VBUS is considered internally to be always at VBUS valid level (5 V)."

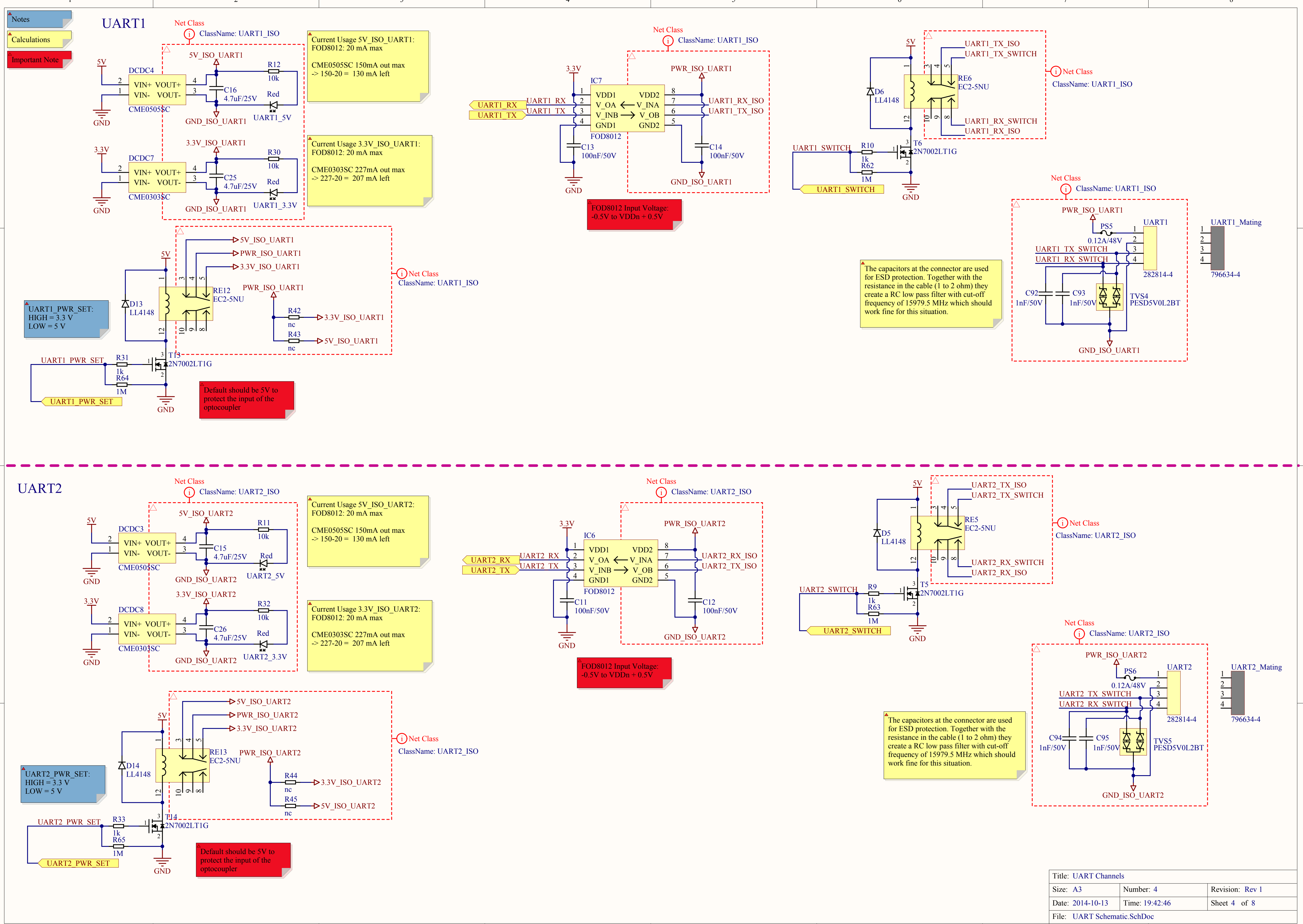
Temperature Sensor

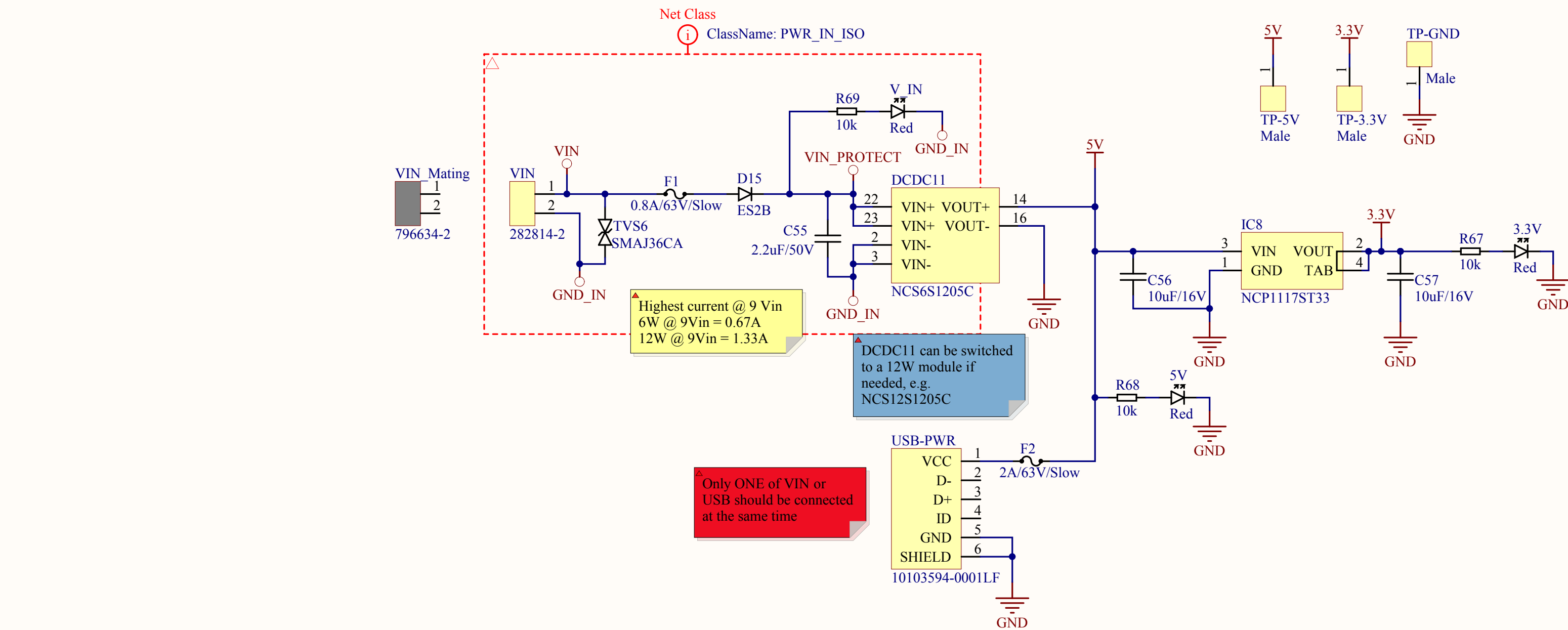


Thermocouple

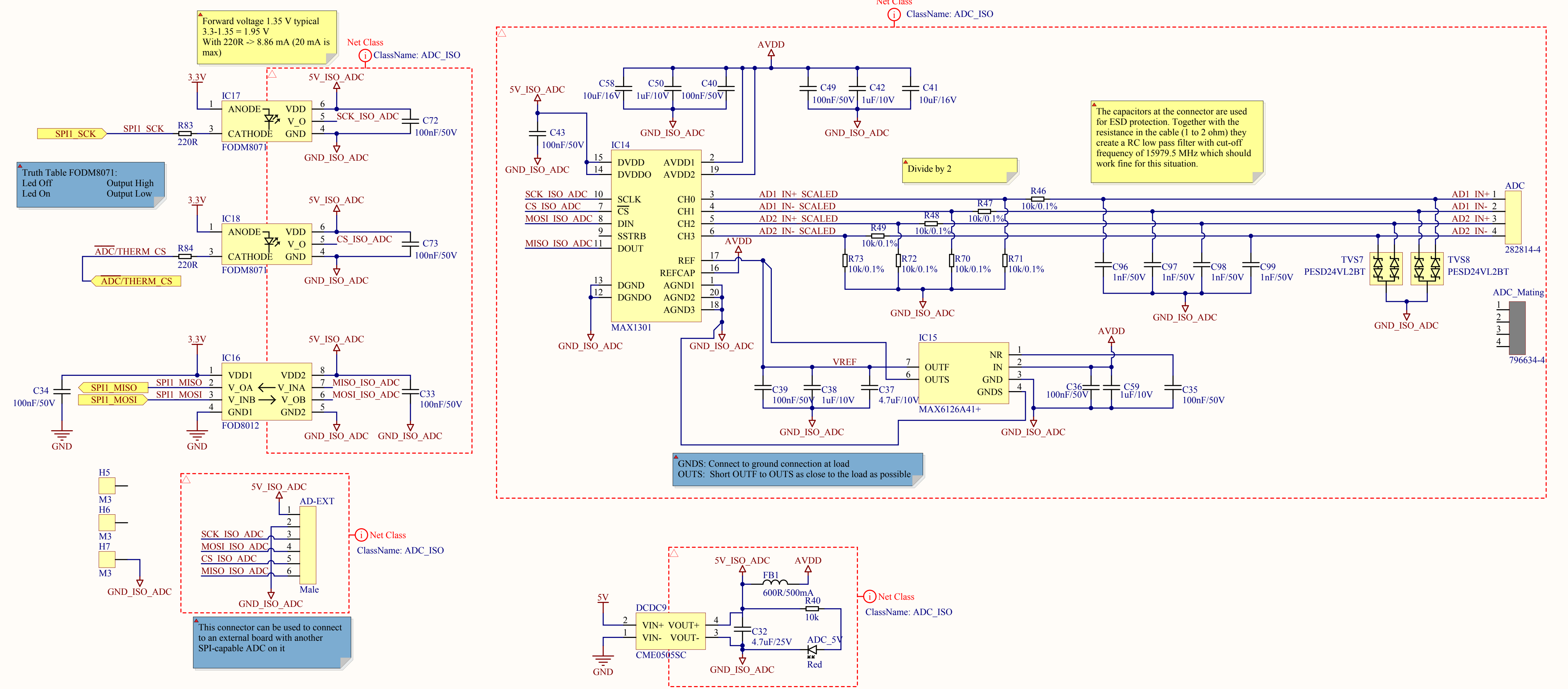


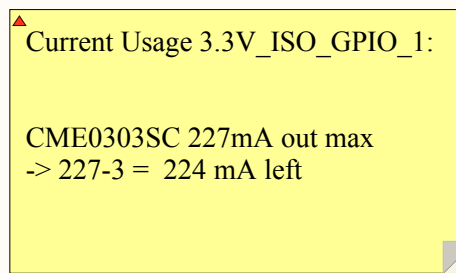
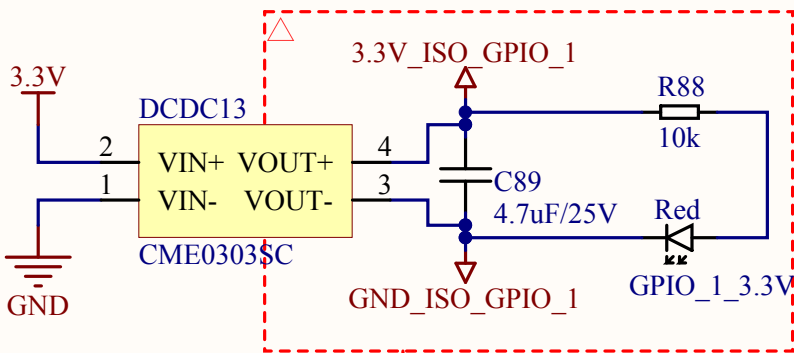
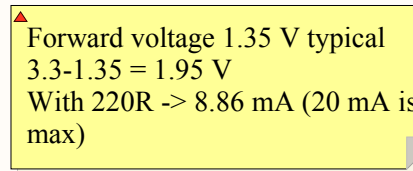
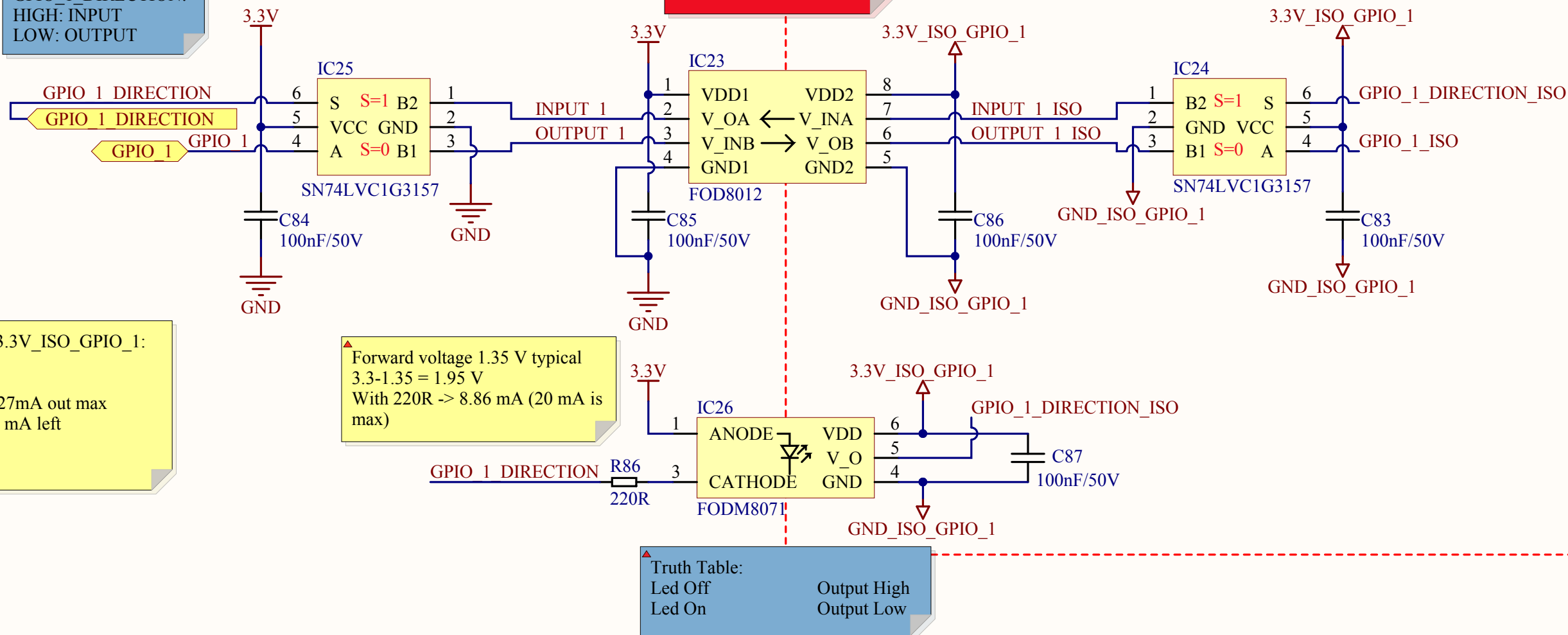
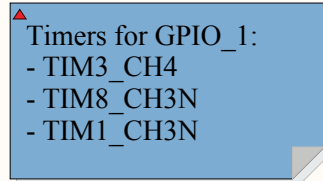
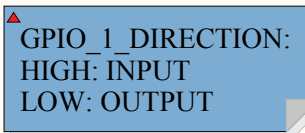
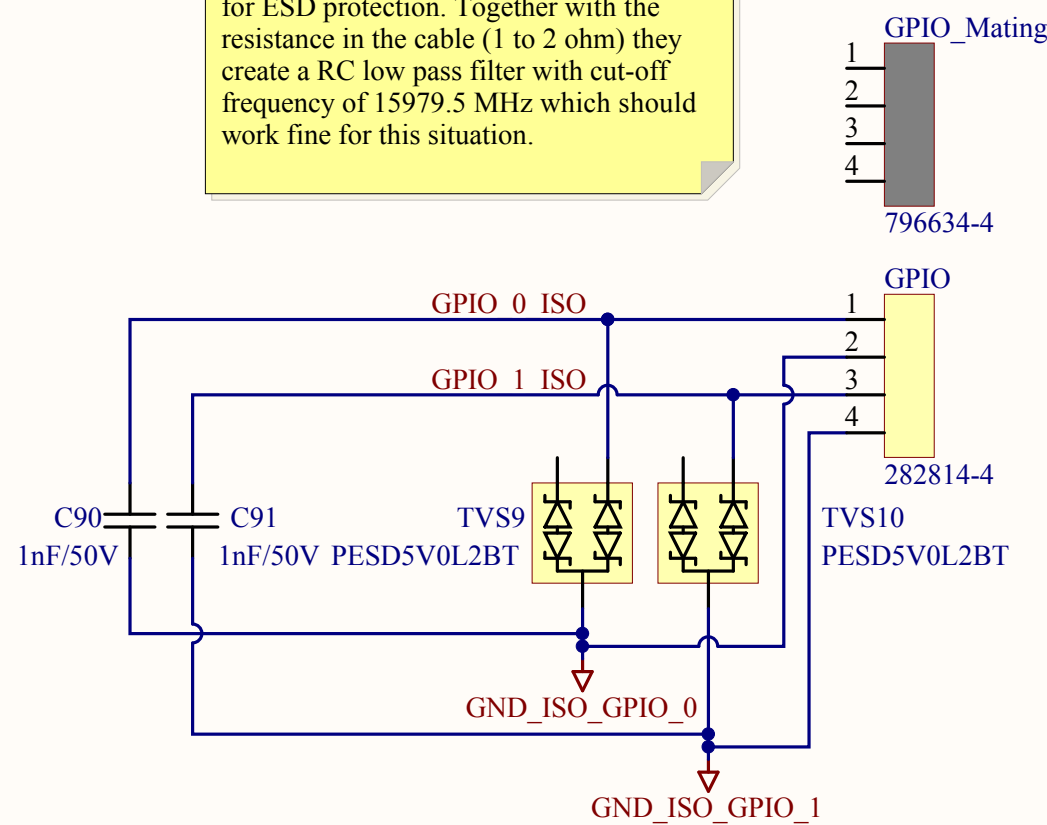
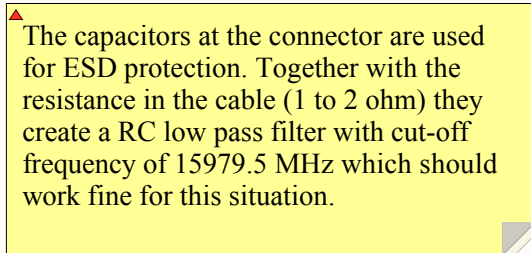
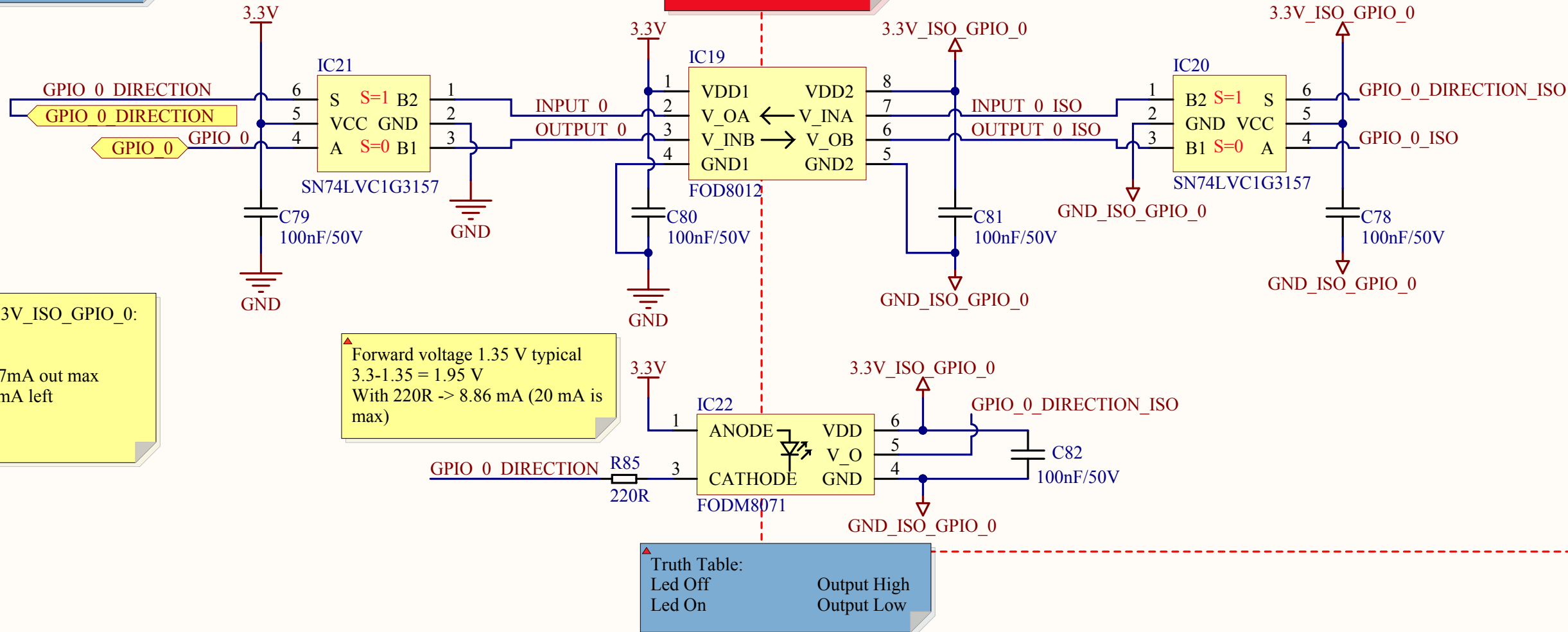
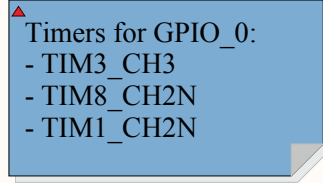
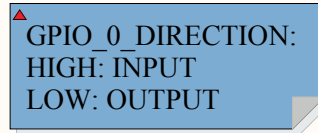
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ADC





Title: ADC		
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Date: 2014-10-13	Time: 19:42:47	Sheet 7 of 8
File: GPIO Schematic.SchDoc		

RS-232 (UART4)

