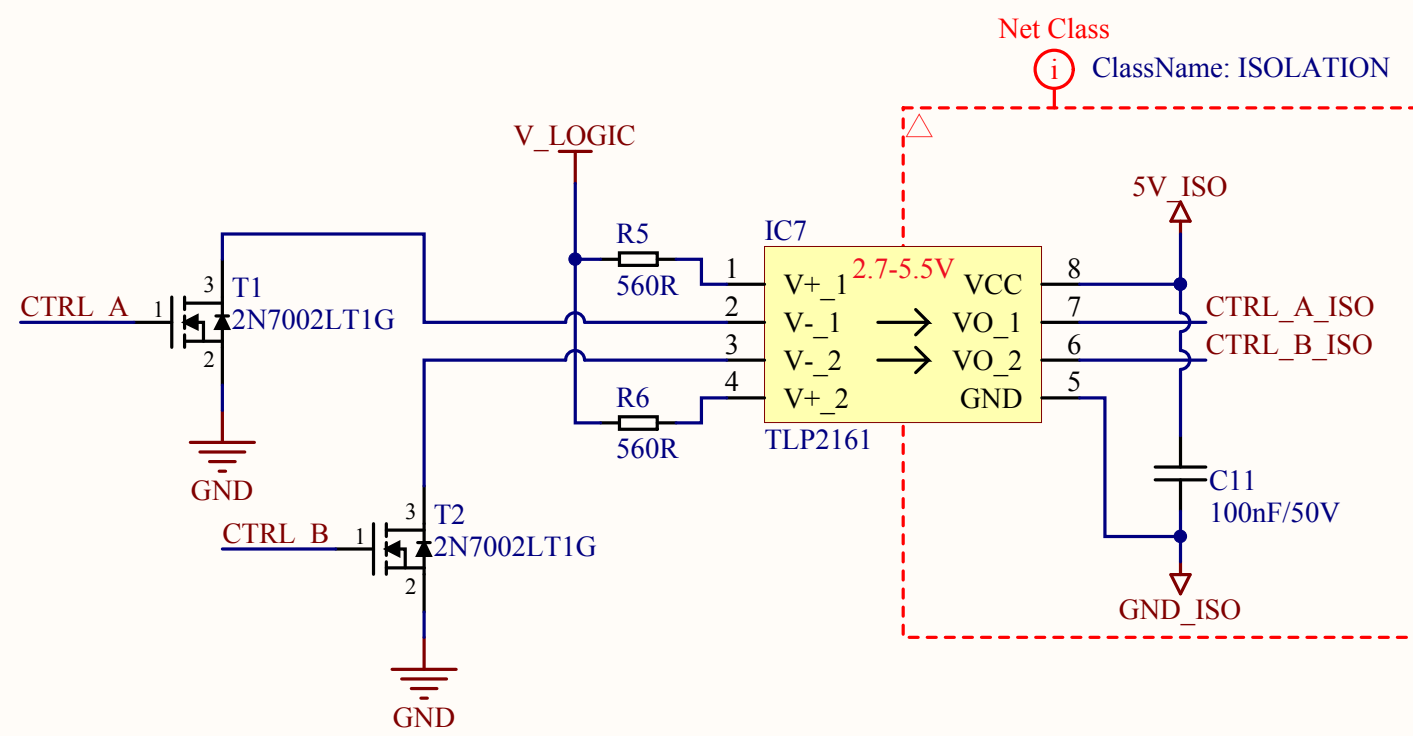
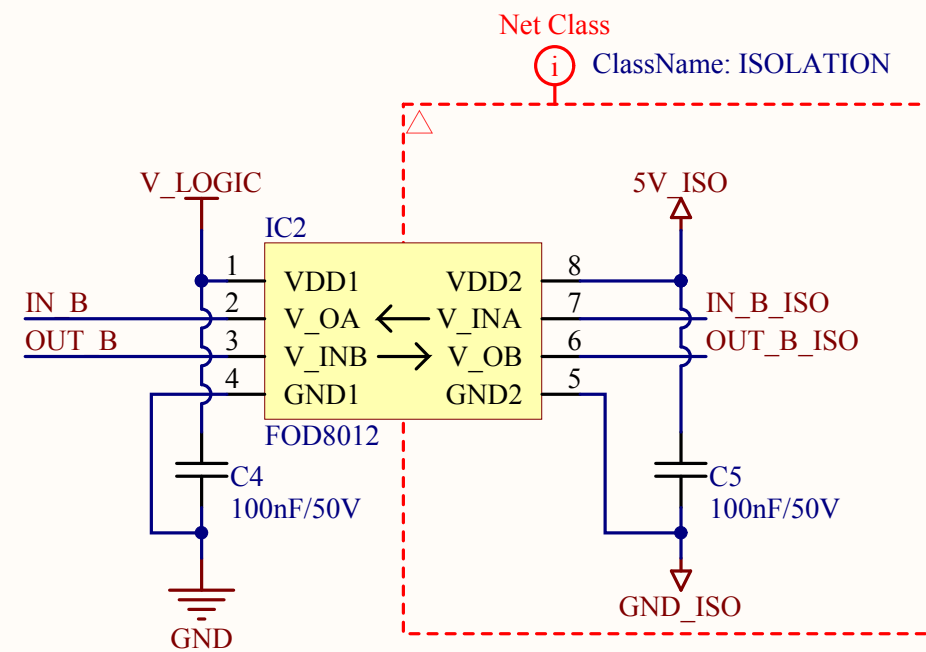
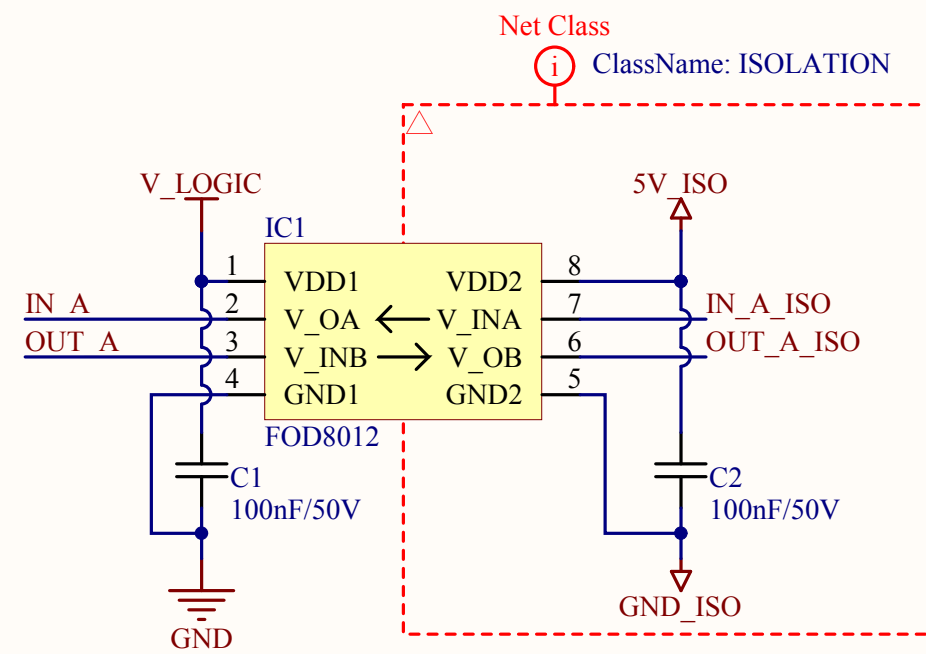


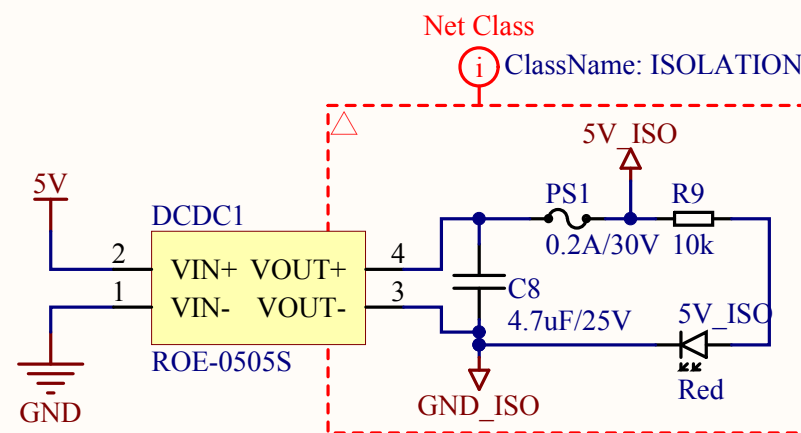
## GPIO



CTRL_A	CTRL_B	Mode
0	0	A in, B In
0	1	A in, B Out
1	0	A out, B in
1	1	A out, B out

▲ TLP2161 V<sub>forward</sub>: 1.5V typ.  
Recommended current: 2-6mA  
Max current: 10mA  
@5V<sub>logic</sub> -> 6.25mA  
@3.3V<sub>logic</sub> -> 3.21mA

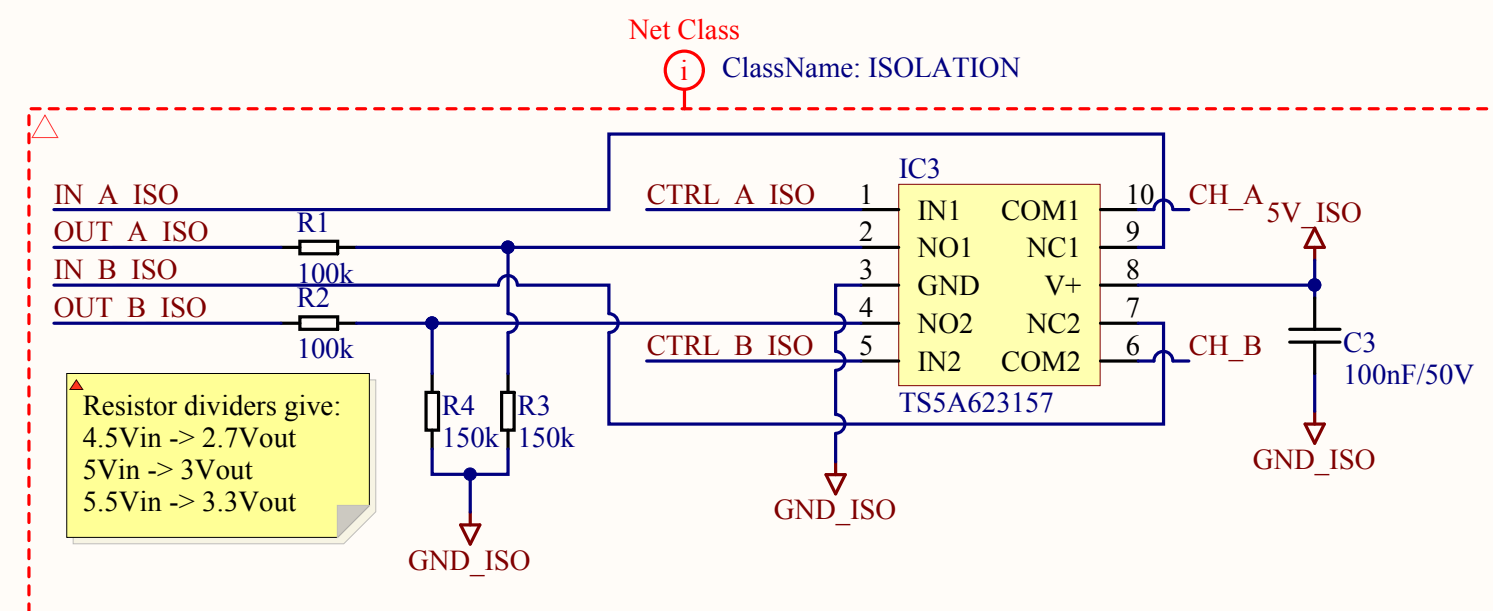
▲ TLP2161:  
LED On -> Output Low  
LED Off -> Output High



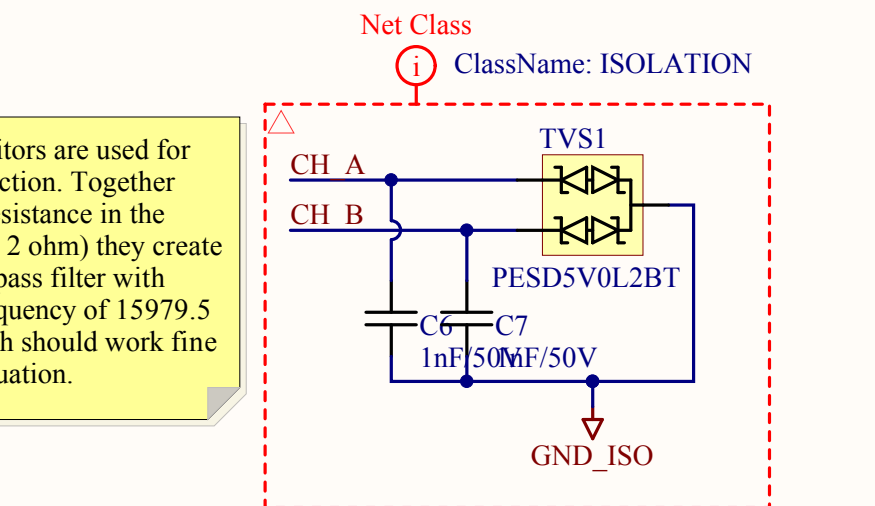
Current Usage 5V\_ISO:  
TLP2160: 5 mA max  
FOD8012: 2x8 mA max = 16 mA  
TS5A623157: 10-150mA ???  
= 168 mA max

ROE-050S5 200mA out max  
-> 200-168 = 32 mA left

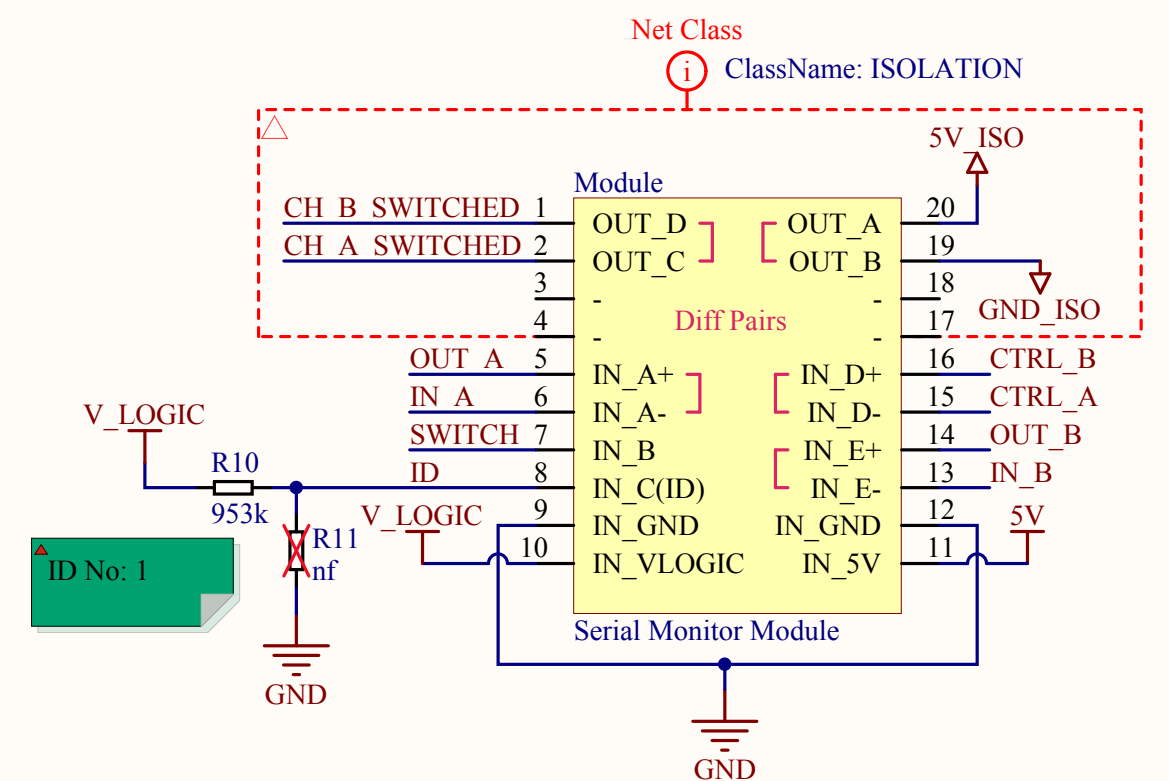
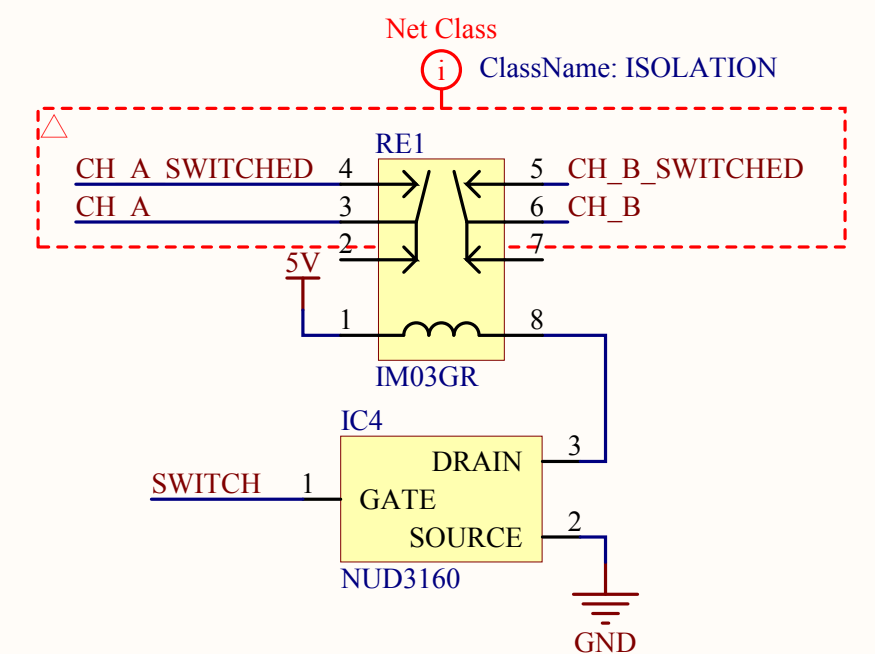
▲ Add load resistor on 5V\_ISO to get better output voltage at standby



▲ Resistor divider on outputs so that they can be compatible with 3.3V logic



▲ The capacitors are used for ESD protection. Together with the resistance in the cable (1 to 2 ohm) they create a RC low pass filter with cut-off frequency of 15979.5 MHz which should work fine for this situation.



Title: <b>GPIO Module</b>		
Size: <b>A3</b>	Number: <b>1</b>	Revision: <b>Rev 1</b>
Date: <b>8/25/2015</b>	Time: <b>11:38:00 AM</b>	Sheet <b>1</b> of <b>1</b>
File: <b>Serial Monitor GPIO Module Schematic.SchDoc</b>		