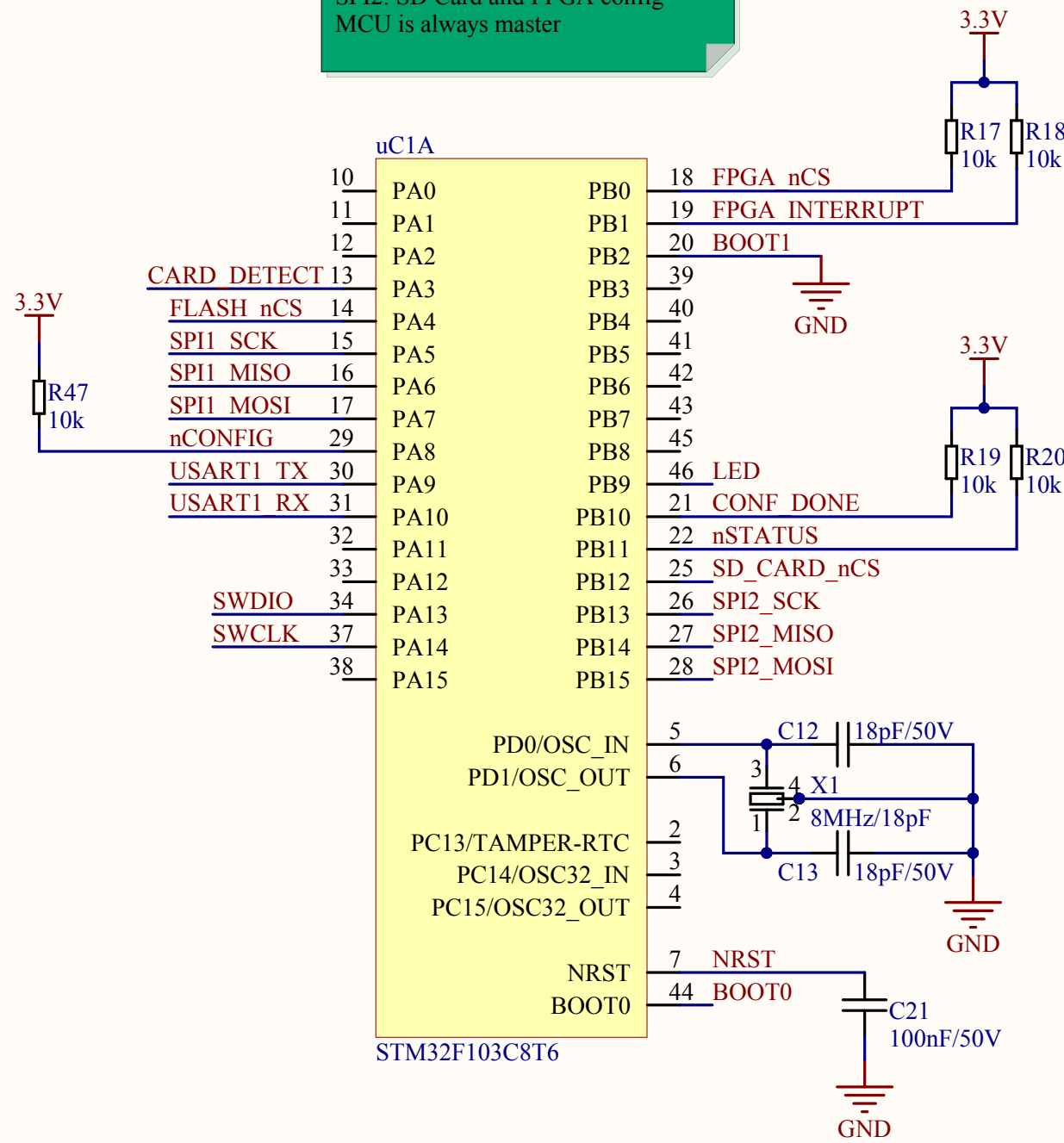
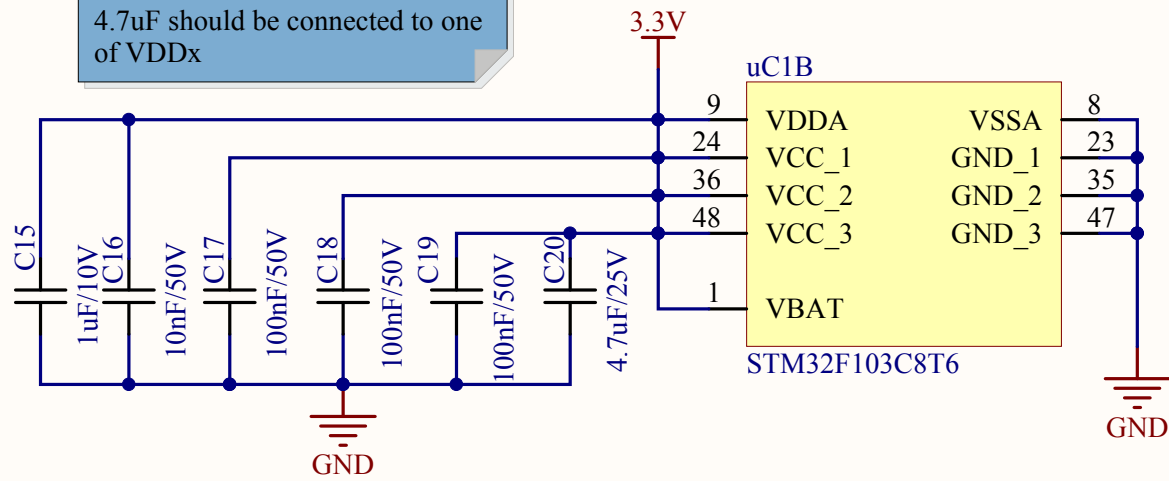


Config MCU

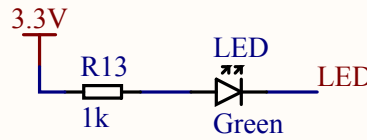
SP11: FLASH and FPGA data
SP12: SD Card and FPGA config
MCU is always master



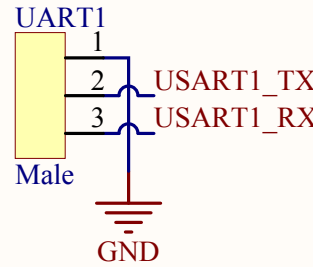
One 100nF for each VDDx.
4.7uF should be connected to one of VDDx



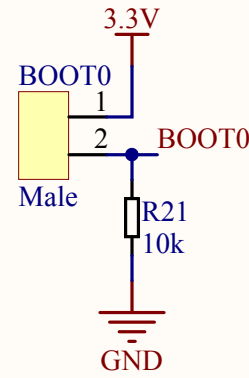
LED



UART1

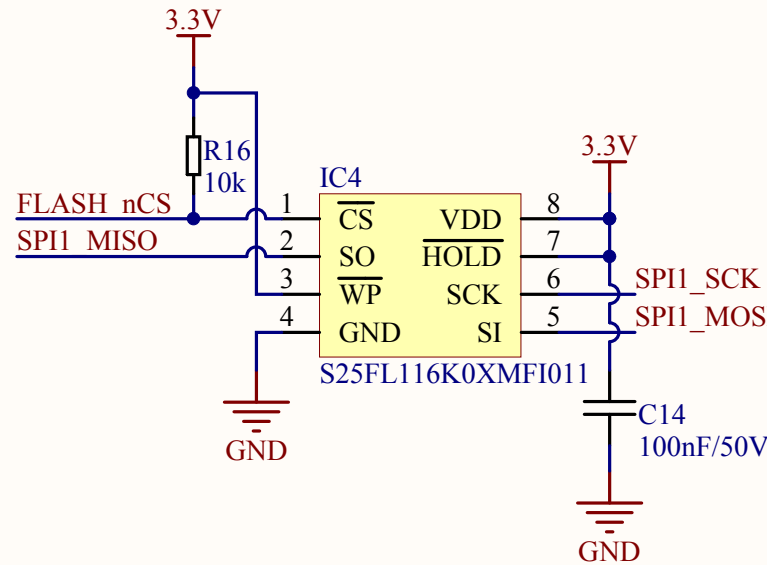


BOOT



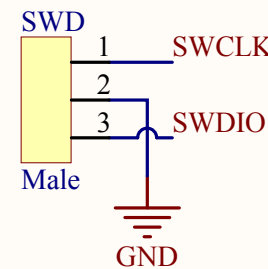
Boot modes:		
BOOT1	BOOT0	Mode
x	0	Flash
0	1	ST Bootloader

FLASH



FPGA (EP4CE6) requires 2 944 088 bits ->
in 16Mbit flash we can fit 5 configuration files

DEBUG



Add 3.3V to SWD connector

MicroSD-Card

