



CONTACT

- **Name:** Nguyễn Phi Hà
- **Email:** haonp1999@gmail.com
- **Phone:** 0877727868
- **Day of birth:** 25/12/1999

SKILL

Microcontroller

- Strong knowledge with Renesas MCUs: RCar S4, RCar V4H, RH850/F1KM, RH850/U2B, ...
- Protocol: UART, CAN, ...
- Peripherals: Port, ADC, CAN, ...

Programming Languages

- Fluency in C/C++ language.

Matlab/Simulink

- Strong knowledge of Matlab/Simulink.

OTHER SKILLS

- Using Git, SVN.
- Microsoft Office.
- Work independently or in groups.
- Analytical ability and creativity.
- Understanding of digital fundamentals

LANGUAGE

- Basic English communication skills.
- Understand English language specialized documents.

ACTIVITIES

- Join the walking program for the poor in Thu Duc District.
- The champion of Ban Vien company's internal football tournament.

NGUYEN PHI HAO

Embedded Software Engineer



PERSONAL PROFILE

I am someone with a strong passion for C programming. With my creativity and innovative ideas, I aspire to apply my accumulated knowledge to contribute to the company's development as an embedded software engineer. Additionally, I aim to gain more work experience and developing my skills.



WORK EXPERIENCE

BanVien Corporation

Dec 2022 – Feb 2024

Embedded Software Engineer – Matlab / Simulink

- As a member of the MBD (Model-Based Design) team, I utilize Matlab/Simulink tools to develop control algorithms and signal processing in automotive embedded systems.
- I have extensive experience with various peripheral devices and protocols such as Port, ADC, TAUD, and CAN on different Renesas MCUs like RCar S4, RCar V4H, RH850/F1KM, RH850/U2B. I possess knowledge of compilers such as Renesas Compiler, ARM compiler, GHS compiler, and debugging techniques.
- I have considerable experience in documentation creation for design and development, including Architectural Design (AD), Unit Design (UD), Code Design (CD), Unit Testing (UT), Integration Testing (IT), and Validation Testing (VT).
- I am proficient in understanding the V-Model and can establish software testing milestones for various stages in the project. Additionally, I evaluate system operations, implement design improvements, and provide progress reports directly to Japanese clients in English.
- I am experienced in working with and understanding models in Model-in-Loop (MIL), Software-in-Loop (SIL), Processor-in-Loop (PIL), and virtual Hardware-in-Loop (vHIL) testing methods.
- I am proficient in using version control systems such as SVN and Git, as well as development tools like Reqtify, Davinci, and Enterprise Architect.
- I have the ability to work independently, creatively, and apply various AI tools to support work, such as ChatGPT and Claude.



EDUCATION

HCMC University of Technology and Education

Major: Electronics and Communications Engineering Technology

GPA: Good.