







# TRƯƠNG NHẬT TÂN

Embedded Software Engineer



## Contact information

 Jan 11, 2000  
 Male  
 0947463826  
 truongnhattanbk2000@gmail.com  
 HoChiMinh, Vietnam  
 <https://www.youtube.com/playlist?list=PLCSDRGLR9b-aJj6NLD86Via0k4So7IHOr>

## Graduation Thesis

Restoring the Robot 6 DOF, using AC Servo to control Robot.  
Using Motion control card PCI-N804, programming MFC interface to control Robot(C++).  
Using HSV color space to detect objects.

## Skills

### Microcontroller

Strong knowledge with Renesas MCUs:  
RH850/F1KM, RH850/U2B, RH850/U2C,...  
Protocol: UART, CAN,...  
Peripherals: OSTM, Port, ADC, PWM,...

### C/C++ Programming

Strong knowledge of C/C++ programming.

### Matlab/Simulink

Strong knowledge of Matlab/Simulink.

## Objective

Aspiring to apply my accumulated knowledge combined with the ability to work independently as well as in a team, in order to contribute to the company's development.  
Gaining more experience working and communication skill.

## Education

**Ho Chi Minh City University of Technology - HCMUT**

*Aug 2018-Apr 2023*

Major: Mechatronics

GPA: 7.2/10

## Work experience

### R&D Intern

*Mar 2021-Oct 2022*

Ngo Ha Gia Limited Liability company

- Research on application of image processing in industrial production.
- Programming library, MFC to control Industrial Robot (C/C++ language).

### Embedded Software Engineer

*Nov 2022-Present*

BanVien Corporation

- Served as a sub-lead in an MBD (Model-Based Design) team using Matlab/Simulink.
- Proficient in understanding the V-Model and capable of setting milestones for various phases within the team.
- Capable of conducting work-related communication in English and providing direct progress reports to Japanese clients.
- Proficient in using various AI tools, including ChatGPT and Claude, for work support.
- Experienced in working with and comprehending Model in the Loop (MIL), Software in the Loop (SIL), and virtual Hardware in the Loop (vHIL) testing methodologies.
- Directly worked with various Renesas MCUs, including RH850/F1KM, U2B, and U2C.
- Extensive experience with various peripherals and protocols, including OSTM, Port, ADC, PWM, CAN, and UART.
- Worked through multiple development phases, including Analysis & Design (AD), Unit Design (UD), Component Design (CD), Unit Testing (UT), Integration Testing (IT), and Validation Testing (VT).
- Strong knowledge of compilers (specifically Renesas compilers) and debugging techniques.
- Proficient in using version control systems such as SVN and Git, as well as tools like Reqtify and Enterprise Architect.

## Certifications

**2021** : Certificate of participation in anual science technology conference of Mechanical science

**2022** : Toeic: 580