

NANDLAND youtube videos

NANDLAND Project Videos 1-10

Nandland Go Board Project 1 - Switches and LEDs
Nandland Go Board Project 2 - The Look-Up Table (LUT)
Nandland Go Board Project 3 - The Flip-Flop (AKA Register)
Nandland Go Board Project 4 - Debounce A Switch
Nandland Go Board Project 5 - Seven Segment Display
Nandland Go Board Project 6 - Simulating LEDs Blinking
Nandland Go Board Project 7 - UART Receiver
Nandland Go Board Project 8 - UART Transmitter (Loopback)
Nandland Go Board Project 9 - Introduction to VGA
Nandland Go Board Project 10 - Pong! (On your VGA Monitor)

<https://www.youtube.com/c/nandland>

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Other Nandland Videos

What is an FPGA? Intro for Beginners
What is a Flip-Flop? How are they used in FPGAs?
What is I2C, Basics for Beginners
What is a Clock in an FPGA?
The Go Board - The First FPGA Development Board You Should Buy
What is SPI? Basics for beginners!
What is a FIFO in an FPGA
Boolean Algebra And LUTs in FPGA
SPI Master and Ambient Light Sensor – Introduction
Introduction to Logic Gates on an FPGA: And, Or, Not, NAND,
What is a Block RAM in an FPGA?
Setup, Hold, Propagation Delay, Timing Errors, Metastability in FPGA
SPI Master in FPGA, Verilog Code Example
How SERDES works in an FPGA, high speed serial TX/RX for beginners
My book Getting Started with FPGAs is NOW AVAILABLE!
FPGA vs. Microcontroller: How to choose the right one for your project
How to fix Timing Errors in your FPGA design during
Place and Route, meeting clock constraints
Live Coding of I2C Core in Verilog, learn FPGAs

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SPI Master in FPGA, Verilog Testbench	https://www.youtube.com/watch?v=aRvbvjyDmag	29
FIRST FPGA Open Source Tools (IceStorm) - Nandland Go Board Tutorial	https://www.youtube.com/watch?v=P-9WCFi0p-0	30
SPI Master with Chip-Select, VHDL Testbench	https://www.youtube.com/watch?v=7o2qojThPuQ	31
What is a Latch in an FPGA?	https://www.youtube.com/watch?v=PHS8qEM3d9w	32
What is a UART in an FPGA? Basics of Serial Ports, COM Port, RS-232, RS-485	https://www.youtube.com/watch?v=sdVPO8rxWS0	33
FPGAs communicating over Bluetooth Demo.		
Send/receive ASCII data, make a transparent UART	https://www.youtube.com/watch?v=-SekDe9MvHo	34
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Shift Register in FPGA - VHDL and Verilog Examples	https://www.youtube.com/watch?v=AEU9bvpzas8	36
SPI Master with Chip-Select in FPGA, Verilog Testbench	https://www.youtube.com/watch?v=Yla_LvePYJ4	37
How to Choose an FPGA for your design	https://www.youtube.com/watch?v=RnvVo8KNFW8	38
Nandland Go Board - Watch This When You Receive Your Board	https://www.youtube.com/watch?v=wWMIY9kjU0	39
VHDL vs. Verilog - Which Language Is Better for FPGA	https://www.youtube.com/watch?v=uvqTRhAWi58	40
SPI Master in FPGA, VHDL Code Example	https://www.youtube.com/watch?v=m1hFeAqQ-H8	41
Icestudio - Open source GUI for FPGA design and programming, visual drag-and-drop, no code required!	https://www.youtube.com/watch?v=pZWFmupcMTs	42
Example Interview Questions for a job in FPGA, VHDL, Verilog	https://www.youtube.com/watch?v=9U9R4lxlACs	43
iCEcube2 Installation and Overview	https://www.youtube.com/watch?v=nfB8-8JfVFE	44
FPGA Job Hunt - Jobs for people working with VHDL, Verilog, FPGA, ASIC. linkedin job hunt.	https://www.youtube.com/watch?v=mwyj5jKAh8U	45
SPI Master Demo - Ambient Light Sensor Project	https://www.youtube.com/watch?v=Uq-dVdUSJdc	46
How to read an ADC using an FPGA (halverscience)	https://www.youtube.com/watch?v=zSz09-Q3Urc	47
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SPI Master with Chip-Select in FPGA, Verilog Code Example	https://www.youtube.com/watch?v=VoGgkNcS880	49
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How to Get A Job as An Engineer - The Resume	https://www.youtube.com/watch?v=ElvU4-MYFaE	51
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My book Getting Started with FPGAs is NOW AVAILABLE!	https://www.youtube.com/watch?v=4UpMB9kNt0s&t=15s	55
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An Overview of the youtube videos about the Nandland project from the Nandland website.

To be amended when there is time

I kindly received a GO BOARD from Russell, and I am now in the process to work through the data.

Juergen Pintaske, ExMark April 2025