# 16006 E2x/AIR

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# **Namespace Index**

## **Namespace List**

# **Hierarchical Index**

# Class Hierarchy

This inheritance list is sorted roughly, but not completely, alphab	etically:
Cair_core::air_mux_table	11
reg_super	
Cair_core_regif	55
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Cair_core_regif::RegCBstr	95
sc_module	
Cair	13
Cair_core_regif::SRegList	97
tlm_tgt_if	
Cair	13

# **Class Index**

## **Class List**

Here are the classes, structs, unions and interfaces with brief descriptions:

Cair core::air mux table	11
Cair (AIR model class )	
Cair_core (AIR_CORE class )	
Cair_core_regif (Register IF class of AIR_CORE model )	55
Cair_core_regif::RegCBstr	
Cair_core_regif::SRegList	97

# **File Index**

## **File List**

Here is a list of all files with brief descriptions:

air.cpp	100
<u>air.h</u>	
air_cmdif.h	
air_core.cpp	
air_core.h	
air_core_regif.cpp (Register IF class of model AIR_CORE \$Id\$ \$Date\$ \$Revison\$	
air core regif.h (Register IF class of model AIR_CORE \$Id\$ \$Date\$ \$Revison\$ \$A	
PY_AIR.cpp	112
PY AIR.h	113

## **Namespace Documentation**

## **PY\_AIR Namespace Reference**

#### **Functions**

- void SetPvExtCmd (void)
- void <u>SeparateString</u> (std::vector< std::string > &vtr, const std::string msg)
- void <a href="ProcessCommand">ProcessCommand</a> (const std::string cmd\_id, const std::string cmd\_name, char \*token, char \*input\_arg)
- PvObject \* DumpStatInfoPy (PvObject \*self, PvObject \*args)
- PyObject \* <u>SetCLKfreqPy</u> (PyObject \*self, PyObject \*args)
- PyObject \* tgtPy (PyObject \*self, PyObject \*args)
- PyObject \* regPy (PyObject \*self, PyObject \*args)
- PyObject \* <u>MessageLevelPy</u> (PyObject \*self, PyObject \*args)
- PyObject \* <a href="helpPy">helpPy</a> (PyObject \*self, PyObject \*args)
- PyObject \* AssertResetPy (PyObject \*self, PyObject \*args)

#### **Variables**

• PyMethodDef <u>mShApiDef</u> []

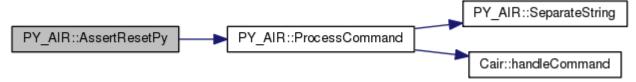
#### **Function Documentation**

## static PyObject \* PY\_AIR::AssertResetPy (PyObject \* self, PyObject \* args)

Definition at line 143 of file PY AIR.cpp.

References ProcessCommand().

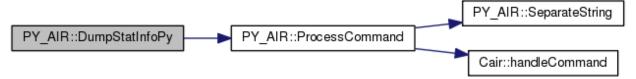
Here is the call graph for this function:



## static PyObject \* PY\_AIR::DumpStatInfoPy (PyObject \* self, PyObject \* args)

Definition at line 67 of file PY\_AIR.cpp.

References ProcessCommand().



## static PyObject \* PY\_AIR::helpPy (PyObject \* self, PyObject \* args)

Definition at line 131 of file PY\_AIR.cpp.

References ProcessCommand().

Here is the call graph for this function:



## static PyObject \* PY\_AIR::MessageLevelPy (PyObject \* self, PyObject \* args)

Definition at line 118 of file PY\_AIR.cpp.

References ProcessCommand().

Here is the call graph for this function:



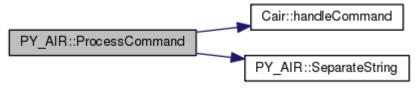
# void PY\_AIR::ProcessCommand (const std::string cmd\_id, const std::string cmd\_name, char \* token, char \* input\_arg)

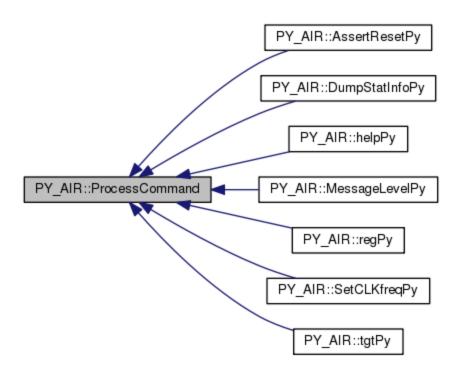
Definition at line 47 of file PY\_AIR.cpp.

References Cair::handleCommand(), and SeparateString().

 $Referenced\ by\ AssertResetPy(),\ DumpStatInfoPy(),\ helpPy(),\ MessageLevelPy(),\ regPy(),\ SetCLKfreqPy(),\ and\ tgtPy().$ 

Here is the call graph for this function:





## static PyObject \* PY\_AIR::regPy (PyObject \* self, PyObject \* args)

Definition at line 105 of file PY\_AIR.cpp.

References ProcessCommand().

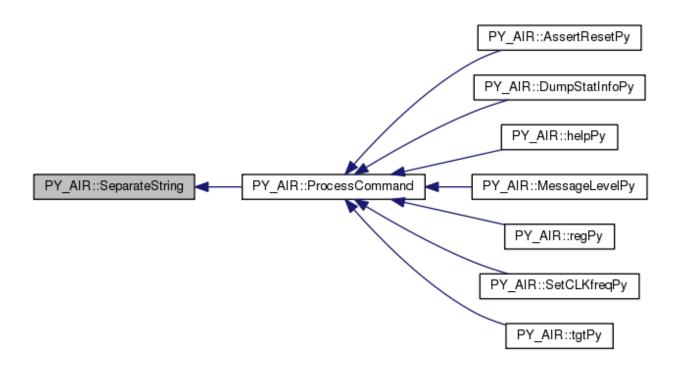
Here is the call graph for this function:



## void PY\_AIR::SeparateString (std::vector< std::string > & vtr, const std::string msg)

Definition at line 32 of file PY\_AIR.cpp.

Referenced by ProcessCommand().



## static PyObject \* PY\_AIR::SetCLKfreqPy (PyObject \* self, PyObject \* args)

Definition at line 79 of file PY\_AIR.cpp.

References ProcessCommand().

Here is the call graph for this function:



## void PY\_AIR::SetPyExtCmd (void )

Definition at line 27 of file PY\_AIR.cpp.

References mShApiDef, and PY\_INITMODULE\_NAME.

## static PyObject \* PY\_AIR::tgtPy (PyObject \* self, PyObject \* args)

Definition at line 92 of file PY\_AIR.cpp.

References ProcessCommand().



## **Variable Documentation**

## PyMethodDef PY\_AIR::mShApiDef[]

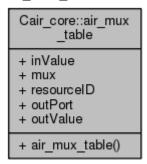
Definition at line 16 of file PY\_AIR.cpp.

Referenced by SetPyExtCmd().

## **Class Documentation**

## Cair\_core::air\_mux\_table Struct Reference

Collaboration diagram for Cair\_core::air\_mux\_table:



#### **Public Member Functions**

• <u>air\_mux\_table</u> (bool <u>inValue</u>, vpcl::bit\_info \*<u>mux</u>, unsigned int id, unsigned int <u>outPort</u>, bool <u>outValue</u>)

#### **Public Attributes**

- bool inValue
- vpcl::bit\_info \* mux
- unsigned int <u>resourceID</u>
- unsigned int outPort
- bool outValue

## **Detailed Description**

Definition at line 43 of file air\_core.h.

#### **Constructor & Destructor Documentation**

Cair\_core::air\_mux\_table::air\_mux\_table (bool inValue, vpcl::bit\_info \* mux, unsigned int id, unsigned int outPort, bool outValue)[inline]

Definition at line 50 of file air\_core.h.

References in Value, mux, outPort, outValue, and resourceID.

## **Member Data Documentation**

bool Cair\_core::air\_mux\_table::inValue

Definition at line 44 of file air\_core.h.

Referenced by air\_mux\_table().

## vpcl::bit\_info\* Cair\_core::air\_mux\_table::mux

Definition at line 45 of file air\_core.h.

Referenced by air\_mux\_table().

## unsigned int Cair\_core::air\_mux\_table::outPort

Definition at line 47 of file air\_core.h.

Referenced by air\_mux\_table().

## bool Cair\_core::air\_mux\_table::outValue

Definition at line 48 of file air\_core.h.

Referenced by air\_mux\_table().

## unsigned int Cair\_core::air\_mux\_table::resourceID

Definition at line 46 of file air\_core.h.

Referenced by air\_mux\_table().

## The documentation for this struct was generated from the following file:

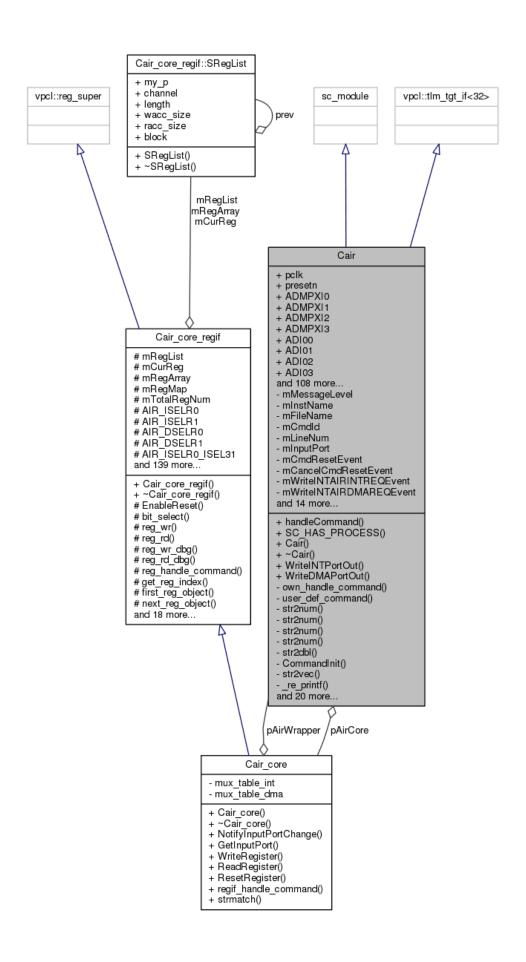
• air\_core.h

# **Cair Class Reference**

AIR model class. #include <air.h> Inheritance diagram for Cair:



Collaboration diagram for Cair:



## **Public Member Functions**

- std::string <u>handleCommand</u> (const std::vector< std::string > &args)
- SC\_HAS\_PROCESS (Cair)
- <u>Cair</u> (sc\_module\_name name)
- ~Cair ()
- void <u>WriteINTPortOut</u> (unsigned int index, bool is\_active)
- void <u>WriteDMAPortOut</u> (unsigned int index, bool is\_active)

#### **Public Attributes**

- sc\_in< sc\_dt::uint64 > pclk
- sc\_in< bool > <u>presetn</u>
- sc\_in< bool > <u>ADMPXI0</u>
- sc\_in< bool > <u>ADMPXI1</u>
- sc\_in< bool > ADMPXI2
- sc\_in< bool > <u>ADMPXI3</u>
- $sc_in < bool > ADI00$
- $sc_in < bool > ADI01$
- $sc_in < bool > ADI02$
- $sc_in < bool > ADIO3$
- $sc_in < bool > ADIO4$
- $sc_in < bool > ADI10$
- $sc_in < bool > ADI11$
- $sc_in < bool > ADI12$
- $sc_in < bool > ADI13$
- $sc_in < bool > ADI14$
- $sc_in < bool > ADI20$
- $sc_in < bool > ADI21$
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- $sc_in < bool > ADI23$
- $sc_in < bool > ADI24$
- sc in < bool > ADI30
- $sc_in < bool > ADI31$
- $sc_in < bool > ADI32$
- sc\_in< bool > <u>ADI33</u>
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- sc\_in< bool > <u>ADE1</u>
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- sc\_in< bool > DSADE00
- sc in < bool > DSADE10
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- sc in<br/>
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- $sc_in < bool > DSADI12$

- sc in < bool > DSADI13
- $sc_in < bool > CADE00$
- sc\_in< bool > CADI00
- $sc_in < bool > ASI0$
- $sc_in < bool > ASI1$
- sc in < bool > ASI2
- sc\_in< bool > ASI3
- $sc_in < bool > ASI4$
- $sc_in < bool > ASI5$
- sc\_in< bool > ASI6
- sc in < bool > ASI7
- $sc_in < bool > ASI8$
- sc\_in< bool > ASI9
- sc\_in< bool > ASI10
- sc in < bool > ASI11
- sc\_in< bool > ASI12
- sc\_in< bool > ASI13
- $sc_in < bool > ASI14$
- sc in < bool > ASI15
- $sc_in < bool > BFP0$
- $sc_in < bool > BFP1$
- $sc_in < bool > BFP2$
- $sc_in < bool > BFP3$
- sc in < bool > BFP4
- sc in < bool > BFP5
- $sc_in < bool > BFP6$
- $sc_in < bool > BFP7$
- sc in < bool > BFP8  $sc_in < bool > BFP9$
- $sc_in < bool > BFP10$
- $sc_in < bool > BFP11$
- $sc_in < bool > BFP12$ sc in < bool > BFP13
- $sc_in < bool > BFP14$
- $sc_in < bool > BFP15$  $sc_in < bool > BFP16$
- $sc_in < bool > BFP17$
- sc in < bool > BFP18
- $sc_in < bool > BFP19$
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- sc\_in< bool > BFP21
- $sc_in < bool > BFP22$
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- $sc_in < bool > BFP25$
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- $sc_in < bool > BFP34$
- sc\_in< bool > <u>BFP35</u>
- sc in<br/>bool > BFP36
- sc in < bool > BFP37
- $sc_in < bool > BFP38$
- sc in < bool > BFP39
- sc\_in< bool > DSADE14
- sc in<br/>
  sc ool > DSADE15
- $sc_in < bool > DSADE21$
- sc\_in< bool > DSADE22
- sc in < bool > DSADI14
- $sc_in < bool > DSADI15$
- sc\_in< bool > DSADI21
- $sc_{in} < bool > \overline{DSADI22}$
- sc in < bool > BFP40
- sc in < bool > BFP41
- sc\_in< bool > BFP42
- $sc_in < bool > BFP43$
- sc in < bool > BFP44
- $sc_in < bool > BFP45$
- sc\_in< bool > <u>BFP46</u>
- sc in < bool > BFP47
- sc\_out< bool > <u>INTAIRINTREQ</u> [<u>emNumOfOutputPort</u>]
- sc\_out< bool > <u>INTAIRDMAREQ</u> [<u>emNumOfOutputPort</u>]

## **Private Types**

- enum <u>eAirConstant</u> { <u>emNumOfOutputPort</u> = 64, <u>emNumOfInputPort</u> = 114 }
- enum eAirState { emResetState = 0, emOperateState = 1 }
- enum eTimeUnit { emNanoSecond = 10000000000 }

#### **Private Member Functions**

- std::string <u>own\_handle\_command</u> (std::vector< std::string > &args)
- std::string <u>user\_def\_command</u> (std::vector< std::string > &args)
- bool <u>str2num</u> (std::string str, bool &num)
- bool <u>str2num</u> (std::string str, char &num)
- bool <u>str2num</u> (std::string str, unsigned char &num)
- template<typename T > bool <u>str2num</u> (std::string str, T &num)
- bool <u>str2dbl</u> (std::string str, double &num)
- void CommandInit (std::string name="")
- std::vector< std::string > <u>str2vec</u> (std::string str, const char sep)
- void <u>re printf</u> (std::string group, const char \*message,...)
- void get\_fileline (std::string filename, int line\_number)
- bool strmatch (const char \*ptn, const char \*str)
- void tgt acc (unsigned int id, tlm::tlm generic payload &trans, sc time &t)
- void tgt acc (tlm::tlm\_generic\_payload &trans, sc\_time &t)
- unsigned int tgt acc dbg (unsigned int id, tlm::tlm\_generic\_payload &trans)
- unsigned int tgt\_acc\_dbg (tlm::tlm\_generic\_payload &trans)
- void <u>Initialize</u> (void)
- double <u>GetTimeResolution</u> (void)
- void <u>SetLatency TLM</u> (const double pclk\_period, const bool is\_constructor)
- void <u>SetCLKfreq</u> (std::string clk\_name, double clk\_freq)
- void <u>AssertReset</u> (double delay, double period)

- void EnableReset (const bool is active)
- void <u>WriteINTAIRDMAREQMethod</u> (void)
- void WriteINTAIRINTREQMethod (void)
- void <u>ReceiveInputMethod</u> (const unsigned int i)
- void <u>PCLKPeriodMethod</u> (void)
- void <u>PortResetMethod</u> (void)
- void CmdResetMethod (void)
- void CancelCmdResetMethod (void)
- void <u>DumpStatInfo</u> (void)

#### **Private Attributes**

- std::map< std::string, bool > mMessageLevel
- std::string <u>mInstName</u>
- std::string <u>mFileName</u>
- std::string <u>mCmdId</u>
- int mLineNum
- sc\_in< bool > mInputPort [emNumOfInputPort]
- <u>Cair\_core</u> \* <u>pAirCore</u>
- sc\_event mCmdResetEvent
- sc\_event mCancelCmdResetEvent
- sc\_event <u>mWriteINTAIRINTREQEvent</u>
- sc\_event <u>mWriteINTAIRDMAREQEvent</u>
- bool <u>mPortReset</u>
- bool mCmdReset
- bool mIsCmdResetStatus
- bool mIsInitialize
- unsigned int <u>mState</u>
- double <u>mPCLKPeriod</u>
- double mResetPeriod
- bool <u>mINTOut</u> [emNumOfOutputPort]
- bool mDMAOut [emNumOfOutputPort]
- bool mPreINTOut [emNumOfOutputPort]
- bool <u>mPreDMAOut</u> [<u>emNumOfOutputPort</u>]
   std::string mInputPortName [emNumOfInputPort]
- unsigned int mNumINTReq [emNumOfOutputPort]
- unsigned int <u>mNumDMAReq</u> [emNumOfOutputPort]

## **Detailed Description**

AIR model class.

Definition at line 21 of file air.h.

#### **Member Enumeration Documentation**

enum Cair::eAirConstant[private]

**Enumerator:** 

emNumOfOutputPort

### emNumOfInputPort

Definition at line 26 of file air.h.

#### enum Cair::eAirState [private]

**Enumerator:** 

emResetState emOperateState

Definition at line 30 of file air.h.

#### enum <u>Cair::eTimeUnit</u>[private]

**Enumerator:** 

emNanoSecond

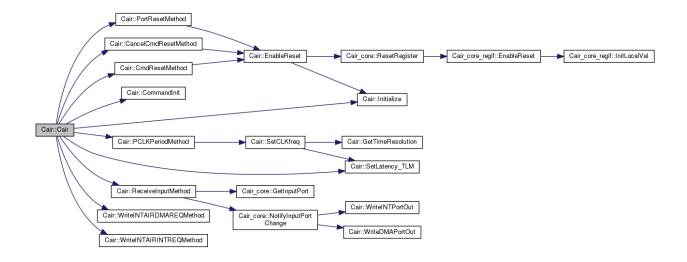
Definition at line 34 of file air.h.

#### **Constructor & Destructor Documentation**

Cair::Cair (sc\_module\_name name)

Definition at line 18 of file air.cpp.

References ADE0, ADE1, ADE2, ADE3, ADI00, ADI01, ADI02, ADI03, ADI04, ADI10, ADI11, ADI12, ADI13, ADI14, ADI20, ADI21, ADI22, ADI23, ADI24, ADI30, ADI31, ADI32, ADI33, ADI34, ADMPXI0, ADMPXI1, ADMPXI2, ADMPXI3, ASI0, ASI1, ASI10, ASI11, ASI12, ASI13, ASI14, ASI15, ASI2, ASI3, ASI4, ASI5, ASI6, ASI7, ASI8, ASI9, BFP0, BFP1, BFP10, BFP11, BFP12, BFP13, BFP14, BFP15, BFP16, BFP17, BFP18, BFP19, BFP2, BFP20, BFP21, BFP22, BFP23, BFP24, BFP25, BFP26, BFP27, BFP28, BFP29, BFP30, BFP31, BFP32, BFP33, BFP34, BFP35, BFP36, BFP37, BFP38, BFP39, BFP40, BFP40, BFP41, BFP42, BFP43, BFP44, BFP45, BFP46, BFP47, BFP5, BFP6, BFP7, BFP8, BFP9, CADE00, CADI00, CancelCmdResetMethod(), CmdResetMethod(), CommandInit(), DSADE00, DSADE10, DSADE11, DSADE12, DSADE13, DSADE14, DSADE15, DSADE20, DSADE21, DSADE22, DSADI00, DSADI10, DSADI11, DSADI12, DSADI13, DSADI14, DSADI15, DSADI20, DSADI21, DSADI22, emNumOfInputPort, emNumOfOutputPort, emOperateState, Initialize(), INTAIRDMAREQ, INTAIRINTREO. mCancelCmdResetEvent, mCmdReset, mCmdResetEvent, mInputPort, mInputPortName, mIsCmdResetStatus, mIsInitialize, mPCLKPeriod, mPortReset, mResetPeriod, mState, mWriteINTAIRDMAREQEvent, mWriteINTAIRINTREQEvent, pAirCore, pclk, PCLKPeriodMethod(), PortResetMethod(), WriteINTAIRDMAREQMethod(), ReceiveInputMethod(), SetLatency TLM(), WriteINTAIRINTREOMethod().



## Cair::~Cair ()

Definition at line 422 of file air.cpp.

References pAirCore.

## **Member Function Documentation**

void Cair::\_re\_printf (std::string group, const char \* message, ...)[inline], [private]

Definition at line 745 of file air.h.

### void Cair::AssertReset (double delay, double period)[private]

Definition at line 618 of file air.cpp.

References mCmdResetEvent, mIsCmdResetStatus, mPortReset, mResetPeriod, and re\_printf.

## void Cair::CancelCmdResetMethod (void)[private]

Definition at line 639 of file air.cpp.

References EnableReset(), mCmdReset, and mIsCmdResetStatus.

Referenced by Cair().

Here is the call graph for this function:





## void Cair::CmdResetMethod (void ) [private]

Definition at line 632 of file air.cpp.

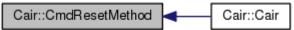
References EnableReset(), mCancelCmdResetEvent, mCmdReset, and mResetPeriod.

Referenced by Cair().

Here is the call graph for this function:



Here is the caller graph for this function:



## void Cair::CommandInit (std::string name = "")[inline], [private]

Definition at line 695 of file air.h.

Referenced by Cair().

Here is the caller graph for this function:



#### void Cair::DumpStatInfo (void ) [private]

Definition at line 439 of file air.cpp.

References emNumOfOutputPort, mNumDMAReq, and mNumINTReq.

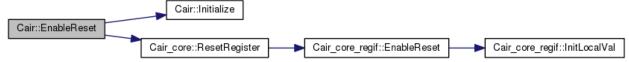
## void Cair::EnableReset (const bool is\_active) [private]

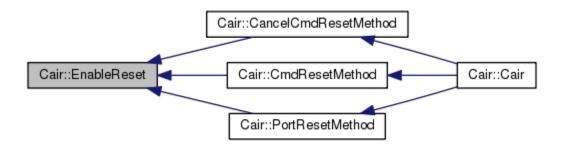
Definition at line 534 of file air.cpp.

References emOperateState, emResetState, Initialize(), mState, mWriteINTAIRDMAREQEvent, mWriteINTAIRINTREQEvent, pAirCore, and Cair\_core::ResetRegister().

Referenced by CancelCmdResetMethod(), CmdResetMethod(), and PortResetMethod().

Here is the call graph for this function:





## void Cair::get\_fileline (std::string filename, int line\_number)[inline], [private]

Definition at line 879 of file air.h.

## double Cair::GetTimeResolution (void )[private]

Definition at line 481 of file air.cpp.

Referenced by SetCLKfreq().

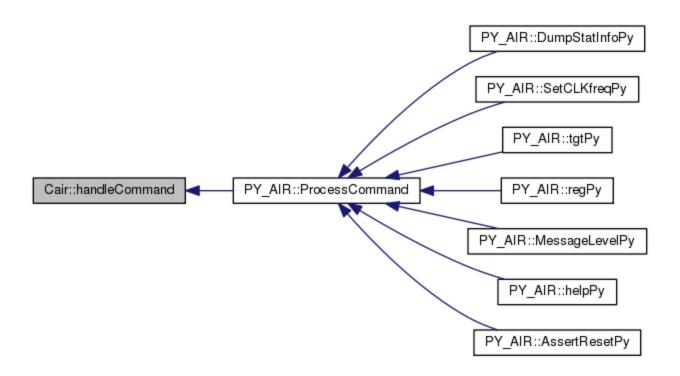
Here is the caller graph for this function:



## std::string Cair::handleCommand (const std::vector< std::string > & args)[inline]

Definition at line 45 of file air.h.

Referenced by PY\_AIR::ProcessCommand().



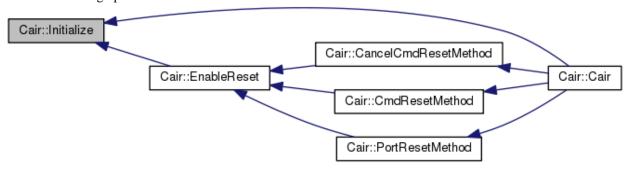
## void Cair::Initialize (void )[private]

Definition at line 427 of file air.cpp.

 $References\ emNumOfOutputPort,\ mDMAOut,\ mINTOut,\ mNumDMAReq,\ mNumINTReq,\ mPreDMAOut,\ and\ mPreINTOut.$ 

Referenced by Cair(), and EnableReset().

Here is the caller graph for this function:



std::string Cair::own\_handle\_command (std::vector< std::string > & args)[inline], [private]

Definition at line 162 of file air.h.

## void Cair::PCLKPeriodMethod (void )[private]

Definition at line 453 of file air.cpp.

References pclk, and SetCLKfreq().

Referenced by Cair().

Here is the call graph for this function:



Here is the caller graph for this function:



#### void Cair::PortResetMethod (void ) [private]

Definition at line 500 of file air.cpp.

 $References \ EnableReset(), \ mCancelCmdResetEvent, \ mCmdResetEvent, \ mCmdResetEvent, \ mIsCmdResetStatus, \\ mIsInitialize, \ mPortReset, \ presetn, \ and \ re\_printf.$ 

Referenced by Cair().

Here is the call graph for this function:



Here is the caller graph for this function:

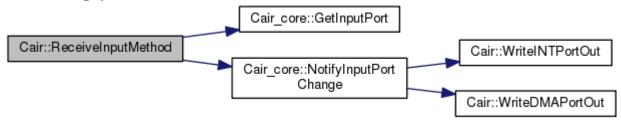


## void Cair::ReceiveInputMethod (const unsigned int i)[private]

Definition at line 647 of file air.cpp.

References emOperateState, Cair\_core::GetInputPort(), mInputPort, mInputPortName, mPCLKPeriod, mState, mWriteINTAIRDMAREQEvent, mWriteINTAIRINTREQEvent, Cair\_core::NotifyInputPortChange(), pAirCore, and re\_printf.

Referenced by Cair().



Here is the caller graph for this function:



## Cair::SC\_HAS\_PROCESS (Cair)

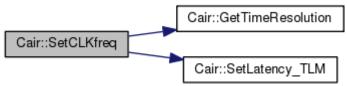
#### void Cair::SetCLKfreq (std::string clk\_name, double clk\_freq)[private]

Definition at line 458 of file air.cpp.

References emNanoSecond, GetTimeResolution(), mPCLKPeriod, mWriteINTAIRDMAREQEvent, mWriteINTAIRINTREQEvent, re\_printf, and SetLatency\_TLM().

Referenced by PCLKPeriodMethod().

Here is the call graph for this function:



Here is the caller graph for this function:



## void Cair::SetLatency\_TLM (const double pclk\_period, const bool is\_constructor)[private]

Definition at line 704 of file air.cpp.

Referenced by Cair(), and SetCLKfreq().

Here is the caller graph for this function:



#### bool Cair::str2dbl (std::string str, double & num)[inline], [private]

Definition at line 679 of file air.h.

bool Cair::str2num (std::string str, bool & num)[inline], [private]

Definition at line 572 of file air.h.

bool Cair::str2num (std::string str, char & num)[inline], [private]

Definition at line 606 of file air.h.

## bool Cair::str2num (std::string str, unsigned char & num)[inline], [private]

Definition at line 630 of file air.h.

#### template<typename T > bool Cair::str2num (std::string str, T & num)[inline], [private]

Definition at line 655 of file air.h.

#### std::vector<std::string> Cair::str2vec (std::string str, const char sep)[inline], [private]

Definition at line 726 of file air.h.

#### bool Cair::strmatch (const char \* ptn, const char \* str)[inline], [private]

Definition at line 893 of file air.h.

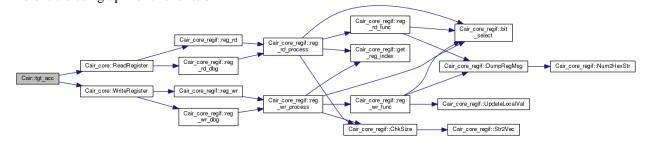
## void Cair::tgt\_acc (unsigned int id, tlm::tlm\_generic\_payload & trans, sc\_time & t)[private]

Definition at line 547 of file air.cpp.

#### void Cair::tgt\_acc (tlm::tlm\_generic\_payload & trans, sc\_time & t)[private]

Definition at line 553 of file air.cpp.

References pAirCore, Cair\_core::ReadRegister(), and Cair\_core::WriteRegister(). Here is the call graph for this function:



#### unsigned int Cair::tgt\_acc\_dbg (unsigned int id, tlm::tlm\_generic\_payload & trans) [private]

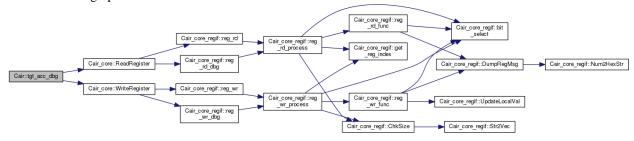
Definition at line 581 of file air.cpp.

#### unsigned int Cair::tgt\_acc\_dbg (tlm::tlm\_generic\_payload & trans)[private]

Definition at line 587 of file air.cpp.

References pAirCore, Cair\_core::ReadRegister(), and Cair\_core::WriteRegister().

Here is the call graph for this function:



#### std::string Cair::user\_def\_command (std::vector < std::string > & args)[inline], [private]

Definition at line 419 of file air.h.

#### void Cair::WriteDMAPortOut (unsigned int index, bool is\_active)

Definition at line 695 of file air.cpp.

References emNumOfOutputPort, mDMAOut, and mNumDMAReq.

Referenced by Cair\_core::NotifyInputPortChange().

Here is the caller graph for this function:



#### void Cair::WriteINTAIRDMAREQMethod (void ) [private]

Definition at line 663 of file air.cpp.

References emNumOfOutputPort, INTAIRDMAREQ, mDMAOut, mPreDMAOut, and re\_printf.

Referenced by Cair().

Here is the caller graph for this function:



#### void Cair::WriteINTAIRINTREQMethod (void ) [private]

Definition at line 675 of file air.cpp.

References emNumOfOutputPort, INTAIRINTREQ, mINTOut, mPreINTOut, and re\_printf.

Referenced by Cair().



## void Cair::WriteINTPortOut (unsigned int index, bool is\_active)

Definition at line 686 of file air.cpp.

References emNumOfOutputPort, mINTOut, and mNumINTReq.

Referenced by Cair\_core::NotifyInputPortChange().

Here is the caller graph for this function:



### **Member Data Documentation**

## sc\_in<bool> Cair::ADE0

Definition at line 112 of file air.h. Referenced by Cair().

#### sc\_in<bool> Cair::ADE1

Definition at line 113 of file air.h. Referenced by Cair().

#### sc\_in<bool> Cair::ADE2

Definition at line 114 of file air.h. Referenced by Cair().

#### sc\_in<bool> Cair::ADE3

Definition at line 115 of file air.h. Referenced by Cair().

## sc\_in<bool> Cair::ADI00

Definition at line 92 of file air.h. Referenced by Cair().

## sc\_in<bool> Cair::ADI01

Definition at line 93 of file air.h. Referenced by Cair().

## sc\_in<bool> Cair::ADI02

Definition at line 94 of file air.h. Referenced by Cair().

## sc\_in<bool> Cair::ADI03

Definition at line 95 of file air.h. Referenced by Cair().

## sc\_in<bool> Cair::ADI04

Definition at line 96 of file air.h. Referenced by Cair().

## sc\_in<bool> Cair::ADI10

Definition at line 97 of file air.h. Referenced by Cair().

### sc\_in<bool> Cair::ADI11

Definition at line 98 of file air.h. Referenced by Cair().

## sc\_in<bool> Cair::ADI12

Definition at line 99 of file air.h. Referenced by Cair().

## sc\_in<bool> Cair::ADI13

Definition at line 100 of file air.h. Referenced by Cair().

## sc\_in<bool> Cair::ADI14

Definition at line 101 of file air.h. Referenced by Cair().

#### sc\_in<br/>cool> Cair::ADI20

Definition at line 102 of file air.h.

Referenced by Cair().

## sc\_in<bool> Cair::ADI21

Definition at line 103 of file air.h. Referenced by Cair().

## sc\_in<bool> Cair::ADI22

Definition at line 104 of file air.h. Referenced by Cair().

## sc\_in<bool> Cair::ADI23

Definition at line 105 of file air.h. Referenced by Cair().

## sc\_in<bool> Cair::ADI24

Definition at line 106 of file air.h. Referenced by Cair().

## sc\_in<bool> Cair::ADI30

Definition at line 107 of file air.h. Referenced by Cair().

#### sc\_in<bool> Cair::ADI31

Definition at line 108 of file air.h. Referenced by Cair().

## sc\_in<bool> Cair::ADI32

Definition at line 109 of file air.h. Referenced by Cair().

## sc\_in<bool> Cair::ADI33

Definition at line 110 of file air.h. Referenced by Cair().

## sc\_in<bool> Cair::ADI34

Definition at line 111 of file air.h. Referenced by Cair().

## sc\_in<bool> Cair::ADMPXI0

Definition at line 88 of file air.h. Referenced by Cair().

## sc\_in<bool> Cair::ADMPXI1

Definition at line 89 of file air.h. Referenced by Cair().

#### sc\_in<br/>cool> Cair::ADMPXI2

Definition at line 90 of file air.h. Referenced by Cair().

### sc\_in<bool> Cair::ADMPXI3

Definition at line 91 of file air.h. Referenced by Cair().

## sc\_in<bool> Cair::ASI0

Definition at line 130 of file air.h. Referenced by Cair().

## sc\_in<bool> Cair::ASI1

Definition at line 131 of file air.h. Referenced by Cair().

## sc\_in<bool> Cair::ASI10

Definition at line 140 of file air.h. Referenced by Cair().

#### sc\_in<bool> Cair::ASI11

Definition at line 141 of file air.h.

Referenced by Cair().

## sc\_in<bool> Cair::ASI12

Definition at line 142 of file air.h. Referenced by Cair().

## sc\_in<bool> Cair::ASI13

Definition at line 143 of file air.h. Referenced by Cair().

## sc\_in<bool> Cair::ASI14

Definition at line 144 of file air.h. Referenced by Cair().

## sc\_in<bool> Cair::ASI15

Definition at line 145 of file air.h. Referenced by Cair().

## sc\_in<bool> Cair::ASI2

Definition at line 132 of file air.h. Referenced by Cair().

#### sc\_in<bool> Cair::ASI3

Definition at line 133 of file air.h. Referenced by Cair().

#### sc\_in<bool> Cair::ASI4

Definition at line 134 of file air.h. Referenced by Cair().

## sc\_in<bool> Cair::ASI5

Definition at line 135 of file air.h. Referenced by Cair().

## sc\_in<bool> Cair::ASI6

Definition at line 136 of file air.h. Referenced by Cair().

## sc\_in<bool> Cair::ASI7

Definition at line 137 of file air.h. Referenced by Cair().

## sc\_in<bool> Cair::ASI8

Definition at line 138 of file air.h. Referenced by Cair().

#### sc\_in<bool> Cair::ASI9

Definition at line 139 of file air.h. Referenced by Cair().

### sc\_in<bool> Cair::BFP0

Definition at line 146 of file air.h. Referenced by Cair().

## sc\_in<br/>cool> Cair::BFP1

Definition at line 147 of file air.h. Referenced by Cair().

## sc\_in<bool> Cair::BFP10

Definition at line 156 of file air.h. Referenced by Cair().

## sc\_in<bool> Cair::BFP11

Definition at line 157 of file air.h. Referenced by Cair().

## sc\_in<bool> Cair::BFP12

Definition at line 158 of file air.h.

Referenced by Cair().

# sc\_in<bool> Cair::BFP13

Definition at line 159 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::BFP14

Definition at line 160 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::BFP15

Definition at line 161 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::BFP16

Definition at line 162 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::BFP17

Definition at line 163 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::BFP18

Definition at line 164 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::BFP19

Definition at line 165 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::BFP2

Definition at line 148 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::BFP20

Definition at line 166 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::BFP21

Definition at line 167 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::BFP22

Definition at line 168 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::BFP23

Definition at line 169 of file air.h. Referenced by Cair().

# sc\_in<br/>cool> Cair::BFP24

Definition at line 170 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::BFP25

Definition at line 171 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::BFP26

Definition at line 172 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::BFP27

Definition at line 173 of file air.h. Referenced by Cair().

# sc\_in<br/>cool> Cair::BFP28

Definition at line 174 of file air.h.

Referenced by Cair().

# sc\_in<bool> Cair::BFP29

Definition at line 175 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::BFP3

Definition at line 149 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::BFP30

Definition at line 176 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::BFP31

Definition at line 177 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::BFP32

Definition at line 178 of file air.h. Referenced by Cair().

# sc\_in<br/>cool> Cair::BFP33

Definition at line 179 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::BFP34

Definition at line 180 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::BFP35

Definition at line 181 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::BFP36

Definition at line 182 of file air.h. Referenced by Cair().

# sc\_in<br/>cool> Cair::BFP37

Definition at line 183 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::BFP38

Definition at line 184 of file air.h. Referenced by Cair().

# sc\_in<br/>cool> Cair::BFP39

Definition at line 185 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::BFP4

Definition at line 150 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::BFP40

Definition at line 195 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::BFP41

Definition at line 196 of file air.h. Referenced by Cair().

# sc\_in<br/>bool> Cair::BFP42

Definition at line 197 of file air.h. Referenced by Cair().

# sc\_in<br/>cool> Cair::BFP43

Definition at line 198 of file air.h.

Referenced by Cair().

# sc\_in<bool> Cair::BFP44

Definition at line 199 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::BFP45

Definition at line 200 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::BFP46

Definition at line 201 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::BFP47

Definition at line 202 of file air.h. Referenced by Cair().

# sc\_in<br/>cool> Cair::BFP5

Definition at line 151 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::BFP6

Definition at line 152 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::BFP7

Definition at line 153 of file air.h. Referenced by Cair().

# sc\_in<br/>cool> Cair::BFP8

Definition at line 154 of file air.h. Referenced by Cair().

# sc\_in<br/>cool> Cair::BFP9

Definition at line 155 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::CADE00

Definition at line 128 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::CADI00

Definition at line 129 of file air.h. Referenced by Cair().

# sc\_in<br/>cool> Cair::DSADE00

Definition at line 116 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::DSADE10

Definition at line 117 of file air.h. Referenced by Cair().

# sc\_in<br/>cool> Cair::DSADE11

Definition at line 119 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::DSADE12

Definition at line 120 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::DSADE13

Definition at line 121 of file air.h. Referenced by Cair().

# sc\_in<br/>cool> Cair::DSADE14

Definition at line 187 of file air.h.

Referenced by Cair().

# sc\_in<bool> Cair::DSADE15

Definition at line 188 of file air.h. Referenced by Cair().

# sc\_in<br/>cool> Cair::DSADE20

Definition at line 118 of file air.h. Referenced by Cair().

# sc\_in<br/>cool> Cair::DSADE21

Definition at line 189 of file air.h. Referenced by Cair().

# sc\_in<br/>cool> Cair::DSADE22

Definition at line 190 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::DSADI00

Definition at line 122 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::DSADI10

Definition at line 123 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::DSADI11

Definition at line 125 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::DSADI12

Definition at line 126 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::DSADI13

Definition at line 127 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::DSADI14

Definition at line 191 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::DSADI15

Definition at line 192 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::DSADI20

Definition at line 124 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::DSADI21

Definition at line 193 of file air.h. Referenced by Cair().

# sc\_in<bool> Cair::DSADI22

Definition at line 194 of file air.h. Referenced by Cair().

# sc\_out<bool> Cair::INTAIRDMAREQ[emNumOfOutputPort]

Definition at line 205 of file air.h.

Referenced by Cair(), and WriteINTAIRDMAREQMethod().

# sc\_out<bool> Cair::INTAIRINTREQ[emNumOfOutputPort]

Definition at line 204 of file air.h.

Referenced by Cair(), and WriteINTAIRINTREQMethod().

# sc\_event Cair::mCancelCmdResetEvent[private]

Definition at line 42 of file air.h.

Referenced by Cair(), CmdResetMethod(), and PortResetMethod().

# std::string Cair::mCmdld[private]

Definition at line 930 of file air.h.

# bool Cair::mCmdReset[private]

Definition at line 47 of file air.h.

Referenced by Cair(), CancelCmdResetMethod(), CmdResetMethod(), and PortResetMethod().

# sc\_event Cair::mCmdResetEvent[private]

Definition at line 41 of file air.h.

Referenced by AssertReset(), Cair(), and PortResetMethod().

# bool Cair::mDMAOut[emNumOfOutputPort][private]

Definition at line 54 of file air.h.

Referenced by Initialize(), WriteDMAPortOut(), and WriteINTAIRDMAREQMethod().

# std::string Cair::mFileName[private]

Definition at line 929 of file air.h.

# 

Definition at line 38 of file air.h.

Referenced by Cair(), and ReceiveInputMethod().

# std::string Cair::mInputPortName[emNumOfInputPort][private]

Definition at line 57 of file air.h.

Referenced by Cair(), and ReceiveInputMethod().

# std::string Cair::mInstName[private]

Definition at line 928 of file air.h.

# bool Cair::mINTOut[emNumOfOutputPort][private]

Definition at line 53 of file air.h.

Referenced by Initialize(), WriteINTAIRINTREQMethod(), and WriteINTPortOut().

## bool Cair::mlsCmdResetStatus[private]

Definition at line 48 of file air.h.

Referenced by AssertReset(), Cair(), CancelCmdResetMethod(), and PortResetMethod().

# bool Cair::mlsInitialize[private]

Definition at line 49 of file air.h.

Referenced by Cair(), and PortResetMethod().

## int Cair::mLineNum[private]

Definition at line 931 of file air.h.

## std::map<std::string, bool> Cair::mMessageLevel[private]

Definition at line 921 of file air.h.

## unsigned int Cair::mNumDMAReg[emNumOfOutputPort][private]

Definition at line 59 of file air.h.

Referenced by DumpStatInfo(), Initialize(), and WriteDMAPortOut().

# unsigned int Cair::mNumINTReq[emNumOfOutputPort][private]

Definition at line 58 of file air.h.

Referenced by DumpStatInfo(), Initialize(), and WriteINTPortOut().

# double Cair::mPCLKPeriod[private]

Definition at line 51 of file air.h.

Referenced by Cair(), ReceiveInputMethod(), and SetCLKfreq().

## bool Cair::mPortReset[private]

Definition at line 46 of file air.h.

Referenced by AssertReset(), Cair(), and PortResetMethod().

# bool Cair::mPreDMAOut[emNumOfOutputPort][private]

Definition at line 56 of file air.h.

Referenced by Initialize(), and WriteINTAIRDMAREQMethod().

# bool Cair::mPreINTOut[emNumOfOutputPort][private]

Definition at line 55 of file air.h.

Referenced by Initialize(), and WriteINTAIRINTREQMethod().

# double Cair::mResetPeriod[private]

Definition at line 52 of file air.h.

Referenced by AssertReset(), Cair(), and CmdResetMethod().

# unsigned int Cair::mState[private]

Definition at line 50 of file air.h.

Referenced by Cair(), EnableReset(), and ReceiveInputMethod().

# sc\_event Cair::mWriteINTAIRDMAREQEvent[private]

Definition at line 44 of file air.h.

Referenced by Cair(), EnableReset(), ReceiveInputMethod(), and SetCLKfreq().

# sc\_event Cair::mWriteINTAIRINTREQEvent[private]

Definition at line 43 of file air.h.

Referenced by Cair(), EnableReset(), ReceiveInputMethod(), and SetCLKfreq().

## Cair\_core\* Cair::pAirCore[private]

Definition at line 40 of file air.h.

Referenced by Cair(), EnableReset(), ReceiveInputMethod(), tgt\_acc(), tgt\_acc\_dbg(), and ~Cair().

## sc\_in<sc\_dt::uint64> Cair::pclk

Definition at line 85 of file air.h.

Referenced by Cair(), and PCLKPeriodMethod().

## sc\_in<bool> Cair::presetn

Definition at line 86 of file air.h.

Referenced by Cair(), and PortResetMethod().

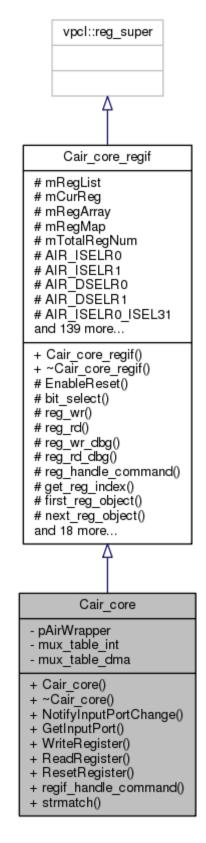
# The documentation for this class was generated from the following files:

• air.h

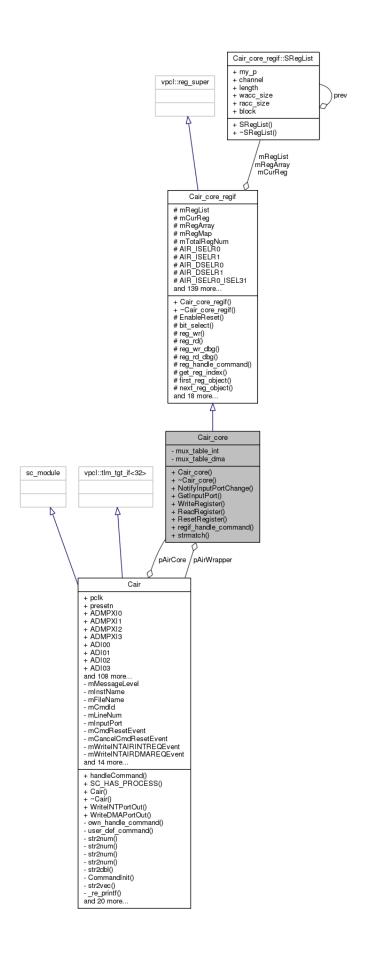
- <u>air cmdif.h</u>
- <u>air.cpp</u>

# Cair\_core Class Reference

AIR\_CORE class.
#include <air\_core.h>
Inheritance diagram for Cair\_core:



Collaboration diagram for Cair\_core:



# **Classes**

• struct air mux table

# **Public Types**

• enum <u>eAirCoreConstant</u> { <u>emResource0</u> = 0, <u>emResource1</u> = 1, <u>emNotResource</u> = 2 }

# **Public Member Functions**

- <u>Cair\_core</u> (std::string name, <u>Cair</u> \*air)
- <u>~Cair core</u> ()
- void NotifyInputPortChange (unsigned int index, bool is\_active)
- bool GetInputPort (unsigned int index)
- bool WriteRegister (cuint addr, const unsigned char \*p\_data, cuint size, bool is\_debug)
- bool ReadRegister (cuint addr, unsigned char \*p\_data, cuint size, bool is\_debug)
- void <u>ResetRegister</u> (bool is\_active)
- std::string <u>regif\_handle\_command</u> (std::vector< std::string > &args)
- bool strmatch (const char \*ptn, const char \*str)

# **Private Attributes**

- <u>Cair</u> \* pAirWrapper
- std::vector< <u>air\_mux\_table</u> > <u>mux\_table\_int</u>
- std::vector< <u>air\_mux\_table</u> > <u>mux\_table\_dma</u>

## **Friends**

• class Cair

# **Additional Inherited Members**

# **Detailed Description**

AIR\_CORE class.

Definition at line 19 of file air\_core.h.

# **Member Enumeration Documentation**

enum Cair\_core::eAirCoreConstant

## **Enumerator:**

emResource0 emResource1 emNotResource

Definition at line 23 of file air\_core.h.

## **Constructor & Destructor Documentation**

Cair\_core::Cair\_core (std::string name, Cair \* air)

Definition at line 18 of file air\_core.cpp.

References Cair\_core\_regif::AIR\_DSELR0, Cair\_core\_regif::AIR\_DSELR1, Cair\_core\_regif::AIR\_ISELR0, Cair\_core\_regif::AIR\_ISELR1, emNotResource, emResource0, emResource1, mux\_table\_dma, mux\_table\_int, and pAirWrapper.

Cair\_core::~Cair\_core ()

Definition at line 259 of file air\_core.cpp.

# **Member Function Documentation**

## bool Cair core::GetInputPort (unsigned int index)

Definition at line 303 of file air\_core.cpp.

References mux\_table\_int.

Referenced by Cair::ReceiveInputMethod().

Here is the caller graph for this function:



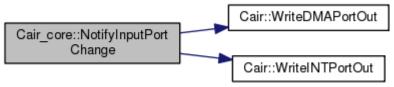
# void Cair\_core::NotifyInputPortChange (unsigned int index, bool is\_active)

Definition at line 284 of file air\_core.cpp.

References emNotResource, mux\_table\_dma, mux\_table\_int, pAirWrapper, Cair::WriteDMAPortOut(), and Cair::WriteINTPortOut().

Referenced by Cair::ReceiveInputMethod().

Here is the call graph for this function:



Here is the caller graph for this function:



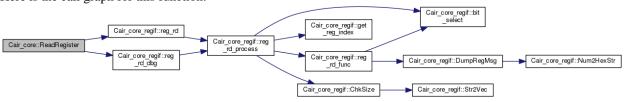
# bool Cair\_core::ReadRegister (cuint addr, unsigned char \* p\_data, cuint size, bool is\_debug)

Definition at line 274 of file air\_core.cpp.

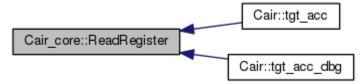
References Cair\_core\_regif::reg\_rd(), and Cair\_core\_regif::reg\_rd\_dbg().

Referenced by Cair::tgt\_acc(), and Cair::tgt\_acc\_dbg().

Here is the call graph for this function:



Here is the caller graph for this function:



# std::string Cair\_core::regif\_handle\_command (std::vector< std::string > & args)

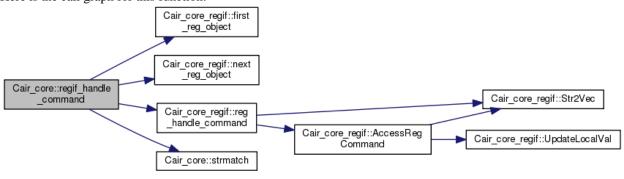
Definition at line 314 of file air\_core.cpp.

References Cair\_core\_regif::first\_reg\_object(),

Cair core regif::mInstName,

Cair\_core\_regif::next\_reg\_object(), Cair\_core\_regif::reg\_handle\_command(), and strmatch().

Here is the call graph for this function:



# void Cair\_core::ResetRegister (bool is\_active)

Definition at line 309 of file air\_core.cpp.

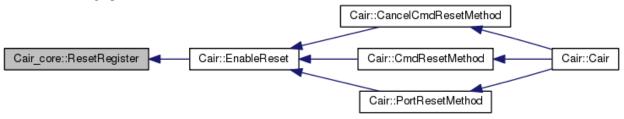
References Cair\_core\_regif::EnableReset().

Referenced by Cair::EnableReset().

Here is the call graph for this function:



Here is the caller graph for this function:



# bool Cair\_core::strmatch (const char \* ptn, const char \* str)

Definition at line 353 of file air\_core.cpp.

Referenced by regif\_handle\_command().

Here is the caller graph for this function:



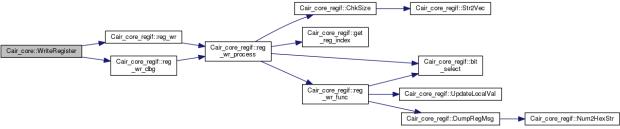
# bool Cair\_core::WriteRegister (<u>cuint</u> addr, const unsigned char \* p\_data, <u>cuint</u> size, bool is\_debug)

Definition at line 264 of file air\_core.cpp.

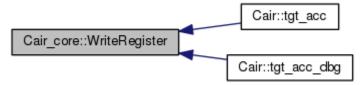
References Cair\_core\_regif::reg\_wr(), and Cair\_core\_regif::reg\_wr\_dbg().

Referenced by Cair::tgt\_acc(), and Cair::tgt\_acc\_dbg().

Here is the call graph for this function:



Here is the caller graph for this function:



# Friends And Related Function Documentation

# friend class Cair [friend]

Definition at line 22 of file air\_core.h.

# **Member Data Documentation**

# std::vector<air\_mux\_table> Cair\_core::mux\_table\_dma[private]

Definition at line 61 of file air\_core.h.

Referenced by Cair\_core(), and NotifyInputPortChange().

# std::vector<air\_mux\_table> Cair\_core::mux\_table\_int[private]

Definition at line 60 of file air\_core.h.

Referenced by Cair\_core(), GetInputPort(), and NotifyInputPortChange().

# Cair\* Cair\_core::pAirWrapper[private]

Definition at line 41 of file air\_core.h.

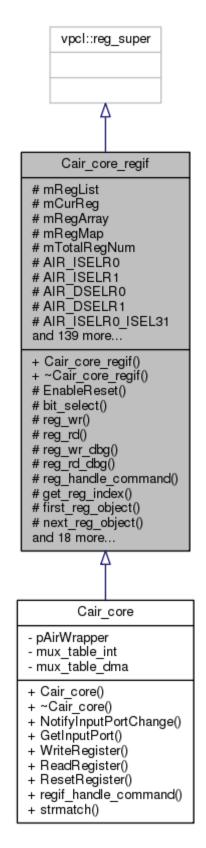
Referenced by Cair\_core(), and NotifyInputPortChange().

# The documentation for this class was generated from the following files:

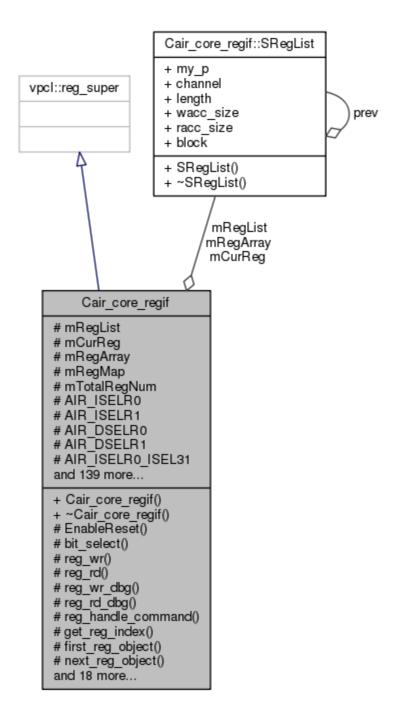
- air core.h
- air\_core.cpp

# Cair\_core\_regif Class Reference

Register IF class of AIR\_CORE model. #include <air\_core\_regif.h> Inheritance diagram for Cair\_core\_regif:



Collaboration diagram for Cair\_core\_regif:



# **Classes**

- struct RegCBstr
- struct <u>SRegList</u>

# **Public Member Functions**

- <u>Cair core regif</u> (std::string name, <u>uint</u> buswidth)
- ~Cair core regif ()

# **Protected Types**

- enum eRegGroup { emNum of gr }
- typedef const unsigned int cuint
- typedef unsigned int uint

# **Protected Member Functions**

- void EnableReset (const bool is\_active)
- uint bit select (cuint val, cuint start, cuint end)
- bool reg\_wr (cuint addr, const unsigned char \*p\_data, cuint size)
- bool reg\_rd (cuint addr, unsigned char \*p\_data, cuint size)
- bool reg wr dbg (cuint addr, const unsigned char \*p data, cuint size)
- bool reg rd dbg (cuint addr, unsigned char \*p data, cuint size)
- std::string <u>reg\_handle\_command</u> (const std::vector< std::string > &args)
- int get\_reg\_index (cuint addr)
- vpcl::re\_register \* <u>first\_reg\_object</u> ()
- vpcl::re\_register \* next\_reg\_object ()
- void <u>wr\_cb</u> (<u>cuint</u> addr, <u>uint</u> data)
- uint rd cb (cuint addr)
- void <u>set\_instance\_name</u> (std::string InstName)
- bool reg wr process (cuint addr, const unsigned char \*p\_data, cuint size, bool IsDbgFunc)
- bool reg rd process (cuint addr, unsigned char \*p\_data, cuint size, bool IsDbgFunc)
- bool <u>reg\_wr\_func</u> (<u>cuint</u> addr, const unsigned char \*p\_data, <u>cuint</u> size, <u>cuint</u> reg\_index, bool IsDbgFunc)
- bool <u>reg\_rd\_func</u> (<u>cuint</u> addr, unsigned char \*p\_data, <u>cuint</u> size, <u>cuint</u> reg\_index, bool IsDbgFunc)
- void <u>re\_printf</u> (const std::string msg\_level, const char \*format,...)
- void get fileline (std::string filename, int line\_number)
- void CommandInit ()
- bool ChkSize (std::string expect\_size, cuint detect\_size, cuint addr, bool is\_wr)
- std::vector< std::string > <u>Str2Vec</u> (std::string str, const char sep)
- std::string Num2HexStr (cuint num, cuint num\_length, bool space\_strip, bool is\_data, uint acc\_size)
- void <u>DumpRegMsg</u> (const std::string operation, const std::string RegName, const std::string BitName, <u>cuint</u> size, <u>cuint</u> addr, <u>cuint</u> wr\_data, <u>cuint</u> pre\_data, <u>cuint</u> data, <u>cuint</u> reg\_length)
- std::string <u>AccessRegCommand</u> (const std::vector< std::string > &args, vpcl::re\_register \*Register, bool &BlockReg)
- void InitLocalVal ()
- void UpdateLocalVal (cuint addr)
- void UpdateRegVal (cuint addr)

## **Protected Attributes**

- struct Cair core regif::SRegList \* mRegList
- struct Cair core regif::SRegList \* mCurReg
- struct Cair core regif::SRegList \*\* mRegArray
- uint \* mRegMap
- uint mTotalRegNum
- vpcl::re\_register \* <u>AIR\_ISELR0</u>
- vpcl::re\_register \* AIR\_ISELR1
- vpcl::re\_register \* AIR\_DSELR0
- vpcl::re register \* AIR DSELR1
- uint AIR\_ISELR0\_ISEL31
- <u>uint AIR ISELR0 ISEL30</u>
- uint AIR\_ISELR0\_ISEL29
- uint AIR ISELR0 ISEL28
- <u>uint AIR\_ISELR0\_ISEL27</u>

- <u>uint AIR ISELR0 ISEL26</u>
- uint AIR ISELRO ISEL25
- <u>uint</u> <u>AIR\_ISELR0\_ISEL24</u>
- uint AIR ISELRO ISEL23
- uint AIR\_ISELR0\_ISEL22
- <u>uint AIR ISELR0</u> ISEL21
- uint <u>AIR\_ISELR0\_ISEL20</u>
- uint AIR\_ISELR0\_ISEL19
- <u>uint AIR\_ISELR0\_ISEL18</u>
- uint <u>AIR\_ISELR0\_ISEL17</u>
- uint AIR ISELR0 ISEL16
- <u>uint AIR\_ISELR0\_ISEL15</u>
- uint <u>AIR\_ISELR0\_ISEL14</u>
- <u>uint AIR\_ISELR0\_ISEL13</u>
- <u>uint AIR\_ISELR0\_ISEL12</u>
- uint AIR ISELRO ISEL11
- uint AIR\_ISELR0\_ISEL10
- uint AIR ISELRO ISEL9
- uint AIR ISELRO ISEL8
- uint AIR ISELRO ISEL7
- uint AIR ISELRO ISEL6
- uint AIR\_ISELR0\_ISEL5
- uint AIR ISELR0 ISEL4
- uint AIR ISELR0 ISEL3
- uint AIR ISELR0 ISEL2
- uint AIR ISELRO ISEL1
- <u>uint AIR\_ISELR0\_ISEL0</u>
- uint AIR ISELR1 ISEL63
- uint AIR\_ISELR1\_ISEL62
- <u>uint AIR ISELR1 ISEL61</u>
- uint AIR\_ISELR1\_ISEL60
- uint AIR\_ISELR1\_ISEL59
- <u>uint AIR ISELR1 ISEL58</u>
- <u>uint AIR\_ISELR1\_ISEL57</u>uint AIR\_ISELR1\_ISEL56
- uint AIR\_ISELR1\_ISEL55
- uint AIR\_ISELR1\_ISEL54
- uint AIR ISELR1 ISEL53
- uint AIR\_ISELR1\_ISEL52
- uint AIR ISELR1 ISEL51
- <u>uint AIR\_ISELR1\_ISEL50</u>
- uint AIR ISELR1 ISEL49
- uint AIR ISELR1 ISEL48
- uint AIR\_ISELR1\_ISEL47uint AIR\_ISELR1\_ISEL46
- uint AIR ISELR1 ISEL45
- uint AIR ISELR1 ISEL44
- uint AIR\_ISELR1\_ISEL43
- <u>uint AIR\_ISELR1\_ISEL42</u>
- uint AIR ISELR1 ISEL41
- uint AIR\_ISELR1\_ISEL40
- uint AIR ISELR1 ISEL39
- uint AIR\_ISELR1\_ISEL38

- uint AIR ISELR1 ISEL37
- uint AIR ISELR1 ISEL36
- uint AIR\_ISELR1\_ISEL35
- uint AIR ISELR1 ISEL34
- uint AIR\_ISELR1\_ISEL33
- uint AIR ISELR1 ISEL32
- uint AIR\_DSELR0\_DSEL31
- uint AIR\_DSELR0\_DSEL30
- uint <u>AIR\_DSELR0\_DSEL29</u>
- <u>uint AIR\_DSELR0\_DSEL28</u>
- uint AIR DSELR0 DSEL27
- uint <u>AIR\_DSELR0\_DSEL26</u>
- <u>uint AIR\_DSELR0\_DSEL25</u>
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- <u>uint AIR DSELR0 DSEL20</u>
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- <u>uint AIR DSELR0 DSEL18</u>
- uint AIR DSELR0 DSEL17
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- uint AIR DSELRO DSEL14
- uint AIR DSELRO DSEL13
- · AIR DOELRO DOELI.
- uint AIR DSELRO DSEL12
- uint <u>AIR\_DSELR0\_DSEL11</u>
- uint AIR DSELR0 DSEL10
- uint AIR\_DSELR0\_DSEL9
- uint AIR DSELRO DSEL8
- uint AIR\_DSELR0\_DSEL7
- uint AIR\_DSELR0\_DSEL6
- uint AIR DSELR0 DSEL5uint AIR\_DSELR0\_DSEL4
- uint AIR DSELR0 DSEL3
- uint AIR\_DSELR0\_DSEL2
- uint AIR\_DSELR0\_DSEL1
- <u>uint AIR DSELRO DSELO</u>
- uint AIR\_DSELR1\_DSEL63
- uint AIR DSELR1 DSEL62
- uint AIR\_DSELR1\_DSEL61
- <u>uint AIR DSELR1 DSEL60</u>
- uint AIR DSELR1 DSEL59
- uint AIR DSELR1 DSEL58
- uint AIR\_DSELR1\_DSEL57uint AIR\_DSELR1\_DSEL56
- uint AIR DSELR1 DSEL55
- uint AIR\_DSELR1\_DSEL54
- uint AIR\_DSELR1\_DSEL53
- uint AIR DSELR1 DSEL52
- uint AIR\_DSELR1\_DSEL51
- uint AIR DSELR1 DSEL50
- uint AIR\_DSELR1\_DSEL49

- <u>uint AIR DSELR1 DSEL48</u>
- uint AIR DSELR1 DSEL47
- uint AIR\_DSELR1\_DSEL46
- uint AIR DSELR1 DSEL45
- uint AIR\_DSELR1\_DSEL44
- uint AIR DSELR1 DSEL43
- uint AIR\_DSELR1\_DSEL42
- uint AIR\_DSELR1\_DSEL41
- uint AIR\_DSELR1\_DSEL40
- uint AIR\_DSELR1\_DSEL39
- uint AIR DSELR1 DSEL38
- uint AIR\_DSELR1\_DSEL37
- uint AIR\_DSELR1\_DSEL36
- uint AIR\_DSELR1\_DSEL35
- uint AIR DSELR1 DSEL34
- uint AIR DSELR1 DSEL33
- uint AIR\_DSELR1\_DSEL32
- <u>uint mBusByteWidth</u>
- <u>uint mBusWidth</u>
- bool <u>mIsReset</u>
- bool <u>mDumpRegisterRW</u>
- bool <u>mAPBAccessMode</u>
- bool <u>mDumpBitInfo</u>
- std::map< std::string, bool > mMessageLevel
- std::map< std::string,</li>
- std::map< std::string, void(Cair\_core\_regif::\*)(RegCBstr)>> mWrCbAPI
- std::map< std::string,
- std::map< std::string, void(Cair core regif::\*)(RegCBstr)>> mRdCbAPI
- std::string <u>mFileName</u>
- std::string mInstName
- int <u>mLineNum</u>

# **Detailed Description**

Register IF class of AIR\_CORE model.

Definition at line 214 of file air\_core\_regif.h.

# **Member Typedef Documentation**

typedef const unsigned int Cair core regif::cuint[protected]

Definition at line 218 of file air\_core\_regif.h.

typedef unsigned int Cair\_core\_regif::uint[protected]

Definition at line 219 of file air\_core\_regif.h.

## **Member Enumeration Documentation**

enum <u>Cair\_core\_regif::eRegGroup</u>[protected]

#### **Enumerator:**

emNum\_of\_gr

Definition at line 224 of file air\_core\_regif.h.

# **Constructor & Destructor Documentation**

## Cair\_core\_regif::Cair\_core\_regif (std::string name, uint buswidth)

Constructor of Register IF class: define registers and bits

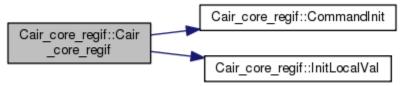
### Returns:

none

Definition at line 202 of file air\_core\_regif.cpp.

References AIR\_DSELR0, AIR\_DSELR1, AIR\_ISELR0, AIR\_ISELR1, CommandInit(), InitLocalVal(), mBusByteWidth, mBusWidth, mCurReg, mFileName, mInstName, mIsReset, mLineNum, mRdCbAPI, mRegArray, mRegList, mRegMap, mTotalRegNum, mWrCbAPI, and Cair\_core\_regif::SRegList::my\_p.

Here is the call graph for this function:



# Cair\_core\_regif::~Cair\_core\_regif ()

Destructor: delete pointers

### Returns:

none

Definition at line 402 of file air\_core\_regif.cpp.

References mRegArray, mRegMap, mTotalRegNum, Cair\_core\_regif::SRegList::my\_p, and Cair\_core\_regif::SRegList::prev.

## **Member Function Documentation**

# void Cair\_core\_regif::\_re\_printf (const std::string msg\_level, const char \* message, ...)[protected]

print message function

#### Returns:

none

Definition at line 1553 of file air\_core\_regif.cpp.

References mFileName, mInstName, mLineNum, and mMessageLevel.

# std::string Cair\_core\_regif::AccessRegCommand (const std::vector< std::string > & args, vpcl::re\_register \* Register, bool & BlockReg)[protected]

process reg\_handle\_command of "reg" parameter

## Returns:

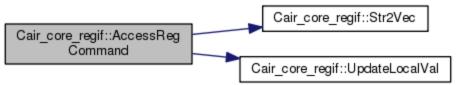
string

Definition at line 942 of file air\_core\_regif.cpp.

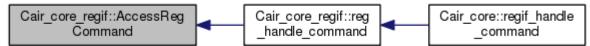
References mInstName, Str2Vec(), and UpdateLocalVal().

Referenced by reg\_handle\_command().

Here is the call graph for this function:



Here is the caller graph for this function:



# Cair\_core\_regif::uint Cair\_core\_regif::bit\_select (cuint val, cuint start, cuint end) [protected]

Mask unselected bit

### Returns:

selected value

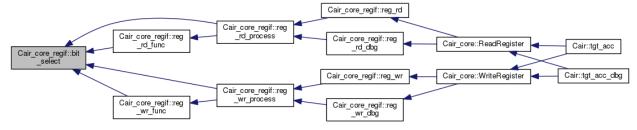
# Parameters:

in	val	Writting address
in	start	start bit position
in	end	end bit position

Definition at line 416 of file air\_core\_regif.cpp.

Referenced by reg\_rd\_func(), reg\_rd\_process(), reg\_wr\_func(), and reg\_wr\_process().

Here is the caller graph for this function:



# bool Cair\_core\_regif::ChkSize (std::string expect\_size, <u>cuint</u> detect\_size, <u>cuint</u> addr, bool is wr)[protected]

Check access size @return true if detect\_size equal expect\_size

Definition at line 796 of file air\_core\_regif.cpp.

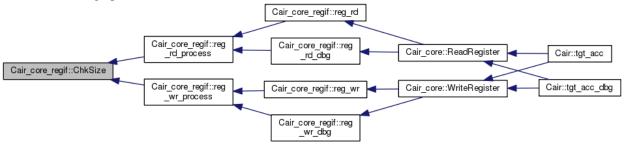
References mAPBAccessMode, mBusByteWidth, re\_printf, and Str2Vec().

Referenced by reg\_rd\_process(), and reg\_wr\_process().

Here is the call graph for this function:



Here is the caller graph for this function:



# void Cair\_core\_regif::CommandInit () [protected]

Initialize reg\_handle\_command variables

### Returns:

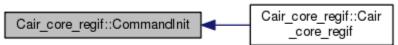
none

Definition at line 828 of file air\_core\_regif.cpp.

References mAPBAccessMode, mDumpBitInfo, mDumpRegisterRW, and mMessageLevel.

Referenced by Cair\_core\_regif().

Here is the caller graph for this function:



void Cair\_core\_regif::DumpRegMsg (const std::string *operation*, const std::string *RegName*, const std::string *BitName*, <u>cuint</u> *size*, <u>cuint</u> *addr*, <u>cuint</u> *wr\_data*, <u>cuint</u> *pre\_data*, <u>cuint</u> *data*, <u>cuint</u> *reg\_length*)[protected]

Dump register access information

#### Returns:

none

Definition at line 1468 of file air\_core\_regif.cpp.

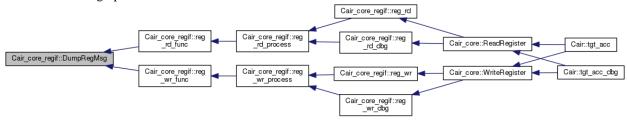
References mDumpRegisterRW, Num2HexStr(), and re\_printf.

Referenced by reg rd func(), and reg wr func().

Here is the call graph for this function:



Here is the caller graph for this function:



# void Cair\_core\_regif::EnableReset (const bool is\_active)[protected]

Method to change value of mIsReset

#### Returns:

none

Definition at line 1693 of file air\_core\_regif.cpp.

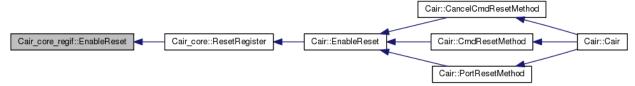
 $References \ \ InitLocalVal(), \ mIsReset, \ mRegArray, \ mTotalRegNum, \ Cair\_core\_regif::SRegList::my\_p, \ and \ re\_printf.$ 

Referenced by Cair\_core::ResetRegister().

Here is the call graph for this function:



Here is the caller graph for this function:



# vpcl::re\_register \* Cair\_core\_regif::first\_reg\_object () [protected]

find first register pointer

### Returns:

first register pointer

Definition at line 1508 of file air\_core\_regif.cpp.

References mCurReg, mRegList, and Cair\_core\_regif::SRegList::my\_p.

Referenced by Cair\_core::regif\_handle\_command().

Here is the caller graph for this function:



# void Cair\_core\_regif::get\_fileline (std::string filename, int line\_number)[protected]

Get file name and line number

## Returns:

none

Definition at line 1678 of file air\_core\_regif.cpp.

References mFileName, and mLineNum.

## int Cair core regif::get reg index (cuint access addr) [protected]

find register pointer based on accessed address

### Returns:

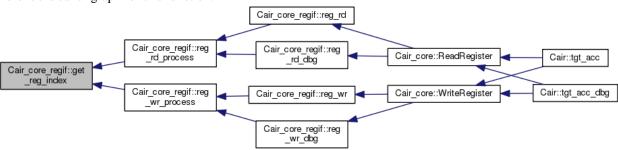
accessed register pointer

Definition at line 1497 of file air\_core\_regif.cpp.

References mRegMap.

Referenced by reg\_rd\_process(), and reg\_wr\_process().

Here is the caller graph for this function:



# void Cair\_core\_regif::InitLocalVal () [protected]

Initialize local variables

#### Returns:

none

Definition at line 1001 of file air\_core\_regif.cpp.

```
References AIR DSELRO DSELO, AIR DSELRO DSELD DSELO, AIR DSELRO DSELO, AIR DSELRO DSELO, AIR DSELRO DS
AIR_DSELR0_DSEL12,
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                                                      AIR_ISELR0_ISEL1,
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AIR_ISELR0_ISEL12,
                       AIR_ISELR0_ISEL13,
                                               AIR_ISELR0_ISEL14,
                                                                       AIR_ISELR0_ISEL15,
AIR ISELRO ISEL16,
                       AIR ISELRO ISEL17.
                                               AIR ISELRO ISEL18,
                                                                      AIR ISELRO ISEL19,
                      AIR_ISELR0_ISEL20,
AIR_ISELR0_ISEL2,
                                              AIR_ISELR0_ISEL21,
                                                                       AIR_ISELR0_ISEL22,
AIR ISELRO ISEL23,
                       AIR ISELRO ISEL24,
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                        AIR ISELRO ISEL31,
                                                AIR_ISELR0_ISEL4,
                                                                       AIR_ISELR0_ISEL5,
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                        AIR ISELRO ISEL7,
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                       AIR ISELR1 ISEL33,
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AIR ISELR1 ISEL52,
                                               AIR ISELR1 ISEL54,
                                                                      AIR ISELR1 ISEL55,
                       AIR ISELR1 ISEL57,
                                               AIR ISELR1 ISEL58,
                                                                       AIR ISELR1 ISEL59,
AIR ISELR1 ISEL56,
AIR ISELR1 ISEL60, AIR ISELR1 ISEL61, AIR ISELR1 ISEL62, and AIR ISELR1 ISEL63.
```

Referenced by Cair core regif(), and EnableReset().

Here is the caller graph for this function:



# vpcl::re\_register \* Cair\_core\_regif::next\_reg\_object () [protected]

find next register pointer

## Returns:

next register pointer

Definition at line 1516 of file air core regif.cpp.

References mCurReg, Cair core regif::SRegList::my p, and Cair core regif::SRegList::prev.

Referenced by Cair\_core::regif\_handle\_command().

Here is the caller graph for this function:



std::string Cair\_core\_regif::Num2HexStr (<u>cuint</u> num, <u>cuint</u> num\_length, bool space\_strip, bool is\_wr\_data, <u>uint</u> acc\_size) [protected]

Align nuber to hexadecimal format

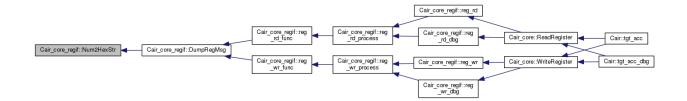
#### Returns:

aligned number

Definition at line 1444 of file air\_core\_regif.cpp.

Referenced by DumpRegMsg().

Here is the caller graph for this function:



# Cair\_core\_regif::uint Cair\_core\_regif::rd\_cb (cuint addr) [protected]

read callback function of CoWare

# Returns:

read data

Definition at line 1540 of file air\_core\_regif.cpp.

# std::string Cair\_core\_regif::reg\_handle\_command (const std::vector< std::string > & args) [protected]

Process reg\_handle\_command command

#### Returns:

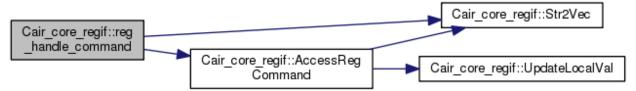
string

Definition at line 841 of file air core regif.cpp.

References AccessRegCommand(), mAPBAccessMode, mDumpBitInfo, mDumpRegisterRW, mInstName, mMessageLevel, mRegArray, mTotalRegNum, and Str2Vec().

Referenced by Cair\_core::regif\_handle\_command().

Here is the call graph for this function:



Here is the caller graph for this function:



# bool Cair\_core\_regif::reg\_rd (cuint addr, unsigned char \* p\_data, cuint size)[protected]

Read the register value of requested module

## Returns:

true if reading transaction is finished normally

## Parameters:

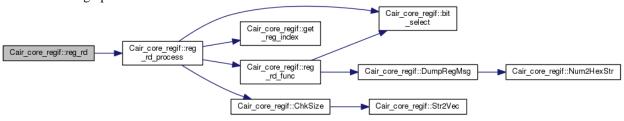
in	addr	Reading address
out	p_data	Reading data
in	size	Data size (byte)

Definition at line 452 of file air core regif.cpp.

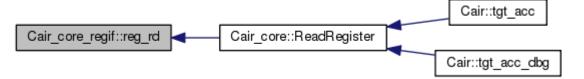
References mBusByteWidth, re\_printf, and reg\_rd\_process().

Referenced by Cair\_core::ReadRegister().

Here is the call graph for this function:



Here is the caller graph for this function:



# bool Cair\_core\_regif::reg\_rd\_dbg (cuint addr, unsigned char \* p\_data, cuint size) [protected]

Read the register value of requested module

#### Returns:

true if reading transaction is finished normally

## Parameters:

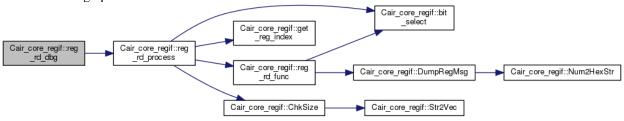
in	addr	Reading address
out	p_data	Reading data
in	size	Data size (byte)

Definition at line 498 of file air\_core\_regif.cpp.

References mBusByteWidth, re\_printf, and reg\_rd\_process().

Referenced by Cair\_core::ReadRegister().

Here is the call graph for this function:



Here is the caller graph for this function:



# bool Cair\_core\_regif::reg\_rd\_func (<u>cuint</u> addr, unsigned char \* p\_data, <u>cuint</u> size, <u>cuint</u> reg\_index, bool IsDbgFunc)[protected]

Process reading function

## Returns:

true if reading successfully

#### Parameters:

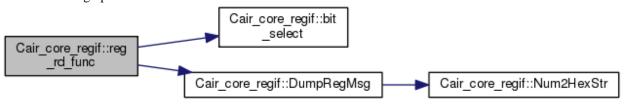
in	addr	Reading address
out	p_data	Reading data
in	size	Data size (byte)
in	reg_index	Register index
in	IsDbgFunc	Flag indicate reg_rd or reg_rd_dbg

Definition at line 669 of file air\_core\_regif.cpp.

References bit\_select(), DumpRegMsg(), Cair\_core\_regif::SRegList::length, mDumpBitInfo, mIsReset, mRegArray, Cair\_core\_regif::SRegList::my\_p, and re\_printf.

Referenced by reg\_rd\_process().

Here is the call graph for this function:



Here is the caller graph for this function:



# bool Cair\_core\_regif::reg\_rd\_process (<u>cuint</u> addr, unsigned char \* p\_data, <u>cuint</u> size, bool IsDbgFunc)[protected]

Read the register value of requested module

#### Returns:

true if reading transaction is finished normally

### Parameters:

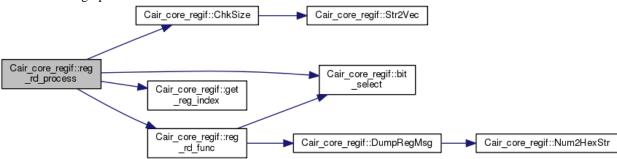
in	addr	Reading address
out	p_data	Reading data
in	size	Data size (byte)
in	IsDbgFunc	Flag indicate reg_rd or reg_rd_dbg

Definition at line 596 of file air\_core\_regif.cpp.

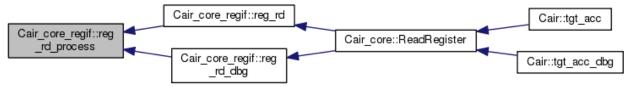
References bit\_select(), ChkSize(), get\_reg\_index(), Cair\_core\_regif::SRegList::length, mAPBAccessMode, mBusByteWidth, mRegArray, Cair\_core\_regif::SRegList::my\_p, re\_printf, and reg\_rd\_func().

Referenced by reg\_rd(), and reg\_rd\_dbg().

Here is the call graph for this function:



Here is the caller graph for this function:



#### bool Cair\_core\_regif::reg\_wr (cuint addr, const unsigned char \* p\_data, cuint size)[protected]

Write the value to requested module register

#### Returns:

true if writing transaction is finished normally

#### Parameters:

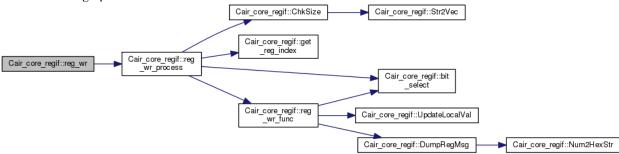
in	addr	Writting address
in	p_data	Writing data
in	size	Data size (byte)

Definition at line 429 of file air core regif.cpp.

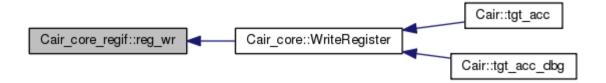
References mBusByteWidth, re printf, and reg wr process().

Referenced by Cair\_core::WriteRegister().

Here is the call graph for this function:



Here is the caller graph for this function:



# bool Cair\_core\_regif::reg\_wr\_dbg (<u>cuint</u> addr, const unsigned char \* p\_data, <u>cuint</u> size)[protected]

Write the value to requested module register

#### Returns:

true if writing transaction is finished normally

#### Parameters:

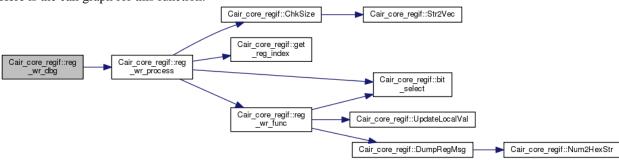
in	addr	Writting address
in	p_data	Writing data
in	size	Data size (byte)

Definition at line 475 of file air\_core\_regif.cpp.

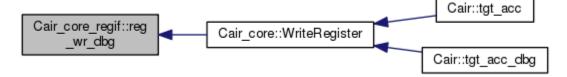
References mBusByteWidth, re\_printf, and reg\_wr\_process().

Referenced by Cair\_core::WriteRegister().

Here is the call graph for this function:



Here is the caller graph for this function:



bool Cair\_core\_regif::reg\_wr\_func (<u>cuint</u> addr, const unsigned char \* p\_data, <u>cuint</u> size, <u>cuint</u> reg\_index, bool IsDbgFunc)[protected]

Process writing function

#### Returns:

true if writing successfully

#### Parameters:

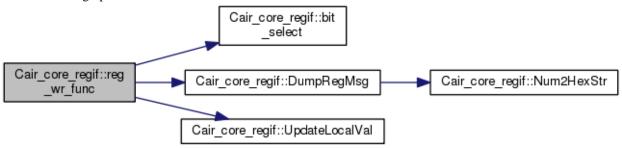
in	addr	Writting address
in	p_data	Writing data
in	size	Data size (byte)
in	reg_index	Register index
in	IsDbgFunc	Flag indicate reg_wr or reg_wr_dbg

Definition at line 728 of file air\_core\_regif.cpp.

References bit\_select(), DumpRegMsg(), Cair\_core\_regif::SRegList::length, mDumpBitInfo, mRegArray, Cair\_core\_regif::SRegList::my\_p, re\_printf, and UpdateLocalVal().

Referenced by reg\_wr\_process().

Here is the call graph for this function:



Here is the caller graph for this function:



# bool Cair\_core\_regif::reg\_wr\_process (<u>cuint</u> addr, const unsigned char \* p\_data, <u>cuint</u> size, bool IsDbgFunc)[protected]

Write the value to requested module register

#### Returns:

true if writing transaction is finished normally

#### Parameters:

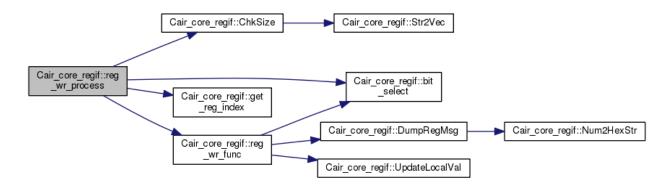
in	addr	Writting address
in	p_data	Writing data
in	size	Data size (byte)
in	IsDbgFunc	Flag indicate reg_rd or reg_rd_dbg

Definition at line 521 of file air\_core\_regif.cpp.

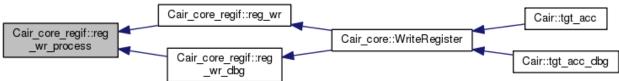
References bit\_select(), ChkSize(), get\_reg\_index(), Cair\_core\_regif::SRegList::length, mBusByteWidth, mIsReset, mRegArray, Cair\_core\_regif::SRegList::my\_p, re\_printf, and reg\_wr\_func().

Referenced by reg\_wr(), and reg\_wr\_dbg().

Here is the call graph for this function:



Here is the caller graph for this function:



#### void Cair\_core\_regif::set\_instance\_name (std::string InstName)[protected]

API for instance registration

#### Returns:

none

Definition at line 1686 of file air\_core\_regif.cpp.

References mInstName.

#### std::vector< std::string > Cair\_core\_regif::Str2Vec (std::string str, const char sep) [protected]

convert string to vector

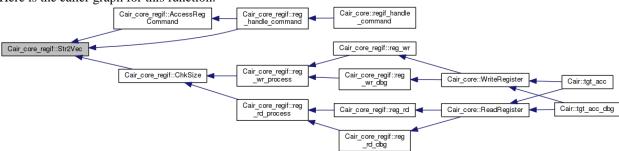
#### Returns:

vector

Definition at line 1428 of file air\_core\_regif.cpp.

Referenced by AccessRegCommand(), ChkSize(), and reg\_handle\_command().

Here is the caller graph for this function:



#### void Cair core regif::UpdateLocalVal (cuint addr)[protected]

Update bit value to local value

#### Returns:

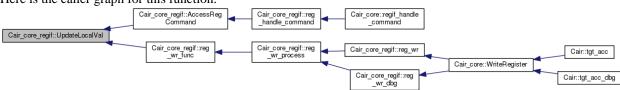
None

Definition at line 1136 of file air\_core\_regif.cpp.

```
References AIR DSELRO DSELO, AIR DSELRO DSEL1, AIR DSELRO DSEL10, AIR DSELRO DSEL11,
                      AIR_DSELR0_DSEL13,
                                                                   AIR_DSELR0_DSEL15,
AIR_DSELR0_DSEL12,
                                            AIR_DSELR0_DSEL14,
AIR_DSELR0_DSEL16,
                      AIR_DSELR0_DSEL17,
                                            AIR_DSELR0_DSEL18,
                                                                   AIR_DSELR0_DSEL19,
AIR_DSELR0_DSEL2,
                     AIR_DSELR0_DSEL20,
                                            AIR_DSELR0_DSEL21,
                                                                   AIR_DSELR0_DSEL22,
                      AIR_DSELR0_DSEL24,
AIR_DSELR0_DSEL23,
                                            AIR_DSELR0_DSEL25,
                                                                   AIR_DSELR0_DSEL26,
                      AIR_DSELR0_DSEL28,
                                             AIR_DSELR0_DSEL29,
AIR_DSELR0_DSEL27,
                                                                   AIR_DSELR0_DSEL3,
                      AIR DSELRO DSEL31,
AIR DSELRO DSEL30,
                                             AIR DSELRO DSEL4,
                                                                   AIR DSELRO DSEL5,
AIR DSELRO DSEL6,
                      AIR DSELRO DSEL7,
                                             AIR DSELRO DSEL8,
                                                                   AIR DSELRO DSEL9,
AIR DSELR1 DSEL32.
                      AIR DSELR1 DSEL33.
                                            AIR DSELR1 DSEL34,
                                                                   AIR DSELR1 DSEL35,
AIR DSELR1 DSEL36,
                      AIR DSELR1 DSEL37,
                                            AIR DSELR1 DSEL38,
                                                                   AIR DSELR1 DSEL39,
AIR DSELR1 DSEL40,
                      AIR DSELR1 DSEL41,
                                            AIR DSELR1 DSEL42,
                                                                   AIR DSELR1 DSEL43,
AIR_DSELR1_DSEL44,
                      AIR_DSELR1_DSEL45,
                                            AIR_DSELR1_DSEL46,
                                                                   AIR_DSELR1_DSEL47,
AIR_DSELR1_DSEL48,
                      AIR DSELR1 DSEL49,
                                            AIR DSELR1 DSEL50,
                                                                   AIR DSELR1 DSEL51,
AIR DSELR1 DSEL52,
                      AIR DSELR1 DSEL53,
                                            AIR DSELR1 DSEL54,
                                                                   AIR_DSELR1_DSEL55,
AIR_DSELR1_DSEL56,
                      AIR_DSELR1_DSEL57,
                                            AIR_DSELR1_DSEL58,
                                                                   AIR_DSELR1_DSEL59,
AIR_DSELR1_DSEL60,
                      AIR_DSELR1_DSEL61,
                                            AIR_DSELR1_DSEL62,
                                                                   AIR_DSELR1_DSEL63,
AIR_ISELR0_ISEL0,
                      AIR ISELRO ISEL1,
                                             AIR ISELRO ISEL10,
                                                                    AIR ISELRO ISEL11,
AIR_ISELR0_ISEL12,
                      AIR_ISELR0_ISEL13,
                                             AIR_ISELR0_ISEL14,
                                                                    AIR_ISELR0_ISEL15,
                      AIR ISELRO ISEL17,
                                             AIR ISELRO ISEL18,
                                                                    AIR ISELRO ISEL19,
AIR ISELRO ISEL16,
                      AIR ISELRO ISEL20,
                                             AIR ISELRO ISEL21,
AIR ISELRO ISEL2,
                                                                    AIR ISELRO ISEL22,
AIR_ISELR0_ISEL23,
                      AIR_ISELR0_ISEL24,
                                             AIR_ISELR0_ISEL25,
                                                                    AIR_ISELR0_ISEL26,
AIR ISELRO ISEL27,
                       AIR ISELRO ISEL28,
                                              AIR ISELRO ISEL29,
                                                                     AIR ISELRO ISEL3,
AIR_ISELR0_ISEL30,
                       AIR ISELRO ISEL31,
                                              AIR ISELRO ISEL4,
                                                                     AIR ISELRO ISEL5,
AIR_ISELR0_ISEL6,
                                                                     AIR_ISELR0_ISEL9,
                       AIR_ISELR0_ISEL7,
                                              AIR_ISELR0_ISEL8,
AIR ISELR1 ISEL32,
                      AIR ISELR1 ISEL33,
                                             AIR ISELR1 ISEL34,
                                                                    AIR ISELR1 ISEL35,
AIR_ISELR1_ISEL36,
                      AIR_ISELR1_ISEL37,
                                             AIR_ISELR1_ISEL38,
                                                                    AIR_ISELR1_ISEL39,
AIR_ISELR1_ISEL40,
                      AIR_ISELR1_ISEL41,
                                             AIR_ISELR1_ISEL42,
                                                                    AIR_ISELR1_ISEL43,
AIR ISELR1 ISEL44,
                      AIR ISELR1 ISEL45,
                                             AIR ISELR1 ISEL46,
                                                                    AIR ISELR1 ISEL47,
AIR_ISELR1_ISEL48,
                      AIR_ISELR1_ISEL49,
                                             AIR_ISELR1_ISEL50,
                                                                    AIR_ISELR1_ISEL51,
AIR_ISELR1_ISEL52,
                      AIR ISELR1 ISEL53,
                                             AIR_ISELR1_ISEL54,
                                                                    AIR_ISELR1_ISEL55,
AIR ISELR1 ISEL56,
                      AIR ISELR1 ISEL57,
                                             AIR ISELR1 ISEL58,
                                                                    AIR ISELR1 ISEL59,
AIR_ISELR1_ISEL60, AIR_ISELR1_ISEL61, AIR_ISELR1_ISEL62, and AIR_ISELR1_ISEL63.
```

Referenced by AccessRegCommand(), and reg wr func().

Here is the caller graph for this function:



#### void Cair\_core\_regif::UpdateRegVal (cuint addr) [protected]

Update local value to bit value

#### Returns:

None

Definition at line 1282 of file air\_core\_regif.cpp.

References AIR\_DSELR0\_DSEL0, AIR\_DSELR0\_DSEL1, AIR\_DSELR0\_DSEL10, AIR\_DSELR0\_DSEL11, AIR\_DSELR0\_DSEL12, AIR\_DSELR0\_DSEL13, AIR\_DSELR0\_DSEL14, AIR\_DSELR0\_DSEL15, AIR\_DSELR0\_DSEL16, AIR\_DSELR0\_DSEL17, AIR\_DSELR0\_DSEL18, AIR\_DSELR0\_DSEL19,

```
AIR_DSELR0_DSEL2,
                     AIR_DSELR0_DSEL20,
                                            AIR_DSELR0_DSEL21,
                                                                   AIR_DSELR0_DSEL22,
AIR DSELRO DSEL23,
                      AIR DSELRO DSEL24,
                                            AIR DSELRO DSEL25,
                                                                   AIR DSELRO DSEL26,
AIR_DSELR0_DSEL27,
                      AIR_DSELR0_DSEL28,
                                             AIR_DSELR0_DSEL29,
                                                                   AIR_DSELR0_DSEL3,
AIR DSELRO DSEL30,
                      AIR DSELRO DSEL31,
                                             AIR DSELRO DSEL4,
                                                                   AIR DSELRO DSEL5,
AIR DSELRO DSEL6,
                      AIR DSELRO DSEL7,
                                             AIR DSELRO DSEL8,
                                                                   AIR DSELRO DSEL9,
                      AIR_DSELR1_DSEL33,
AIR_DSELR1_DSEL32,
                                            AIR_DSELR1_DSEL34,
                                                                   AIR_DSELR1_DSEL35,
AIR DSELR1 DSEL36,
                      AIR DSELR1 DSEL37,
                                            AIR DSELR1 DSEL38,
                                                                   AIR DSELR1 DSEL39,
AIR DSELR1 DSEL40,
                      AIR DSELR1 DSEL41,
                                            AIR DSELR1 DSEL42,
                                                                   AIR DSELR1 DSEL43,
AIR_DSELR1_DSEL44,
                      AIR_DSELR1_DSEL45,
                                            AIR_DSELR1_DSEL46,
                                                                   AIR_DSELR1_DSEL47,
AIR_DSELR1_DSEL48,
                      AIR_DSELR1_DSEL49,
                                            AIR_DSELR1_DSEL50,
                                                                   AIR DSELR1 DSEL51,
AIR_DSELR1_DSEL52,
                      AIR DSELR1 DSEL53,
                                            AIR_DSELR1_DSEL54,
                                                                   AIR DSELR1 DSEL55,
AIR_DSELR1_DSEL56,
                      AIR_DSELR1_DSEL57,
                                            AIR_DSELR1_DSEL58,
                                                                   AIR_DSELR1_DSEL59,
                      AIR DSELR1 DSEL61,
                                                                   AIR DSELR1 DSEL63,
AIR DSELR1 DSEL60,
                                            AIR DSELR1 DSEL62,
                      AIR ISELRO ISEL1,
                                             AIR ISELRO ISEL10,
AIR ISELRO ISELO,
                                                                    AIR ISELRO ISEL11.
                      AIR ISELRO ISEL13,
                                             AIR ISELRO ISEL14,
                                                                    AIR ISELRO ISEL15,
AIR ISELRO ISEL12,
AIR ISELRO ISEL16,
                      AIR ISELRO ISEL17,
                                             AIR ISELRO ISEL18,
                                                                    AIR ISELRO ISEL19,
AIR ISELRO ISEL2,
                      AIR ISELRO ISEL20,
                                             AIR ISELRO ISEL21,
                                                                    AIR ISELRO ISEL22,
AIR_ISELR0_ISEL23,
                      AIR_ISELR0_ISEL24,
                                             AIR_ISELR0_ISEL25,
                                                                    AIR_ISELR0_ISEL26,
AIR ISELRO ISEL27,
                       AIR ISELRO ISEL28,
                                              AIR ISELRO ISEL29,
                                                                     AIR ISELRO ISEL3,
AIR_ISELR0_ISEL30,
                       AIR_ISELR0_ISEL31,
                                              AIR_ISELR0_ISEL4,
                                                                     AIR_ISELR0_ISEL5,
                       AIR ISELRO ISEL7,
                                              AIR ISELRO ISEL8,
                                                                     AIR ISELRO ISEL9,
AIR ISELRO ISEL6,
AIR_ISELR1_ISEL32,
                      AIR ISELR1 ISEL33,
                                             AIR_ISELR1_ISEL34,
                                                                    AIR ISELR1 ISEL35,
                                             AIR_ISELR1_ISEL38,
AIR_ISELR1_ISEL36,
                      AIR_ISELR1_ISEL37,
                                                                    AIR_ISELR1_ISEL39,
                      AIR_ISELR1_ISEL41,
                                                                    AIR_ISELR1_ISEL43,
AIR_ISELR1_ISEL40,
                                             AIR_ISELR1_ISEL42,
AIR ISELR1 ISEL44,
                      AIR ISELR1 ISEL45,
                                             AIR ISELR1 ISEL46,
                                                                    AIR ISELR1 ISEL47,
AIR_ISELR1_ISEL48,
                      AIR_ISELR1_ISEL49,
                                             AIR_ISELR1_ISEL50,
                                                                    AIR_ISELR1_ISEL51,
AIR_ISELR1_ISEL52,
                      AIR_ISELR1_ISEL53,
                                             AIR_ISELR1_ISEL54,
                                                                    AIR_ISELR1_ISEL55,
AIR ISELR1 ISEL56,
                      AIR ISELR1 ISEL57,
                                             AIR ISELR1 ISEL58,
                                                                    AIR ISELR1 ISEL59,
AIR_ISELR1_ISEL60, AIR_ISELR1_ISEL61, AIR_ISELR1_ISEL62, and AIR_ISELR1_ISEL63.
```

#### void Cair\_core\_regif::wr\_cb (cuint addr, uint data)[protected]

write callback function of CoWare

#### Returns:

none

Definition at line 1529 of file air\_core\_regif.cpp.

#### **Member Data Documentation**

vpcl::re register\* Cair core regif::AIR DSELR0[protected]

Definition at line 297 of file air\_core\_regif.h.

Referenced by Cair core::Cair core(), and Cair core regif().

#### uint Cair\_core\_regif::AIR\_DSELR0\_DSEL0[protected]

Definition at line 399 of file air\_core\_regif.h.

#### uint Cair\_core\_regif::AIR\_DSELR0\_DSEL1 [protected]

Definition at line 398 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR0\_DSEL10[protected]

Definition at line 389 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR0\_DSEL11[protected]

Definition at line 388 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR0\_DSEL12[protected]

Definition at line 387 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR0\_DSEL13[protected]

Definition at line 386 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR0\_DSEL14[protected]

Definition at line 385 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR0\_DSEL15[protected]

Definition at line 384 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR0\_DSEL16[protected]

Definition at line 383 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR0\_DSEL17[protected]

Definition at line 382 of file air\_core\_regif.h.

#### uint Cair\_core\_regif::AIR\_DSELR0\_DSEL18[protected]

Definition at line 381 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR0\_DSEL19[protected]

Definition at line 380 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR0\_DSEL2[protected]

Definition at line 397 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR0\_DSEL20[protected]

Definition at line 379 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR0\_DSEL21 [protected]

Definition at line 378 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR0\_DSEL22[protected]

Definition at line 377 of file air core regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair core regif::AIR DSELR0 DSEL23[protected]

Definition at line 376 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR0\_DSEL24[protected]

Definition at line 375 of file air\_core\_regif.h.

#### uint Cair\_core\_regif::AIR\_DSELR0\_DSEL25[protected]

Definition at line 374 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR0\_DSEL26[protected]

Definition at line 373 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR0\_DSEL27[protected]

Definition at line 372 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR0\_DSEL28[protected]

Definition at line 371 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR0\_DSEL29[protected]

Definition at line 370 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR0\_DSEL3 [protected]

Definition at line 396 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR0\_DSEL30[protected]

Definition at line 369 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR0\_DSEL31 [protected]

Definition at line 368 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR0\_DSEL4[protected]

Definition at line 395 of file air\_core\_regif.h.

#### uint Cair\_core\_regif::AIR\_DSELR0\_DSEL5[protected]

Definition at line 394 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR0\_DSEL6[protected]

Definition at line 393 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR0\_DSEL7[protected]

Definition at line 392 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR0\_DSEL8[protected]

Definition at line 391 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR0\_DSEL9[protected]

Definition at line 390 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### vpcl::re\_register\* Cair\_core\_regif::AIR\_DSELR1[protected]

Definition at line 298 of file air core regif.h.

Referenced by Cair core::Cair core(), and Cair core regif().

#### uint Cair core regif::AIR DSELR1 DSEL32[protected]

Definition at line 431 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR1\_DSEL33[protected]

Definition at line 430 of file air\_core\_regif.h.

# uint Cair\_core\_regif::AIR\_DSELR1\_DSEL34[protected] Definition at line 429 of file air\_core\_regif.h. Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal(). uint Cair\_core\_regif::AIR\_DSELR1\_DSEL35[protected] Definition at line 428 of file air\_core\_regif.h. Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal(). uint Cair\_core\_regif::AIR\_DSELR1\_DSEL36[protected] Definition at line 427 of file air core regif.h. Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal(). uint Cair\_core\_regif::AIR\_DSELR1\_DSEL37[protected] Definition at line 426 of file air\_core\_regif.h. Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal(). uint Cair\_core\_regif::AIR\_DSELR1\_DSEL38[protected] Definition at line 425 of file air\_core\_regif.h. Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal(). uint Cair\_core\_regif::AIR\_DSELR1\_DSEL39[protected] Definition at line 424 of file air\_core\_regif.h. Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal(). uint Cair\_core\_regif::AIR\_DSELR1\_DSEL40[protected] Definition at line 423 of file air\_core\_regif.h. Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal(). uint Cair\_core\_regif::AIR\_DSELR1\_DSEL41[protected] Definition at line 422 of file air\_core\_regif.h. Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

Definition at line 421 of file air\_core\_regif.h.

uint Cair\_core\_regif::AIR\_DSELR1\_DSEL42[protected]

#### uint Cair\_core\_regif::AIR\_DSELR1\_DSEL43[protected]

Definition at line 420 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR1\_DSEL44[protected]

Definition at line 419 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR1\_DSEL45[protected]

Definition at line 418 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR1\_DSEL46[protected]

Definition at line 417 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR1\_DSEL47[protected]

Definition at line 416 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR1\_DSEL48[protected]

Definition at line 415 of file air core regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair core regif::AIR DSELR1 DSEL49[protected]

Definition at line 414 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR1\_DSEL50[protected]

Definition at line 413 of file air\_core\_regif.h.

#### uint Cair\_core\_regif::AIR\_DSELR1\_DSEL51 [protected]

Definition at line 412 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR1\_DSEL52[protected]

Definition at line 411 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR1\_DSEL53[protected]

Definition at line 410 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR1\_DSEL54[protected]

Definition at line 409 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR1\_DSEL55[protected]

Definition at line 408 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR1\_DSEL56[protected]

Definition at line 407 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR1\_DSEL57[protected]

Definition at line 406 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR1\_DSEL58[protected]

Definition at line 405 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR1\_DSEL59[protected]

Definition at line 404 of file air\_core\_regif.h.

#### uint Cair\_core\_regif::AIR\_DSELR1\_DSEL60[protected]

Definition at line 403 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR1\_DSEL61 [protected]

Definition at line 402 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR1\_DSEL62[protected]

Definition at line 401 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_DSELR1\_DSEL63[protected]

Definition at line 400 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### vpcl::re\_register\* Cair\_core\_regif::AIR\_ISELR0 [protected]

Definition at line 295 of file air\_core\_regif.h.

Referenced by Cair\_core::Cair\_core(), and Cair\_core\_regif().

#### uint Cair\_core\_regif::AIR\_ISELR0\_ISEL0[protected]

Definition at line 335 of file air core regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair core regif::AIR ISELR0 ISEL1[protected]

Definition at line 334 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR0\_ISEL10[protected]

Definition at line 325 of file air\_core\_regif.h.

#### uint Cair\_core\_regif::AIR\_ISELR0\_ISEL11[protected]

Definition at line 324 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR0\_ISEL12[protected]

Definition at line 323 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR0\_ISEL13[protected]

Definition at line 322 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR0\_ISEL14[protected]

Definition at line 321 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR0\_ISEL15[protected]

Definition at line 320 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR0\_ISEL16[protected]

Definition at line 319 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR0\_ISEL17[protected]

Definition at line 318 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR0\_ISEL18[protected]

Definition at line 317 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR0\_ISEL19[protected]

Definition at line 316 of file air\_core\_regif.h.

#### uint Cair\_core\_regif::AIR\_ISELR0\_ISEL2[protected]

Definition at line 333 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR0\_ISEL20[protected]

Definition at line 315 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR0\_ISEL21[protected]

Definition at line 314 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR0\_ISEL22[protected]

Definition at line 313 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR0\_ISEL23[protected]

Definition at line 312 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR0\_ISEL24[protected]

Definition at line 311 of file air core regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair core regif::AIR ISELRO ISEL25[protected]

Definition at line 310 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR0\_ISEL26[protected]

Definition at line 309 of file air\_core\_regif.h.

#### uint Cair\_core\_regif::AIR\_ISELR0\_ISEL27 [protected]

Definition at line 308 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR0\_ISEL28[protected]

Definition at line 307 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR0\_ISEL29[protected]

Definition at line 306 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR0\_ISEL3[protected]

Definition at line 332 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR0\_ISEL30[protected]

Definition at line 305 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR0\_ISEL31 [protected]

Definition at line 304 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR0\_ISEL4[protected]

Definition at line 331 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR0\_ISEL5[protected]

Definition at line 330 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR0\_ISEL6 [protected]

Definition at line 329 of file air\_core\_regif.h.

#### uint Cair\_core\_regif::AIR\_ISELR0\_ISEL7[protected]

Definition at line 328 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR0\_ISEL8[protected]

Definition at line 327 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR0\_ISEL9 [protected]

Definition at line 326 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### vpcl::re\_register\* Cair\_core\_regif::AIR\_ISELR1 [protected]

Definition at line 296 of file air\_core\_regif.h.

Referenced by Cair\_core::Cair\_core(), and Cair\_core\_regif().

#### uint Cair\_core\_regif::AIR\_ISELR1\_ISEL32[protected]

Definition at line 367 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR1\_ISEL33[protected]

Definition at line 366 of file air core regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair core regif::AIR ISELR1 ISEL34[protected]

Definition at line 365 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR1\_ISEL35[protected]

Definition at line 364 of file air\_core\_regif.h.

#### uint Cair\_core\_regif::AIR\_ISELR1\_ISEL36[protected]

Definition at line 363 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR1\_ISEL37 [protected]

Definition at line 362 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR1\_ISEL38[protected]

Definition at line 361 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR1\_ISEL39[protected]

Definition at line 360 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR1\_ISEL40[protected]

Definition at line 359 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR1\_ISEL41 [protected]

Definition at line 358 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR1\_ISEL42[protected]

Definition at line 357 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR1\_ISEL43[protected]

Definition at line 356 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR1\_ISEL44[protected]

Definition at line 355 of file air\_core\_regif.h.

#### uint Cair\_core\_regif::AIR\_ISELR1\_ISEL45[protected]

Definition at line 354 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR1\_ISEL46[protected]

Definition at line 353 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR1\_ISEL47[protected]

Definition at line 352 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR1\_ISEL48[protected]

Definition at line 351 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR1\_ISEL49[protected]

Definition at line 350 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR1\_ISEL50[protected]

Definition at line 349 of file air core regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair core regif::AIR ISELR1 ISEL51[protected]

Definition at line 348 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR1\_ISEL52[protected]

Definition at line 347 of file air\_core\_regif.h.

#### uint Cair\_core\_regif::AIR\_ISELR1\_ISEL53[protected]

Definition at line 346 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR1\_ISEL54[protected]

Definition at line 345 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR1\_ISEL55[protected]

Definition at line 344 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR1\_ISEL56[protected]

Definition at line 343 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR1\_ISEL57 [protected]

Definition at line 342 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR1\_ISEL58[protected]

Definition at line 341 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR1\_ISEL59[protected]

Definition at line 340 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR1\_ISEL60[protected]

Definition at line 339 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR1\_ISEL61[protected]

Definition at line 338 of file air\_core\_regif.h.

#### uint Cair\_core\_regif::AIR\_ISELR1\_ISEL62[protected]

Definition at line 337 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### uint Cair\_core\_regif::AIR\_ISELR1\_ISEL63[protected]

Definition at line 336 of file air\_core\_regif.h.

Referenced by InitLocalVal(), UpdateLocalVal(), and UpdateRegVal().

#### bool Cair\_core\_regif::mAPBAccessMode[protected]

Definition at line 454 of file air\_core\_regif.h.

Referenced by ChkSize(), CommandInit(), reg\_handle\_command(), and reg\_rd\_process().

#### uint Cair\_core\_regif::mBusByteWidth[protected]

Definition at line 450 of file air\_core\_regif.h.

Referenced by Cair\_core\_regif(), ChkSize(), reg\_rd(), reg\_rd\_dbg(), reg\_rd\_process(), reg\_wr(), reg\_wr\_dbg(), and reg\_wr\_process().

#### uint Cair\_core\_regif::mBusWidth[protected]

Definition at line 451 of file air\_core\_regif.h.

Referenced by Cair\_core\_regif().

#### struct Cair\_core\_regif::SRegList \* Cair\_core\_regif::mCurReg [protected]

Referenced by Cair\_core\_regif(), first\_reg\_object(), and next\_reg\_object().

#### bool Cair\_core\_regif::mDumpBitInfo[protected]

Definition at line 455 of file air\_core\_regif.h.

Referenced by CommandInit(), reg\_handle\_command(), reg\_rd\_func(), and reg\_wr\_func().

#### bool Cair\_core\_regif::mDumpRegisterRW [protected]

Definition at line 453 of file air\_core\_regif.h.

Referenced by CommandInit(), DumpRegMsg(), and reg\_handle\_command().

#### std::string Cair\_core\_regif::mFileName[protected]

Definition at line 459 of file air\_core\_regif.h.

Referenced by \_re\_printf(), Cair\_core\_regif(), and get\_fileline().

#### std::string Cair\_core\_regif::mInstName[protected]

Definition at line 460 of file air\_core\_regif.h.

Referenced by \_re\_printf(), AccessRegCommand(), Cair\_core\_regif(), reg\_handle\_command(), Cair\_core::regif\_handle\_command(), and set\_instance\_name().

#### bool Cair\_core\_regif::mlsReset[protected]

Definition at line 452 of file air\_core\_regif.h.

Referenced by Cair\_core\_regif(), EnableReset(), reg\_rd\_func(), and reg\_wr\_process().

#### int Cair\_core\_regif::mLineNum[protected]

Definition at line 461 of file air\_core\_regif.h.

Referenced by re printf(), Cair core regif(), and get fileline().

#### std::map<std::string, bool> Cair\_core\_regif::mMessageLevel [protected]

Definition at line 456 of file air\_core\_regif.h.

Referenced by \_re\_printf(), CommandInit(), and reg\_handle\_command().

# std::map<std::string, std::map<std::string, void (Cair\_core\_regif::\*) (<a href="RegCBstr">RegCBstr</a>)> > Cair\_core\_regif::mRdCbAPI[protected]

Definition at line 458 of file air\_core\_regif.h.

Referenced by Cair\_core\_regif().

#### struct Cair\_core\_regif::SRegList \*\* Cair\_core\_regif::mRegArray [protected]

Referenced by Cair\_core\_regif(), EnableReset(), reg\_handle\_command(), reg\_rd\_func(), reg\_rd\_process(), reg\_wr\_func(), reg\_wr\_process(), and ~Cair\_core\_regif().

#### struct <u>Cair core regif::SRegList</u> \* Cair\_core\_regif::mRegList[protected]

Referenced by Cair\_core\_regif(), and first\_reg\_object().

#### uint\* Cair core regif::mRegMap[protected]

```
Definition at line 287 of file air_core_regif.h.
```

Referenced by Cair\_core\_regif(), get\_reg\_index(), and ~Cair\_core\_regif().

#### uint Cair\_core\_regif::mTotalRegNum[protected]

```
Definition at line 288 of file air_core_regif.h.
```

Referenced by Cair\_core\_regif(), EnableReset(), reg\_handle\_command(), and ~Cair\_core\_regif().

# std::map<std::string, std::map<std::string, void (Cair\_core\_regif::\*) (<a href="RegCBstr">RegCBstr</a>)> > Cair\_core\_regif::mWrCbAPI[protected]

Definition at line 457 of file air\_core\_regif.h.

Referenced by Cair\_core\_regif().

#### The documentation for this class was generated from the following files:

- air core regif.h
- <u>air\_core\_regif.cpp</u>

## Cair\_core\_regif::RegCBstr Struct Reference

#include <air\_core\_regif.h>

Collaboration diagram for Cair\_core\_regif::RegCBstr:

# Cair\_core\_regif::RegCBstr + channel + is\_wr + size + pre\_data + data + RegCBstr()

#### **Public Member Functions**

• RegCBstr (uint channel, bool is\_wr, uint size, uint pre\_data, uint data)

#### **Public Attributes**

- <u>uint channel</u>
- bool <u>is wr</u>
- <u>uint size</u>
- uint pre\_data
- <u>uint</u> <u>data</u>

#### **Detailed Description**

Definition at line 228 of file air\_core\_regif.h.

#### **Constructor & Destructor Documentation**

Cair\_core\_regif::RegCBstr::RegCBstr (<u>uint</u> channel, bool is\_wr, <u>uint</u> size, <u>uint</u> pre\_data, <u>uint</u> data)[inline]

Definition at line 234 of file air\_core\_regif.h.

References channel, data, is\_wr, pre\_data, and size.

#### **Member Data Documentation**

uint Cair\_core\_regif::RegCBstr::channel

Definition at line 229 of file air\_core\_regif.h.

Referenced by RegCBstr().

#### uint Cair\_core\_regif::RegCBstr::data

Definition at line 233 of file air\_core\_regif.h. Referenced by RegCBstr().

#### bool Cair\_core\_regif::RegCBstr::is\_wr

Definition at line 230 of file air\_core\_regif.h. Referenced by RegCBstr().

#### uint Cair\_core\_regif::RegCBstr::pre\_data

Definition at line 232 of file air\_core\_regif.h. Referenced by RegCBstr().

#### uint Cair\_core\_regif::RegCBstr::size

Definition at line 231 of file air\_core\_regif.h. Referenced by RegCBstr().

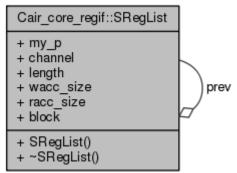
#### The documentation for this struct was generated from the following file:

• <u>air\_core\_regif.h</u>

## Cair\_core\_regif::SRegList Struct Reference

#include <air core regif.h>

Collaboration diagram for Cair\_core\_regif::SRegList:



#### **Public Member Functions**

- <u>SRegList</u> (vpcl::re\_register \*\_my\_p, <u>SRegList</u> \*\_prev, <u>uint</u> \_channel, <u>uint</u> \_length, std::string \_wacc\_size, std::string \_racc\_size, bool \_block=false)
- <u>~SRegList</u> ()

#### **Public Attributes**

- vpcl::re\_register \* my\_p
- <u>SRegList</u> \* <u>prev</u>
- uint channel
- <u>uint length</u>
- std::string <u>wacc\_size</u>
- std::string <u>racc\_size</u>
- bool block

#### **Detailed Description**

Definition at line 243 of file air\_core\_regif.h.

#### **Constructor & Destructor Documentation**

Cair\_core\_regif::SRegList::SRegList (vpcl::re\_register \* \_my\_p, <u>SRegList</u> \* \_prev, <u>uint</u> \_channel, <u>uint</u> \_length, std::string \_wacc\_size, std::string \_racc\_size, bool \_block = false)[inline]

Definition at line 264 of file air\_core\_regif.h.

References block, channel, length, my\_p, prev, racc\_size, and wacc\_size.

#### Cair\_core\_regif::SRegList::~SRegList()[inline]

Definition at line 280 of file air\_core\_regif.h.

#### **Member Data Documentation**

#### bool Cair\_core\_regif::SRegList::block

Definition at line 250 of file air\_core\_regif.h.

Referenced by SRegList().

#### uint Cair\_core\_regif::SRegList::channel

Definition at line 246 of file air\_core\_regif.h.

Referenced by SRegList().

#### uint Cair\_core\_regif::SRegList::length

Definition at line 247 of file air\_core\_regif.h.

Referenced by Cair\_core\_regif::reg\_rd\_func(), Cair\_core\_regif::reg\_rd\_process(), Cair\_core\_regif::reg\_wr\_func(), Cair\_core\_regif::reg\_wr\_process(), and SRegList().

#### vpcl::re\_register\* Cair\_core\_regif::SRegList::my\_p

Definition at line 244 of file air\_core\_regif.h.

Referenced by Cair\_core\_regif::Cair\_core\_regif(), Cair\_core\_regif::EnableReset(), Cair\_core\_regif::first\_reg\_object(), Cair\_core\_regif::next\_reg\_object(), Cair\_core\_regif::reg\_wr\_func(), Cair\_core\_regif::reg\_wr\_process(), Cair\_core\_regif::reg\_wr\_process(), SRegList(), and Cair\_core\_regif::~Cair\_core\_regif().

#### <u>SRegList</u>\* Cair\_core\_regif::SRegList::prev

Definition at line 245 of file air core regif.h.

Referenced by Cair\_core\_regif::next\_reg\_object(), SRegList(), and Cair\_core\_regif::~Cair\_core\_regif().

#### std::string Cair\_core\_regif::SRegList::racc\_size

Definition at line 249 of file air\_core\_regif.h.

Referenced by SRegList().

#### std::string Cair\_core\_regif::SRegList::wacc\_size

Definition at line 248 of file air\_core\_regif.h.

Referenced by SRegList().

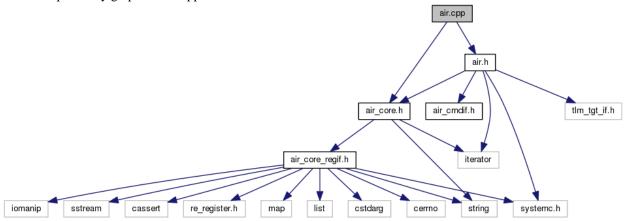
The documentation for this struct was generated from the following file:

• <u>air core regif.h</u>

## **File Documentation**

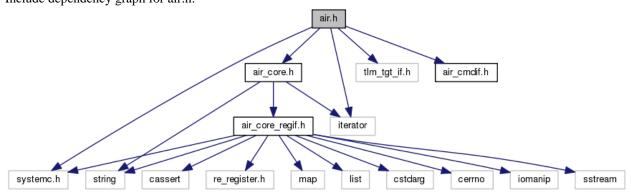
## air.cpp File Reference

#include "air.h"
#include "air\_core.h"
Include dependency graph for air.cpp:

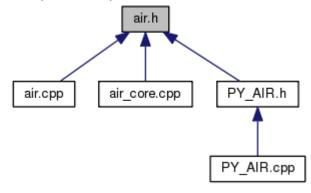


## air.h File Reference

```
#include "systemc.h"
#include "tlm_tgt_if.h"
#include "air_core.h"
#include <iterator>
#include "air_cmdif.h"
Include dependency graph for air.h:
```



This graph shows which files directly or indirectly include this file:

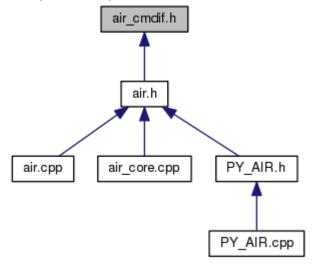


#### **Classes**

• class <u>Cair</u> AIR model class.

## air\_cmdif.h File Reference

This graph shows which files directly or indirectly include this file:



#### **Macros**

• #define re\_printf get\_fileline(\_\_FILE\_\_, \_\_LINE\_\_); re\_printf

#### **Functions**

- std::string <u>handleCommand</u> (const std::vector< std::string > &args)
- std::string <u>own\_handle\_command</u> (std::vector< std::string > &args)
- std::string <u>user\_def\_command</u> (std::vector< std::string > &args)
- bool str2num (std::string str, bool &num)
- bool <u>str2num</u> (std::string str, char &num)
- bool <u>str2num</u> (std::string str, unsigned char &num)
- template<typename T > bool str2num (std::string str, T &num)
- bool <u>str2dbl</u> (std::string str, double &num)
- void CommandInit (std::string name="")
- std::vector< std::string > str2vec (std::string str, const char sep)
- void <u>re\_printf</u> (std::string group, const char \*message,...)
- void <a href="mailto:get\_fileline">get\_fileline</a> (std::string filename, int line\_number)
- bool <u>strmatch</u> (const char \*ptn, const char \*str)

#### **Variables**

- std::map< std::string, bool > mMessageLevel
- std::string mInstName
- std::string <u>mFileName</u>
- std::string mCmdId
- int <u>mLineNum</u>

#### **Macro Definition Documentation**

#### #define re\_printf <a href="mailto:get\_fileline">get\_fileline</a>(\_\_FILE\_\_\_, \_\_LINE\_\_\_); <a href="mailto:re\_printf">re\_printf</a>

Definition at line 889 of file air cmdif.h.

Referenced by Cair::AssertReset(), Cair::PortResetMethod(), Cair::ReceiveInputMethod(), Cair::SetCLKfreq(), Cair::WriteINTAIRDMAREQMethod(), and Cair::WriteINTAIRINTREQMethod().

#### **Function Documentation**

#### void \_re\_printf (std::string group, const char \* message, ...)[private]

Definition at line 744 of file air\_cmdif.h.

References mFileName, mInstName, mLineNum, and mMessageLevel.

#### void CommandInit (std::string name = "")[private]

Definition at line 694 of file air\_cmdif.h.

References mCmdId, mFileName, mInstName, mLineNum, and mMessageLevel.

#### void get\_fileline (std::string filename, int line\_number) [private]

Definition at line 878 of file air\_cmdif.h.

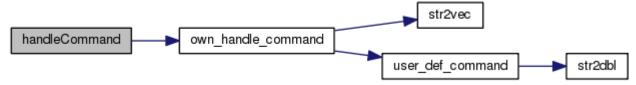
References mFileName, and mLineNum.

#### std::string handleCommand (const std::vector< std::string > & args)

Definition at line 44 of file air\_cmdif.h.

References mCmdId, mInstName, and own\_handle\_command().

Here is the call graph for this function:



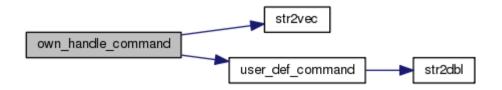
#### std::string own\_handle\_command (std::vector< std::string > & args) [private]

Definition at line 161 of file air\_cmdif.h.

References mInstName, mMessageLevel, str2vec(), and user\_def\_command().

Referenced by handleCommand().

Here is the call graph for this function:



Here is the caller graph for this function:



#### bool str2dbl (std::string str, double & num)[private]

Definition at line 678 of file air\_cmdif.h.

Referenced by user\_def\_command().

Here is the caller graph for this function:



#### bool str2num (std::string str, bool & num)[private]

Definition at line 571 of file air\_cmdif.h.

#### bool str2num (std::string str, char & num)[private]

Definition at line 605 of file air\_cmdif.h.

#### bool str2num (std::string str, unsigned char & num)[private]

Definition at line 629 of file air\_cmdif.h.

#### template<typename T > bool str2num (std::string str, T & num)[private]

Definition at line 654 of file air\_cmdif.h.

#### std::vector<std::string> str2vec (std::string str, const char sep)[private]

Definition at line 725 of file air cmdif.h.

Referenced by own\_handle\_command().

Here is the caller graph for this function:



#### bool strmatch (const char \* ptn, const char \* str)[private]

Definition at line 892 of file air\_cmdif.h.

#### std::string user\_def\_command (std::vector< std::string > & args)[private]

Definition at line 418 of file air\_cmdif.h.

References mInstName, and str2dbl().

Referenced by own\_handle\_command().

Here is the call graph for this function:



Here is the caller graph for this function:



#### **Variable Documentation**

#### std::string mCmdld

Definition at line 929 of file air\_cmdif.h.

Referenced by CommandInit(), and handleCommand().

#### std::string mFileName

Definition at line 928 of file air\_cmdif.h.

Referenced by \_re\_printf(), CommandInit(), and get\_fileline().

#### std::string mlnstName

Definition at line 927 of file air\_cmdif.h.

 $Referenced \quad by \quad \_re\_printf(), \quad CommandInit(), \quad handleCommand(), \quad own\_handle\_command(), \quad and \\ user\_def\_command().$ 

#### int mLineNum

Definition at line 930 of file air\_cmdif.h.

Referenced by \_re\_printf(), CommandInit(), and get\_fileline().

#### std::map<std::string, bool> mMessageLevel

Definition at line 920 of file air\_cmdif.h.

 $Referenced\ by\ \_re\_printf(),\ CommandInit(),\ and\ own\_handle\_command().$ 

# air\_core.cpp File Reference

cstdarg

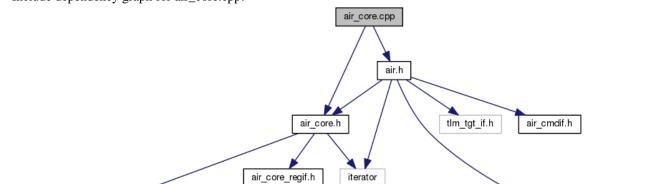
cerrno

list

map

string

#include "air\_core.h"
#include "air.h"
Include dependency graph for air\_core.cpp:



iomanip

sstream

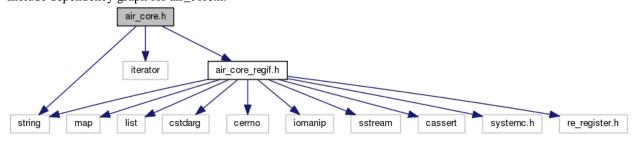
cassert

re\_register.h

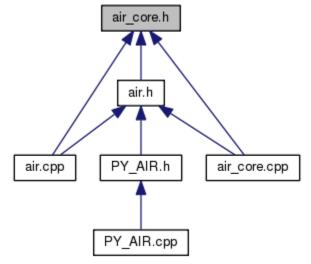
systemc.h

## air\_core.h File Reference

```
#include <string>
#include <iterator>
#include "air_core_regif.h"
Include dependency graph for air_core.h:
```



This graph shows which files directly or indirectly include this file:



### **Classes**

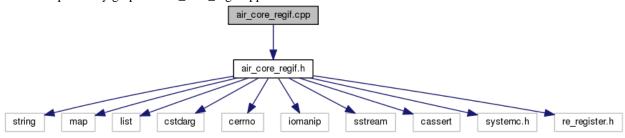
- class <u>Cair\_core</u>
- AIR\_CORE class. struct Cair core::air mux table

## air\_core\_regif.cpp File Reference

Register IF class of model AIR\_CORE \$Id\$ \$Date\$ \$Revison\$ \$Author\$.

#include "air core regif.h"

Include dependency graph for air\_core\_regif.cpp:



#### **Macros**

• #define re printf get fileline(\_\_FILE\_\_, \_\_LINE\_\_); re printf

### **Detailed Description**

Register IF class of model AIR\_CORE \$Id\$ \$Date\$ \$Revison\$ \$Author\$.

Definition in file air core regif.cpp.

#### **Macro Definition Documentation**

#define re\_printf get\_fileline(\_\_FILE\_\_, \_\_LINE\_\_); \_re\_printf

Definition at line 197 of file air\_core\_regif.cpp.

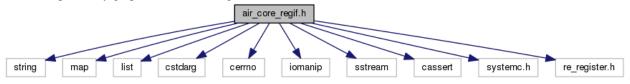
Referenced by Cair\_core\_regif::ChkSize(), Cair\_core\_regif::DumpRegMsg(), Cair\_core\_regif::EnableReset(), Cair\_core\_regif::reg\_rd\_dbg(), Cair\_core\_regif::reg\_rd\_func(), Cair\_core\_regif::reg\_rd\_func(), Cair\_core\_regif::reg\_wr\_dbg(), Cair\_core\_regif::reg\_wr\_func(), and Cair\_core\_regif::reg\_wr\_process().

## air\_core\_regif.h File Reference

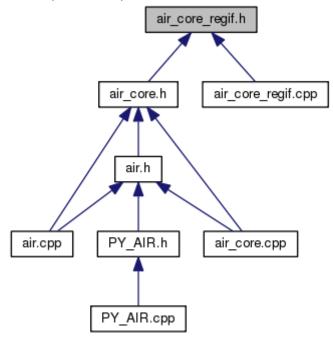
Register IF class of model AIR\_CORE \$Id\$ \$Date\$ \$Revison\$ \$Author\$.

```
#include <string>
#include <map>
#include <list>
#include <cstdarg>
#include <cerrno>
#include <iomanip>
#include <sstream>
#include <cassert>
#include "systemc.h"
#include "re register.h"
```

Include dependency graph for air\_core\_regif.h:



This graph shows which files directly or indirectly include this file:



#### **Classes**

- class Cair core regif
- Register IF class of AIR\_CORE model. struct Cair core regif::RegCBstr
- struct Cair\_core\_regif::SRegList

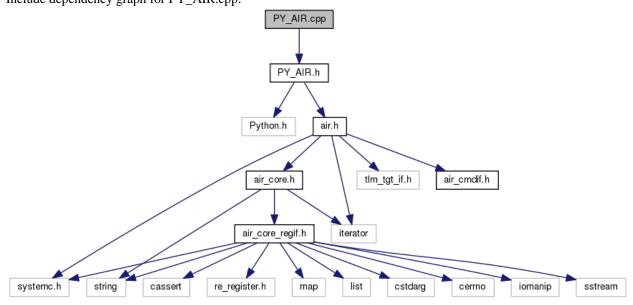
## **Detailed Description**

Register IF class of model AIR\_CORE \$Id\$ \$Date\$ \$Revison\$ \$Author\$.

Definition in file air core regif.h.

## **PY\_AIR.cpp File Reference**

#include "PY\_AIR.h"
Include dependency graph for PY\_AIR.cpp:



## **Namespaces**

• namespace PY\_AIR

#### **Functions**

- void PY AIR::SetPvExtCmd (void)
- void PY AIR::SeparateString (std::vector< std::string > &vtr, const std::string msg)
- void <a href="PY\_AIR::ProcessCommand">PY\_AIR::ProcessCommand</a> (const std::string cmd\_id, const std::string cmd\_name, char \*token, char \*input\_arg)
- PyObject \* PY AIR::DumpStatInfoPy (PyObject \*self, PyObject \*args)
- PyObject \* PY AIR::SetCLKfreqPy (PyObject \*self, PyObject \*args)
- PyObject \* PY\_AIR::tgtPy (PyObject \*self, PyObject \*args)
- PyObject \* PY\_AIR::regPy (PyObject \*self, PyObject \*args)
- PyObject \* PY\_AIR::MessageLevelPy (PyObject \*self, PyObject \*args)
- PyObject \* PY AIR::helpPy (PyObject \*self, PyObject \*args)
- PyObject \* PY\_AIR::AssertResetPy (PyObject \*self, PyObject \*args)

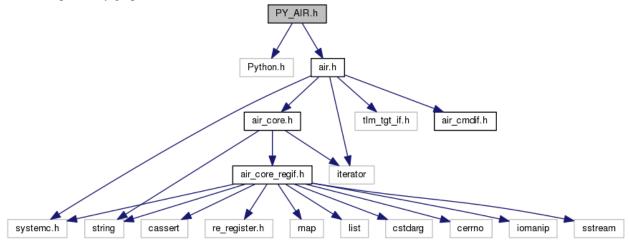
### **Variables**

• PyMethodDef PY AIR::mShApiDef []

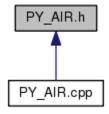
## PY\_AIR.h File Reference

#include <Python.h>
#include "air.h"

Include dependency graph for PY\_AIR.h:



This graph shows which files directly or indirectly include this file:



### **Namespaces**

namespace <u>PY\_AIR</u>

### **Macros**

• #define PY INITMODULE NAME "SCHEAP"

#### **Functions**

- PyObject \* PY AIR::DumpStatInfoPy (PyObject \*self, PyObject \*args)
- PyObject \* PY AIR::SetCLKfreqPy (PyObject \*self, PyObject \*args)
- PyObject \* PY\_AIR::tgtPy (PyObject \*self, PyObject \*args)
- PyObject \* PY AIR::regPy (PyObject \*self, PyObject \*args)
- PyObject \* PY\_AIR::MessageLevelPy (PyObject \*self, PyObject \*args)
- PyObject \* PY AIR::helpPy (PyObject \*self, PyObject \*args)
- PyObject \* PY\_AIR::AssertResetPy (PyObject \*self, PyObject \*args)
- void PY\_AIR::SetPyExtCmd (void)
- void PY AIR::SeparateString (std::vector< std::string > &vtr, const std::string msg)
- void <a href="PY\_AIR::ProcessCommand">PY\_AIR::ProcessCommand</a> (const std::string cmd\_id, const std::string cmd\_name, char \*token, char \*input\_arg)

## **Macro Definition Documentation**

## #define PY\_INITMODULE\_NAME "SCHEAP"

Definition at line 17 of file PY\_AIR.h.
Referenced by PY\_AIR::SetPyExtCmd().

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