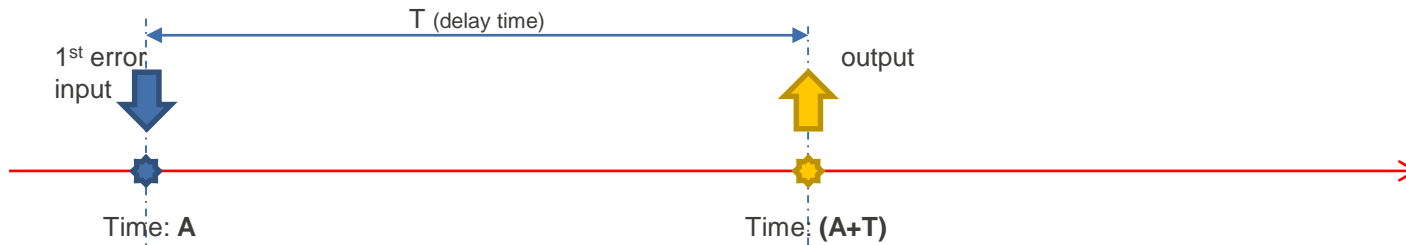
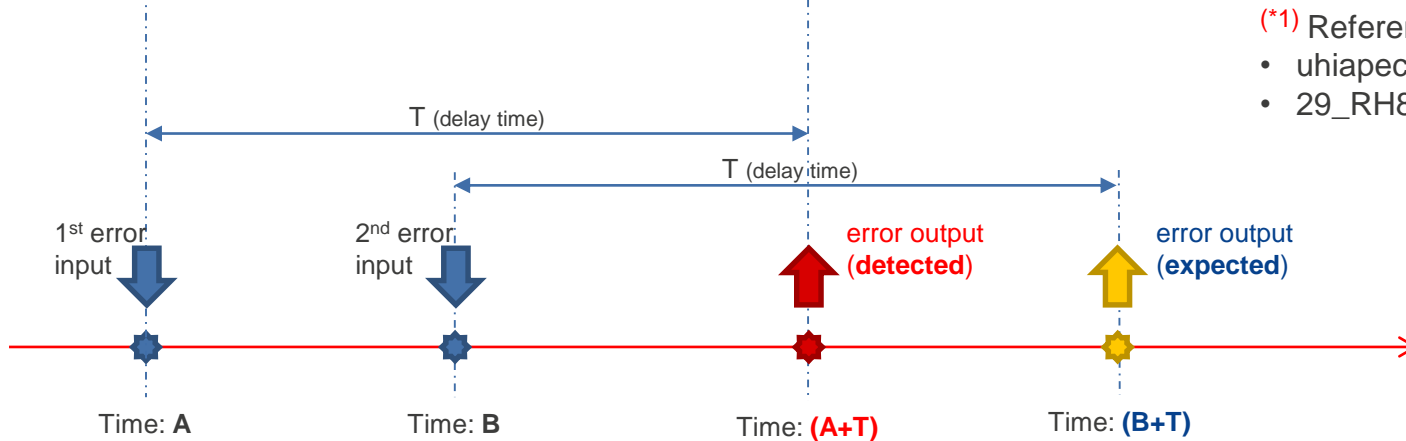


BUG DESCRIPTION

- ❑ After an error input asserts, after a delay time (configured by ECMDTMCMP register), an error output is issued.



- ❑ If another error input asserts during the delay time of the first error input, the delay time should be reset to 0 and count again^(*), but it is not reset and the delay time is shorter than expected.



(*) Reference:

- uhiapecm0020_target_specification_ver1.pdf /Chapter 4.3.6
- 29_RH850_CCC_TS_ECM.pdf, Chapter 29.3.11