**Nham Xuan Tung**

Junior Engineer

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**SUMMARY**

I’m an engineer with 2 years of experience in Semiconductor Industry, with mindset is to gain knowledge and develop skillset day by day, to become a good engineer to bring a lot value for company and customer. Learn new technologies is what I am passionate about.

Seeking for a position in an environment of growth and excellence, healthy competition and promotion opportunities to contribute my knowledge, qualifications and experience into profitability and growth of the company. Besides, I hope I will become an active member of a dynamic team.

**EDUCATION**

**Ho Chi Minh City University of Science** From 09/2007 to 09/2011

* Studying Maths and Computer Science
* Degree grade: Good – GPA 7.2.

**TECHNICAL SKILLS**

* **Programming Languages:** 
  + - C/C++, SystemC, Verilog
* **Scripting Languages:**
* C-shell, Perl, Python
* **Version Control:**
* Git, SVN
* **Foreign Language:**
* English
* **Soft skills:**
* Time management
* Business communication
* Teamwork
* **Others:**
* Redmine usage
* Document making (MS Office)
* Basic Photoshop skill. Can do simple design: banner, poster,...

**PROFESSIONAL EXPERIENCE**

**Renesas Design Vietnam** 03/2017 – present

**Position: Junior Engineer**

**Department: Front End Design 2**

**Group: IP Design 3**

**Team: System Level Design and High Level Design.**

I had joined and experienced in some projects of SystemC Modeling:

* **E2x-FCC2/ECM (Error Control Module)**
* Make checklist
* Functinonal debug.
* **New Unit Test Environment**
* Run regression test for all models
* Make INR (Verification Report).
* **U2A-ECM (Error Control Module)/ U2A-OTS (On-chip Temperature Sensor)**
* Make checklist
* Functional debug (Pointed out 24 bugs in ECM and 1 bug in OTS)

Besides, I had experienced in evaluation of some famous EDA tools and in-house tools which work with RTL & SystemC and improve productivity.

* **Cadence Stratus (Many versions from v17.20-beta to v18.20-p100)**

**(This tool is used to generate RTL from Synthesizable SystemC code)**

* Evaluate and report based on intrinsic environment.
* **SSGen (The Synthesizable SystemC Code Generator)**

**(Renesas in-house tool that allows to generate module template code, hierarchical module template code, test bench template code, and tool scripts simply by defining hardware interface, internal information,…)**

* Make checklist and verify for some enhancement phases.
* **Mentor Graphics SLEC (Check equivalence of RTL and SystemC).**
* Evaluate and report based on intrinsic environment.
* **Synopsys VCS FGP (Fine-Grained Parallelism).**
* Evaluate and report based on check list.
* Write User Guide.
* **Synopsys ZOIX & Cadence XFS (Fault Simulator)**
* Compare the result of fault simulation between RTL & gate-level for sample data.

**Angel Beauty Care Co.,Ltd** 12/2013 - 02/2017

**Position: IT Specialist and Graphic Designer**

**Responsibilities:**

* Provide technical and troubleshoot assistance related to service desk operation, IT hotline, computer hardware and software.
* Manage server, applications and all internal softwares used by the company.
* Responsibility for security of systems including establishment and monitoring of security policies.
* Implement data backup and recovery.
* Website administration and maintenance.
* Design poster, banner and website images in accordance with company’s plans.
* Video editing.

**SVHouse Co.,Ltd** 03/2012 - 11/2013

**Position: IT Support & Technical Consultant**

**Responsibilities:**

* Manage and troubleshoot clients and server systems
* Diagnose local server, local network communication and equipment issues.
* Install, operate and test operating systems, software programs and related peripheral devices.
* Support customer based on assignment.
* Support customer about products of computer devices and sound equipments.

**SELF-PORTRAIT**

* Characteristics: Carefully, hard-working and friendly.
* Able to work in team as well as individually.
* Able to learn new knowledge, technology quickly.
* Taking full responsibility for self-actions.