

NEMA PANDORA V2

PAGE	INDEX
1	VOLTAGE_SOURCE
2	MCU
3	DIMMER
4	LORA

DESIGN CONSIDERATIONS

NetName Color: +12V

NetName Color: +5V

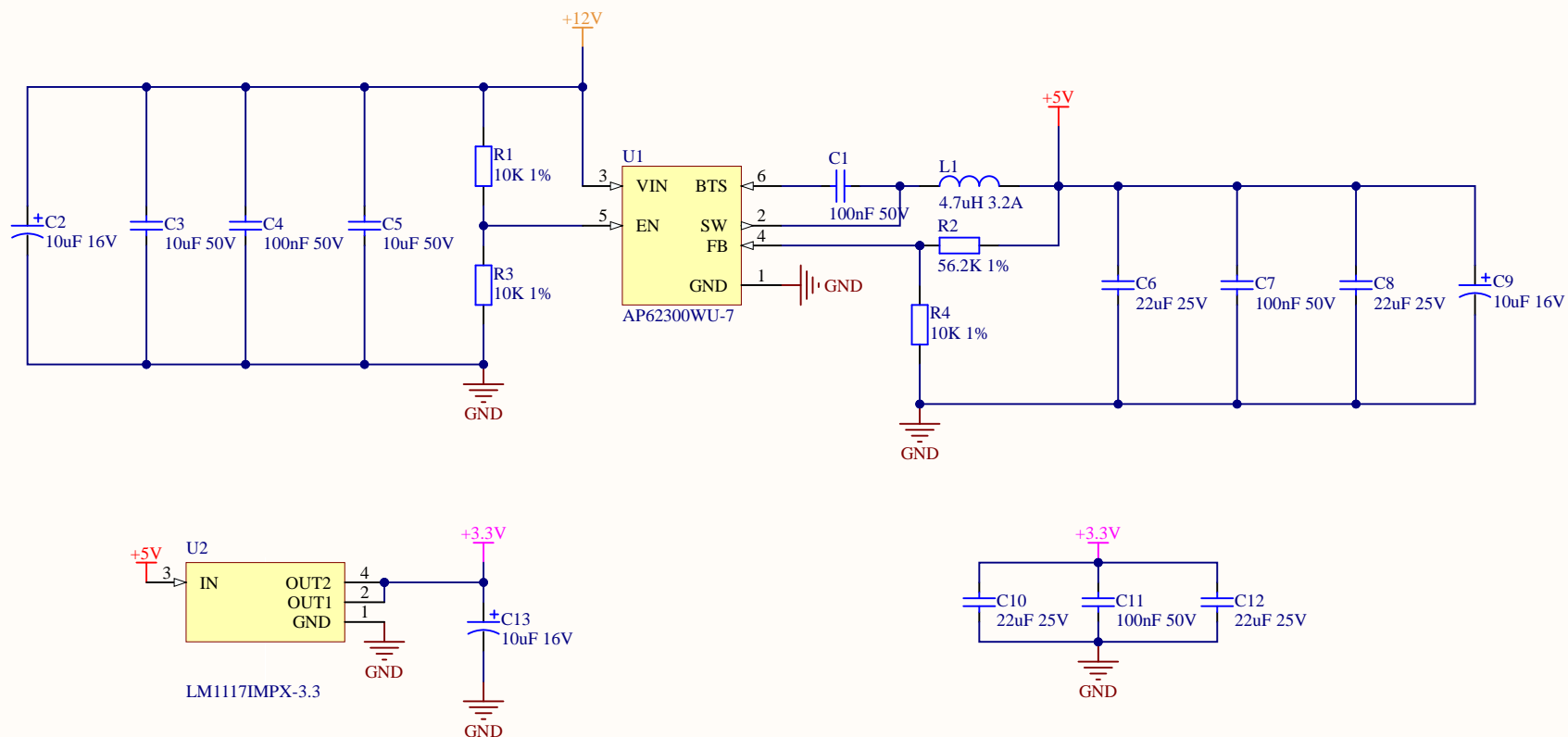
NetName Color: +3V3

DESIGN CONSIDERATIONS

DESIGN NOTE:
Cautionary for layout PCB

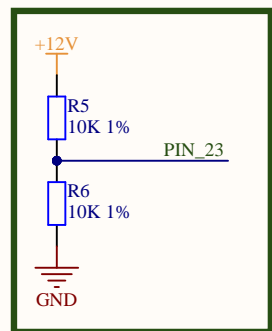
DESIGN NOTE:
Information for functions of
block

Title		
Size A4	Number	Revision
Date: 5/22/2024	Sheet of	
File: C:\Users\...\COVER PAGE.SchDoc	Drawn By:	



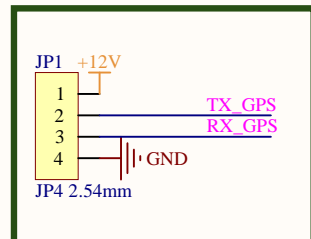
Title		
Size	Number	Revision
A		
Date:	5/22/2024	Sheet of
File:	C:\Users\...\VOLTAGE_REGULATOR.SchDoc	
Drawn By:		

A

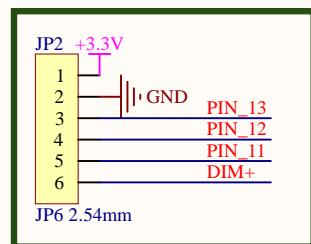


DETECT NGUỒN 12V

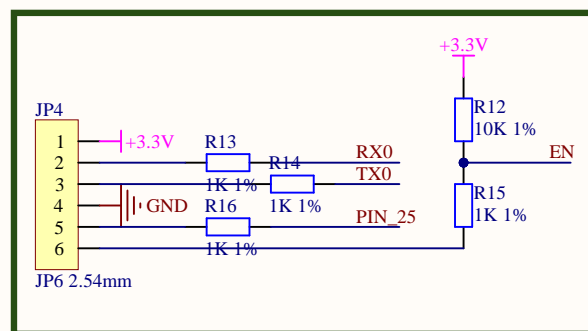
B

HEADER GIAO TIẾP
GPS BẰNG UART

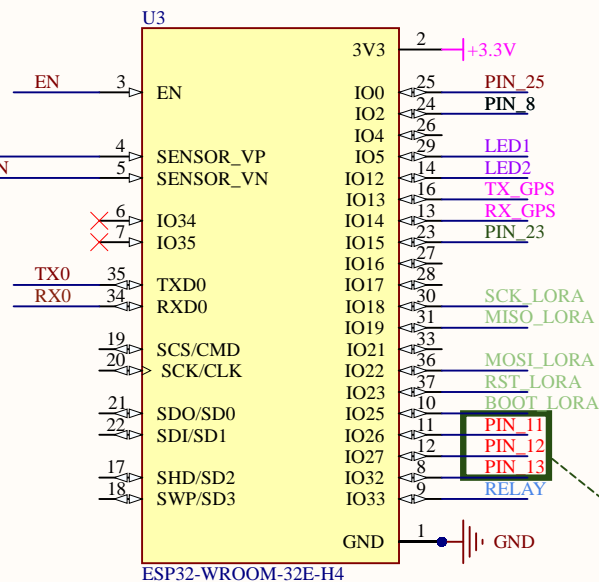
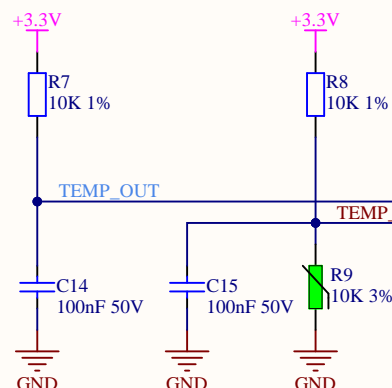
C

NHẬN TÍN HIỆU ĐO
NĂNG LƯỢNG TỪ
MẠCH CÔNG SUẤT
GỬI LÊN

D

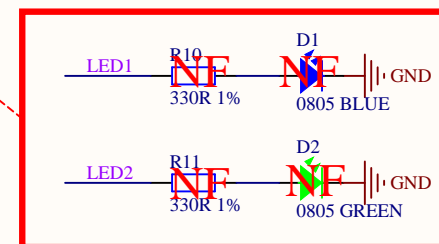


MẠCH NẠP

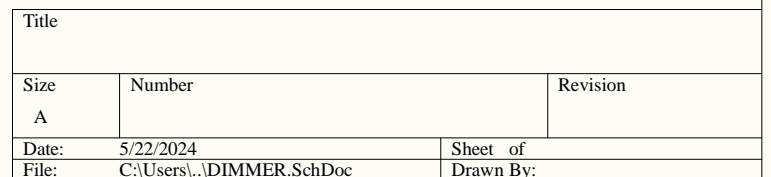


3 CHÂN ĐO NĂNG LƯỢNG

LED DEBUG



Title		
Size	Number	Revision
A		
Date:	5/22/2024	Sheet of
File:	C:\Users\...\MCU.SchDoc	Drawn By:



A

A

B

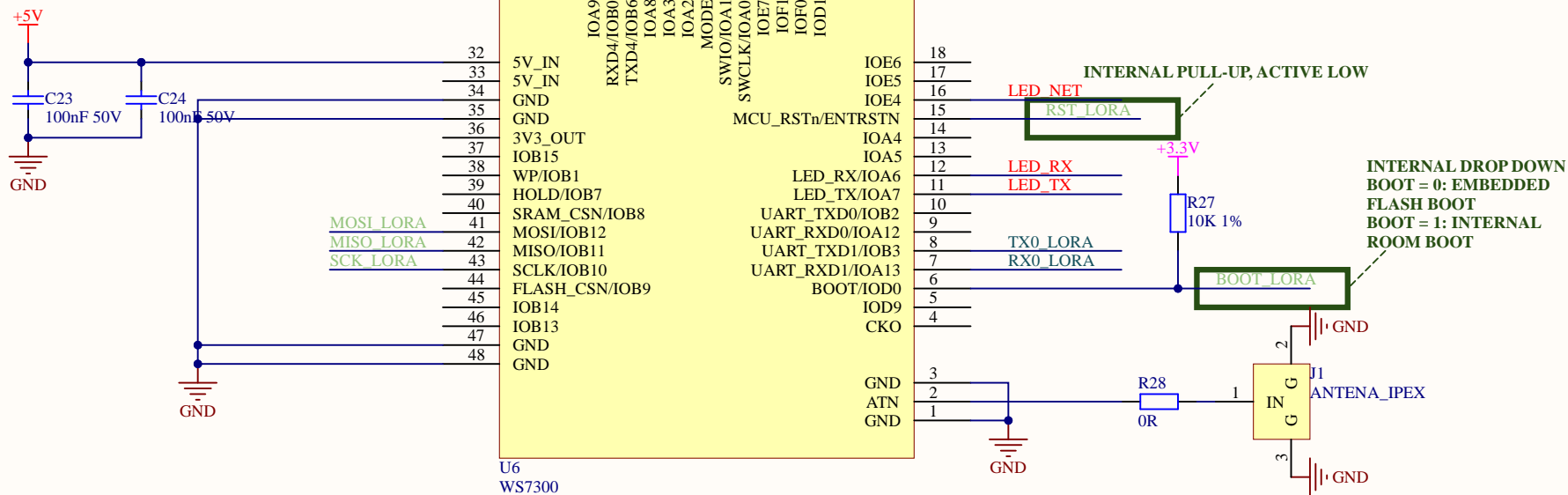
B

C

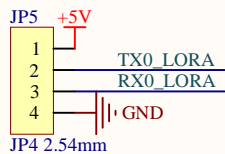
C

D

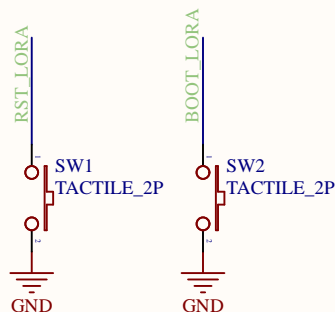
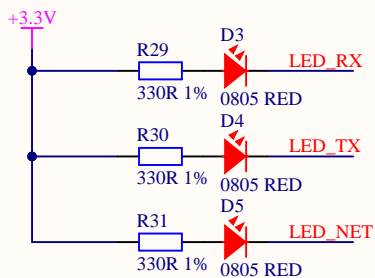
D



UART PORT



LED DEBUG



Title		
Size	Number	Revision
A		
Date:	5/22/2024	Sheet of
File:	C:\Users\...\LORA.SchDoc	Drawn By: