

4

3

2

1

A

B

C

D

E

NOTES, UNLESS OTHERWISE SPECIFIED:

1. RESISTANCE VALUES ARE IN OHMS.

2. CAPACITANCE VALUES ARE IN MICROFARADS.

3. INDUCTANCE VALUES ARE IN MICROHENRIES.

4. HIGHEST REFERENCE DESIGNATOR USED:

A. CERAMIC CAPS C127

B. TANTALUM CAPS CT20

C. INDUCTORS L7

D. RESISTORS R167

E. FILTERS E1

F. DIODES/RECTIFIERS D11/CR5

G. TRANSISTORS Q2

H. IC'S U35

I. CRYSTALS/OSCILLATORS Y2

J. CONNECTORS/HEADERS J8/JP1

K. SWITCHES SW2

L. TEST POINTS TP21

5. THE FOLLOWING UNINSTALLED PARTS ARE INDICATED WITH 'NU' (FOR "NOT USED"):

A. CERAMIC CAPS C32,C33,C34,C35,C101,C102,C105,C110

B. TANTALUM CAPS CT11

D. RESISTORS R10,R15,R27,R29,R30,R82,R86,R88,R93,R96,R98,R101,R103,R106,R109,R128,R130,R149,R151,R163,R165,R167

E. FILTERS E1

H. IC'S U2,U3

J. CONNECTORS/HEADERS J8/JP1

6. MISSING REFERENCE DESIGNATORS:

A. CERAMIC CAPS C87,C124,C125

D. RESISTORS R16,R17,R75,R139,R140,R141,R148

H. IC'S U23

L. TEST POINTS TP8,TP10,TP11,TP12

REV

DESCRIPTION

DATE

APPROVED

FORMAL RELEASE

\_ \_ -2000

M. DAWKINS

SHEET

01: COVER SHEET

02: PARALLEL PORT I/F

03: JTAG

04: HPI-C6711

05: CLOCK-PLL & PRG LOGIC

06: CLOCK-OSCILLATORS

07: EMIF-C6711

08: EMIF-SDRAM(16M-BIT) (FOR ALTERNATE USE)

09: EMIF-SDRAM(64M-BIT+)

10: EMIF-ROM & I/O PORT

11: DB-DATA XCVRS

12: DB-ADDR & CNTRL XCVRS

13: DB-PERIPH & MEM I/F

14: ANALOG I/F CIRCUIT

15: MCBSP-C6711

16: PWR-INPUT, SEQ, & SVS

17: PWR- 1.8V REGULATOR

18: PWR- 3.3V REGULATOR

19: PWR-DECOUPLING C6711

20: PWR-DECOUPLING (SHEETS 2-5)

21: PWR-DECOUPLING (SHEETS 6-9)

22: PWR-DECOUPLING (SHEETS 10-16)

REVISION STATUS OF SHEETS

REV

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SH

22

REV

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SH

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REV

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REV

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SH

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APPLICATION

DWN

DATE

G. CONNELLY

1-1-2000

CHK

DATE

M. DAWKINS

ENGR

DATE

G. CONNELLY

ENGR-MGR

DATE

T. MISCIO

QA

DATE

J. WHISONANT

MFG

DATE

M. JACKSON

RLSE

DATE

M. DAWKINS

TEXAS INSTRUMENTS INCORPORATED

Software Development Systems, Semiconductor Group, Houston, Texas

Title

TMS320C6711 DSP STARTER KIT

Size

Document Number

D600982

Rev

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Date:

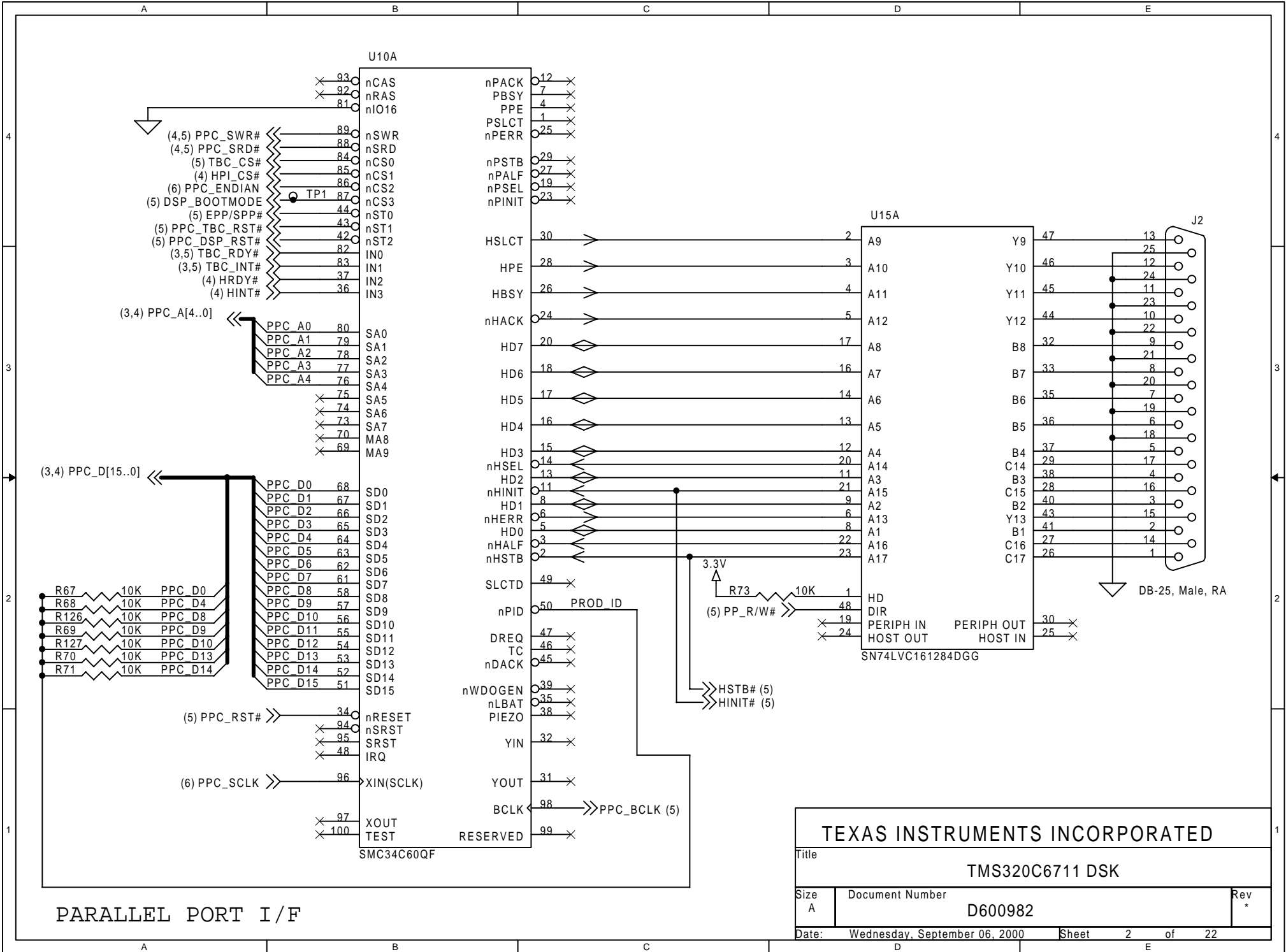
Wednesday, September 06, 2000

Sheet

1

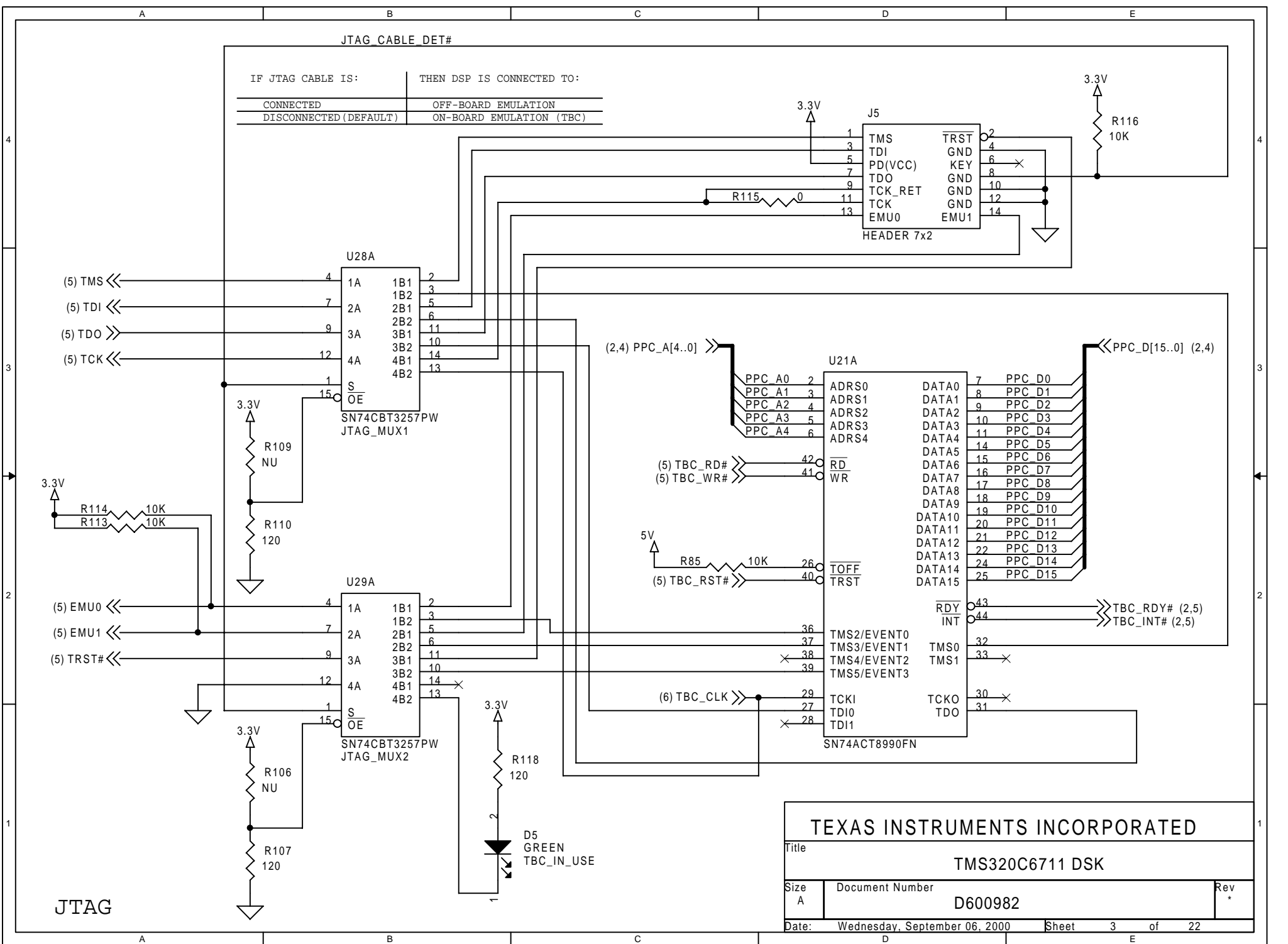
of

22



|                                     |                         |       |       |
|-------------------------------------|-------------------------|-------|-------|
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| Title TMS320C6711 DSK               |                         |       |       |
| Size A                              | Document Number D600982 |       | Rev * |
| Date: Wednesday, September 06, 2000 | Sheet 2                 | of 22 |       |

| JTAG_CABLE_DET#        |                           |
|------------------------|---------------------------|
| IF JTAG CABLE IS:      | THEN DSP IS CONNECTED TO: |
| CONNECTED              | OFF-BOARD EMULATION       |
| DISCONNECTED (DEFAULT) | ON-BOARD EMULATION (TBC)  |



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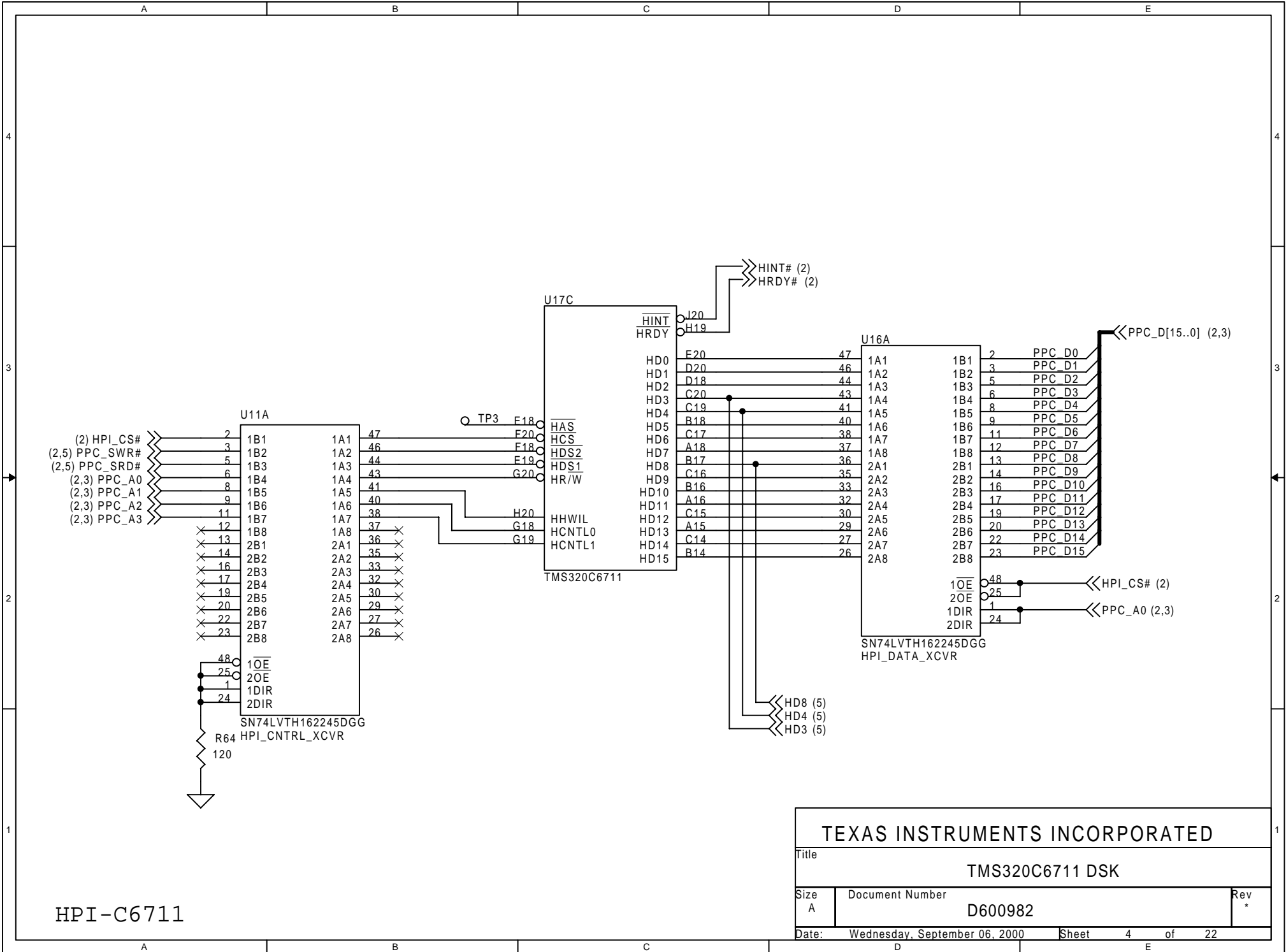
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22

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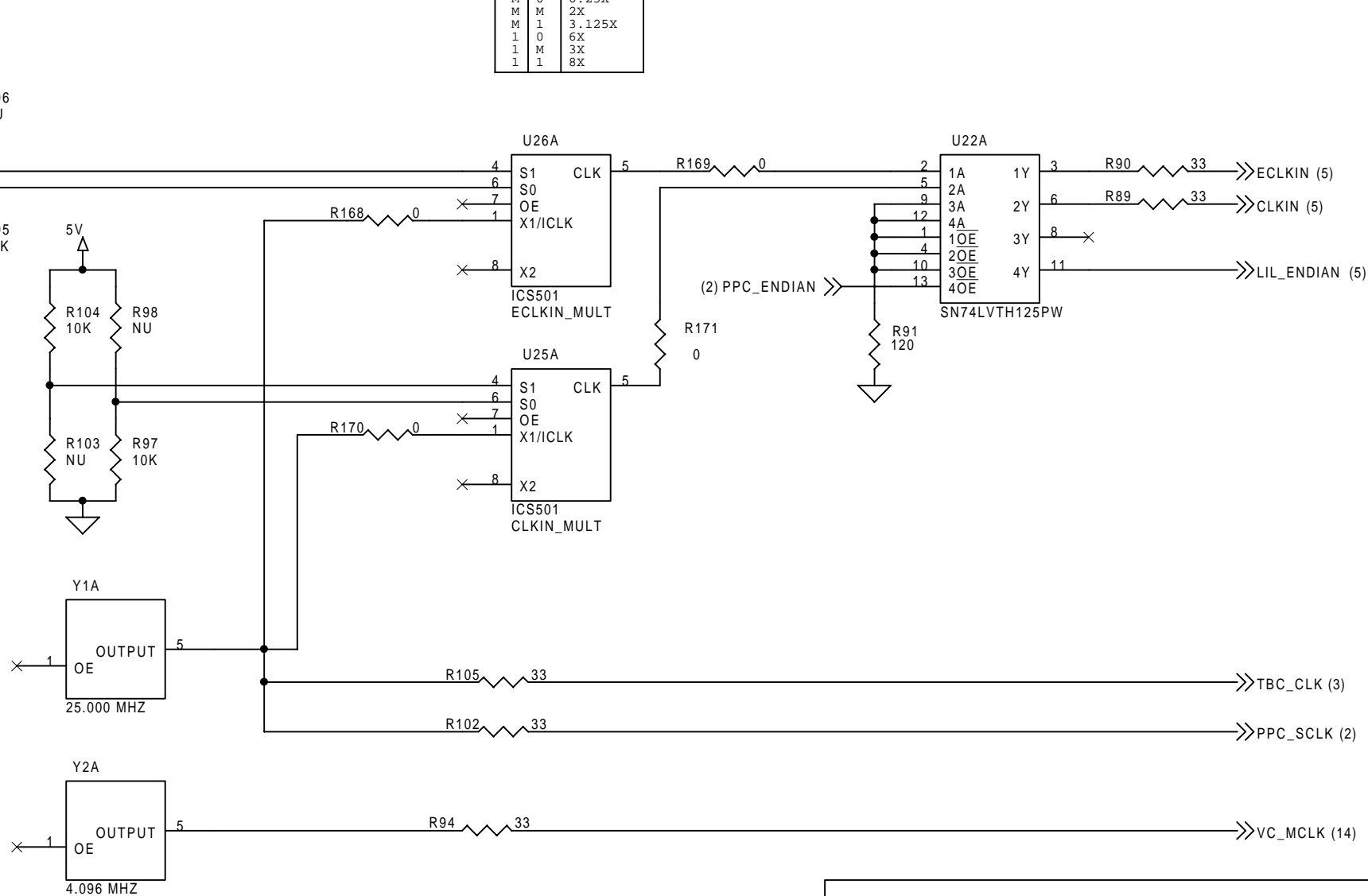
HPI-C6711

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ICS501 SELECTION TABLE

| S1 | S0 | CLK     |
|----|----|---------|
| 0  | 0  | 4X      |
| 0  | M  | 5.3125X |
| 0  | 1  | 5X      |
| M  | 0  | 6.25X   |
| M  | M  | 2X      |
| M  | 1  | 3.125X  |
| 1  | 0  | 6X      |
| 1  | M  | 3X      |
| 1  | 1  | 8X      |



CLOCK-OSCILLATORS

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Title

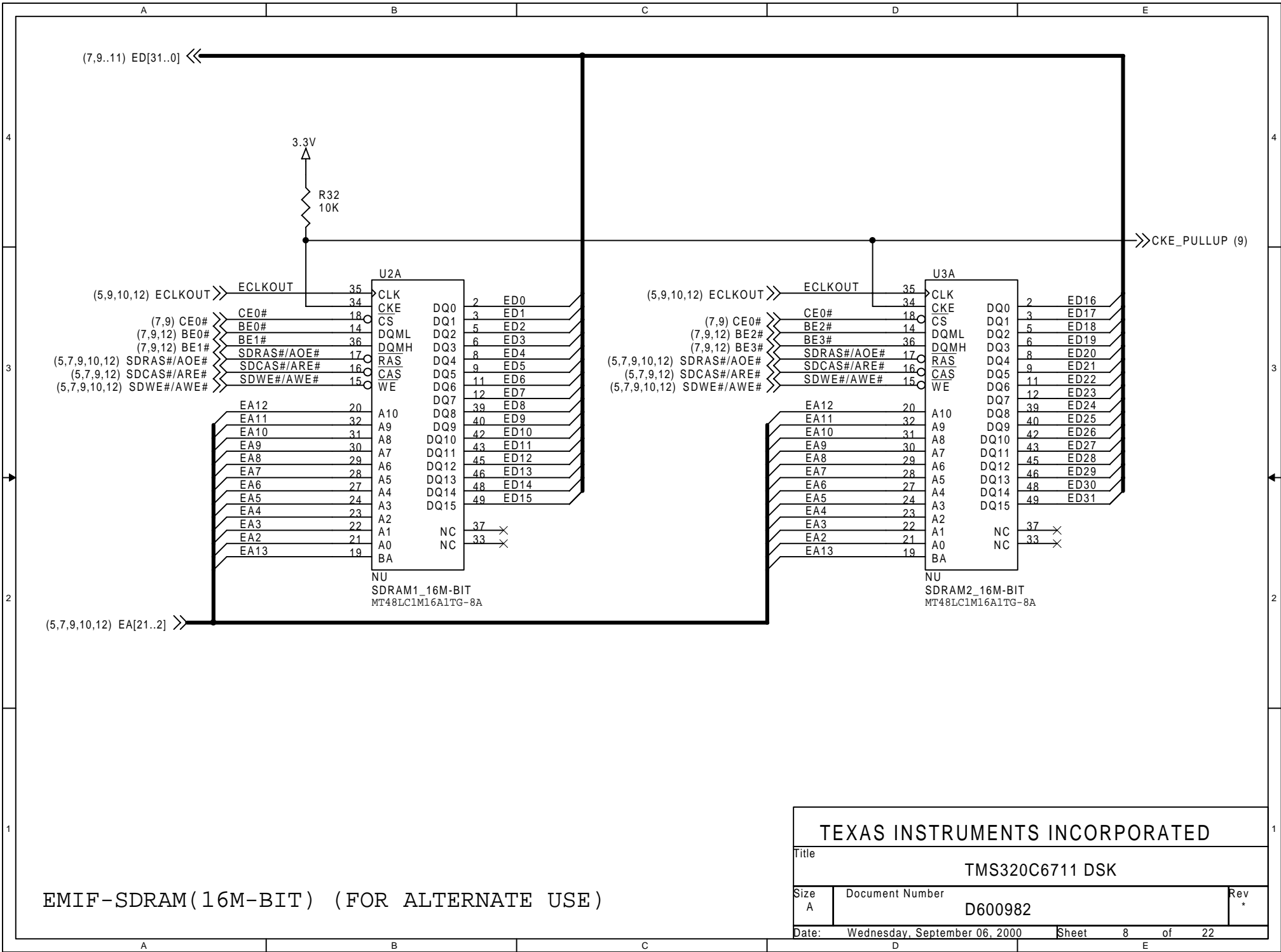
TMS320C6711 DSK

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D600982

Date: Wednesday, September 06, 2000 Sheet 6 of 22

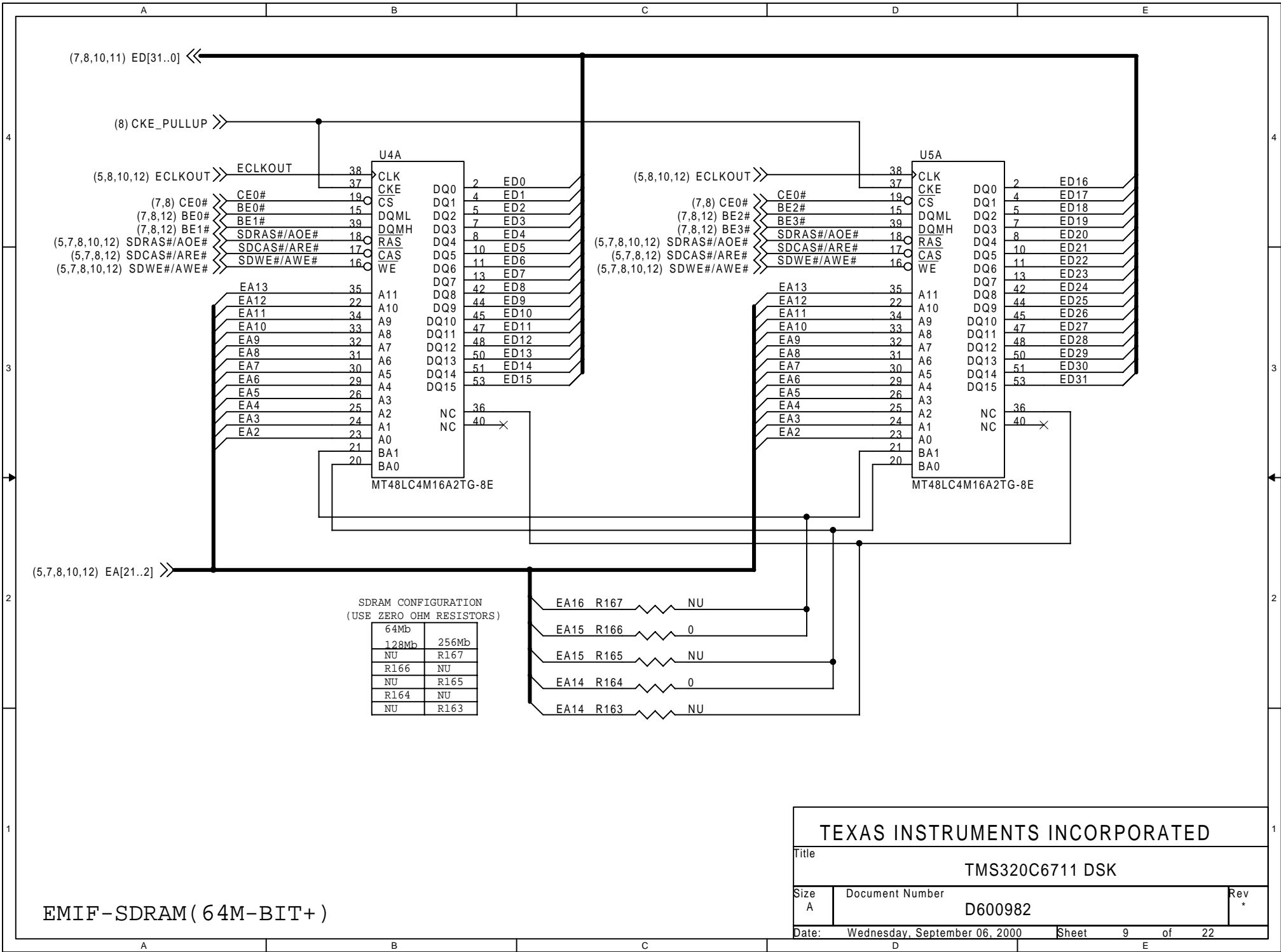




EMIF-SDRAM(16M-BIT) (FOR ALTERNATE USE)

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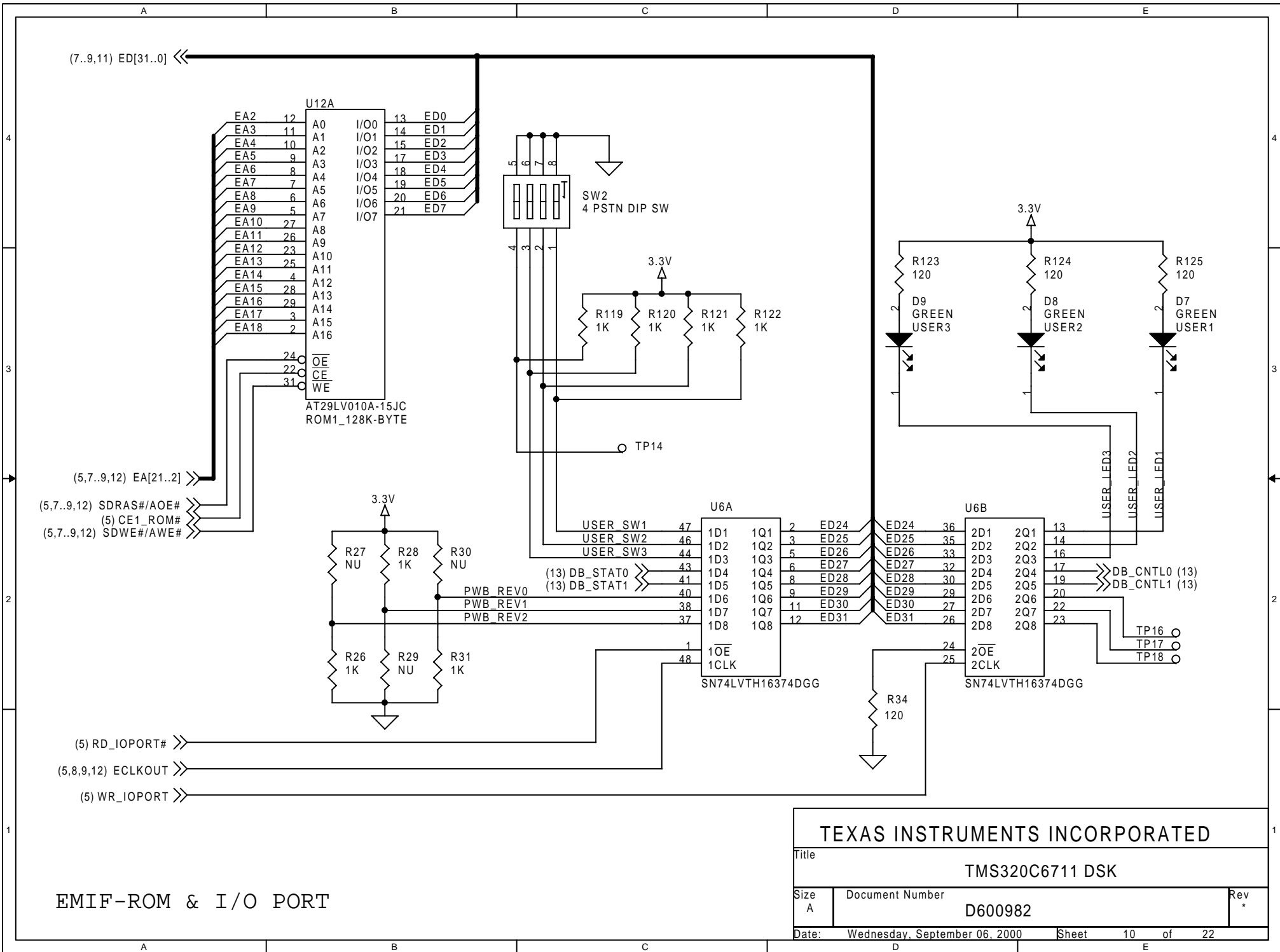
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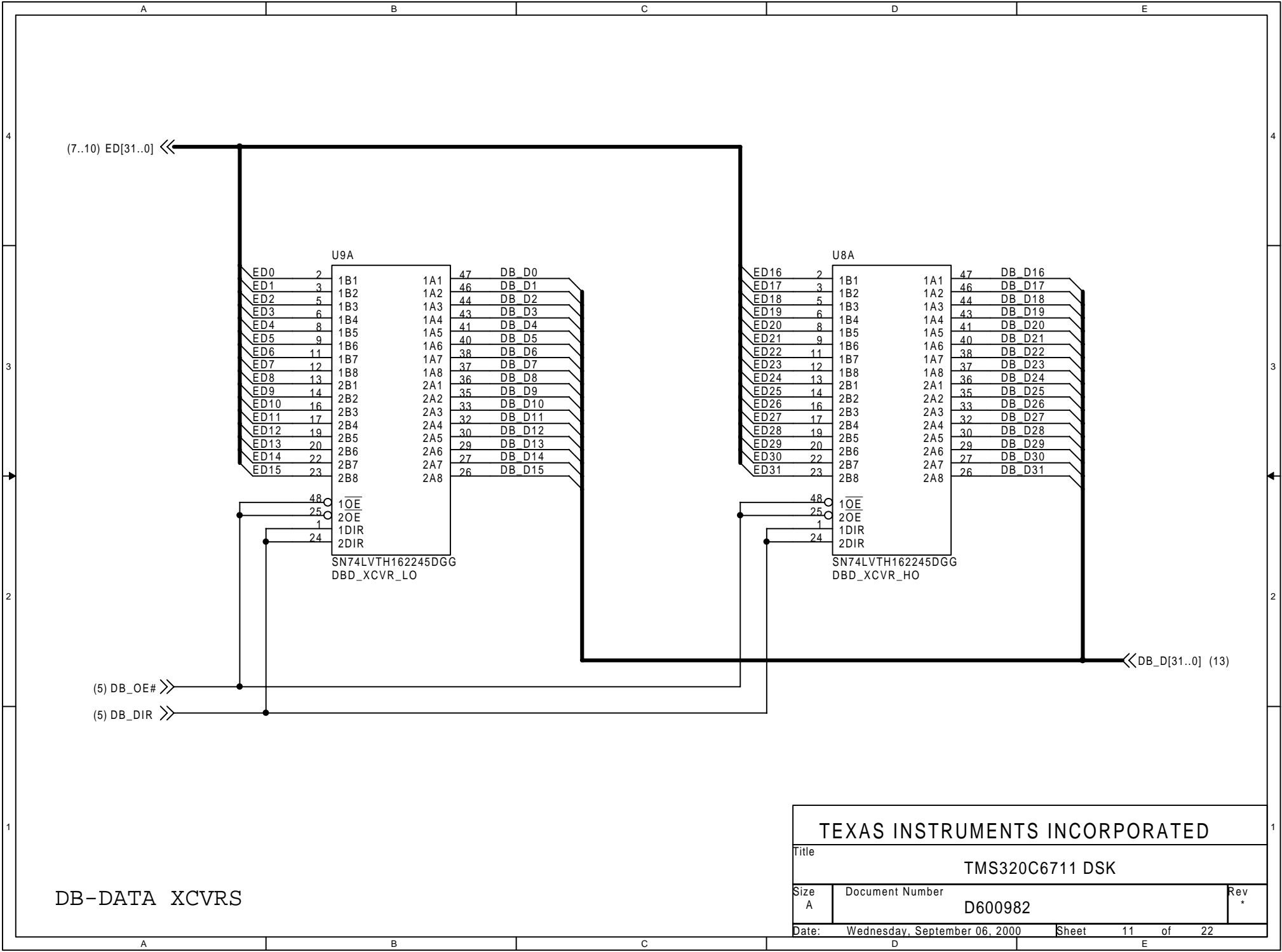
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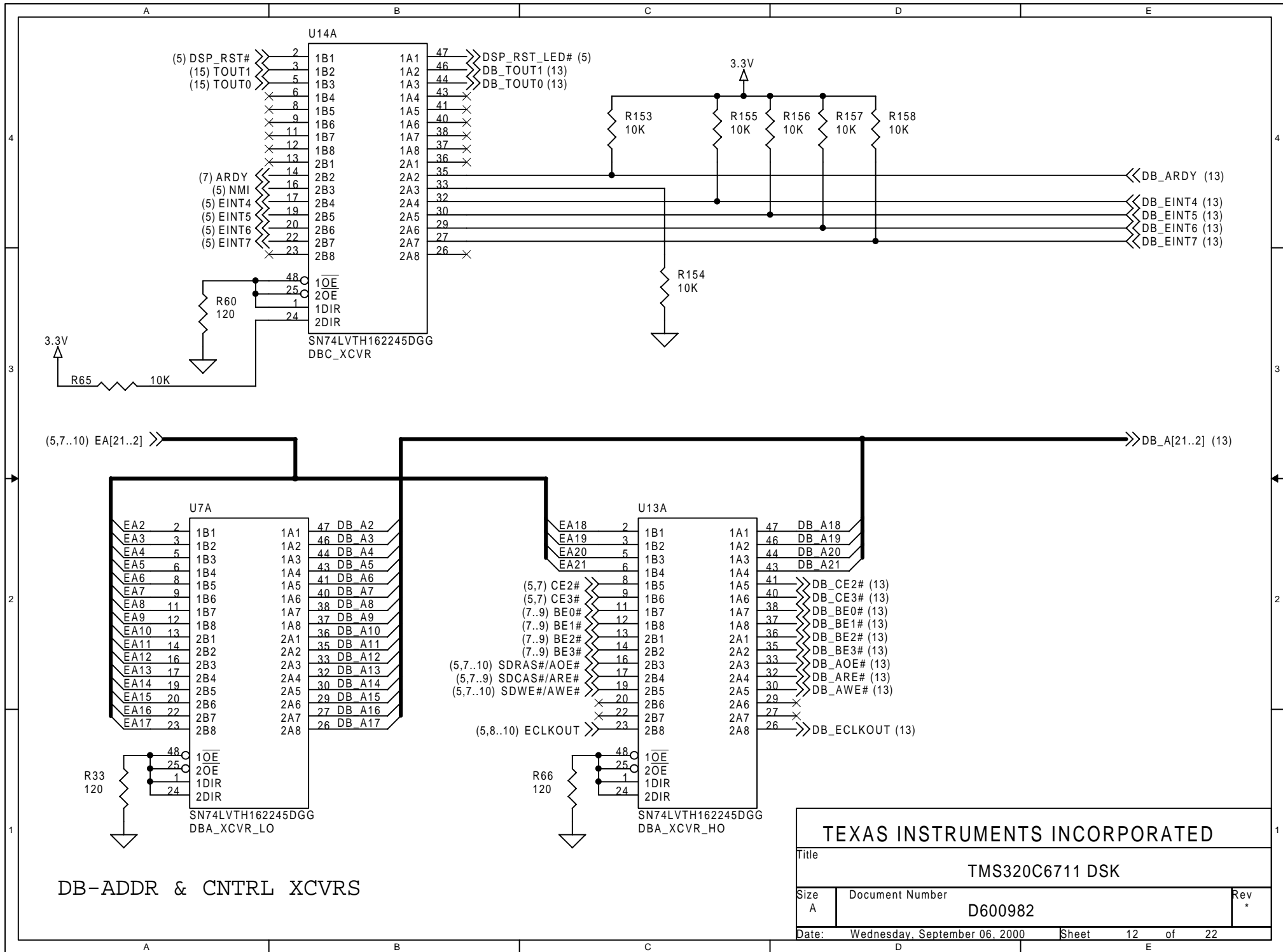
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DB-DATA XCVRS

|                                |                               |       |          |
|--------------------------------|-------------------------------|-------|----------|
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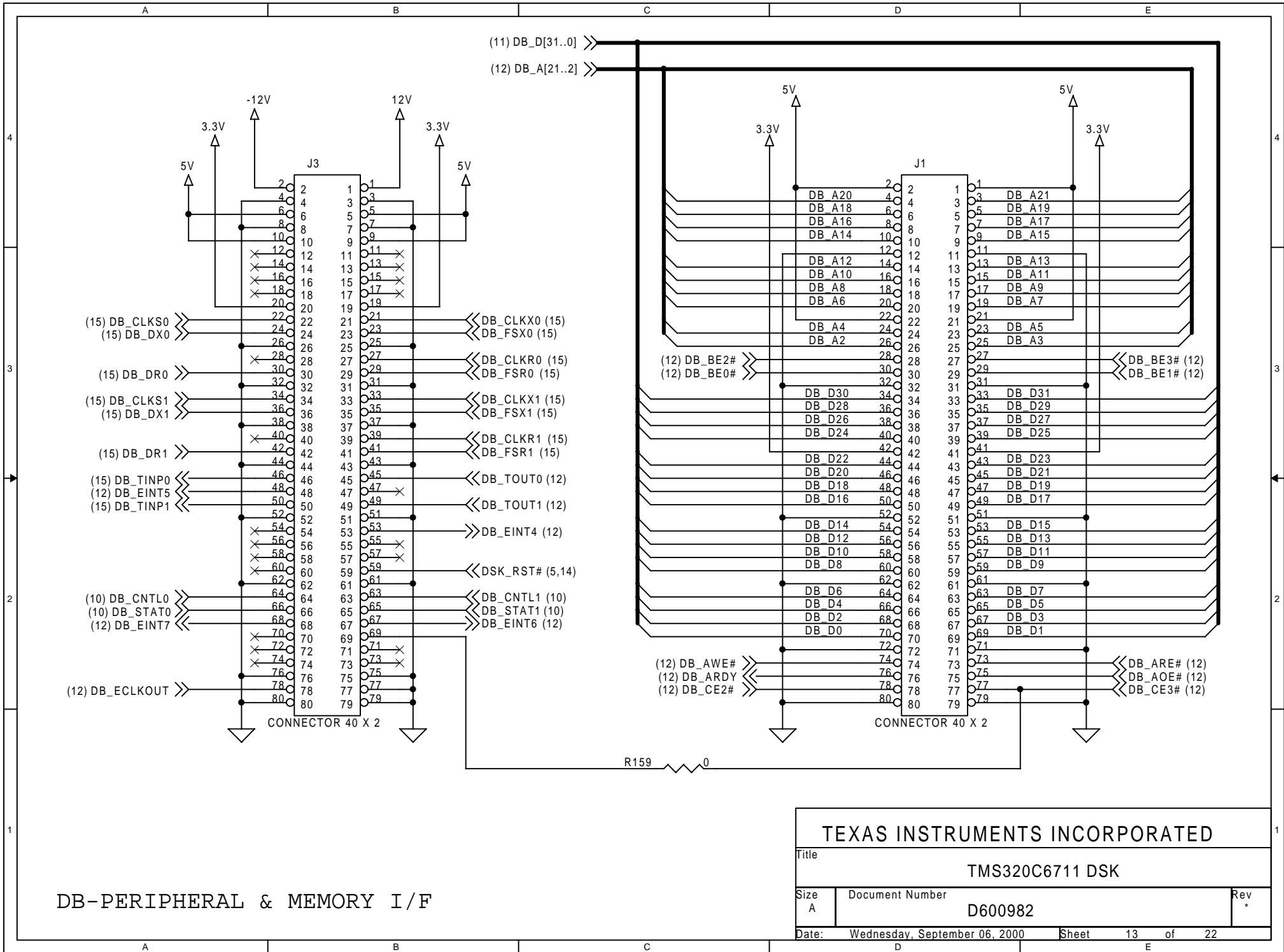
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DB-PERIPHERAL & MEMORY I/F

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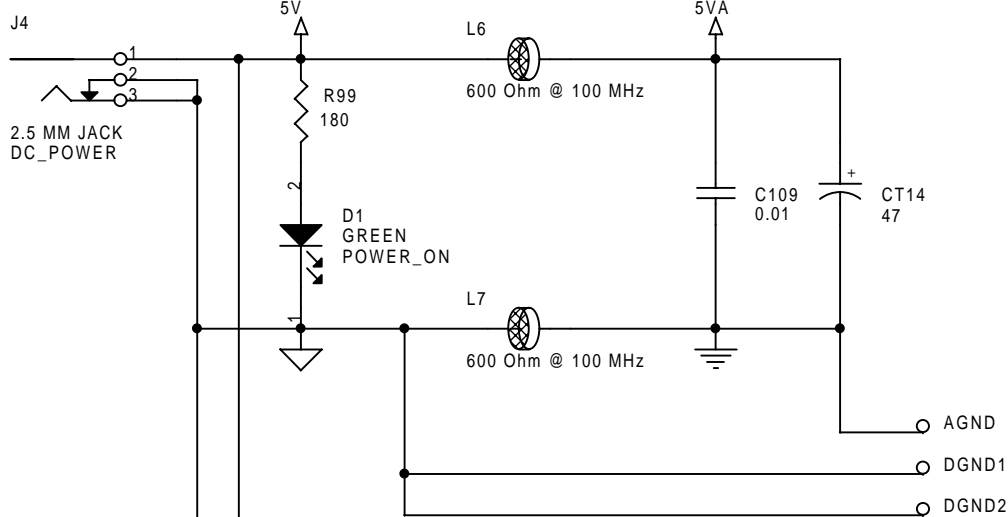
Date: Wednesday, September 06, 2000

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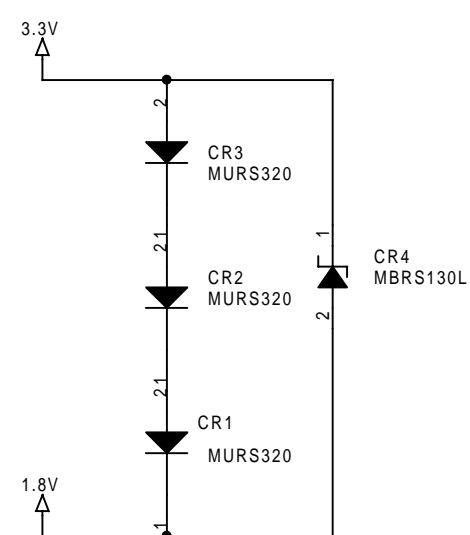




## POWER INPUT

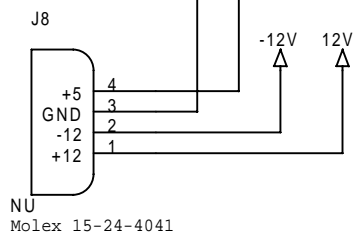


## POWER SEQUENCING

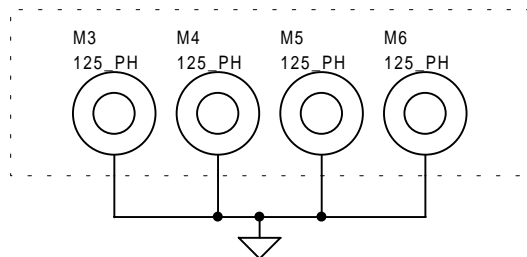


**WARNING: DO NOT SUPPLY POWER TO J4 AND J8 AT THE SAME TIME!**

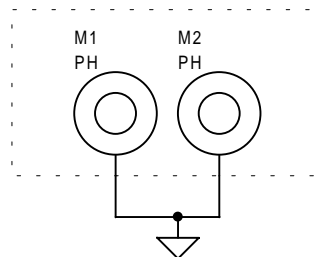
J8 IS TO BE POPULATED BY THE USER IF NEEDED.



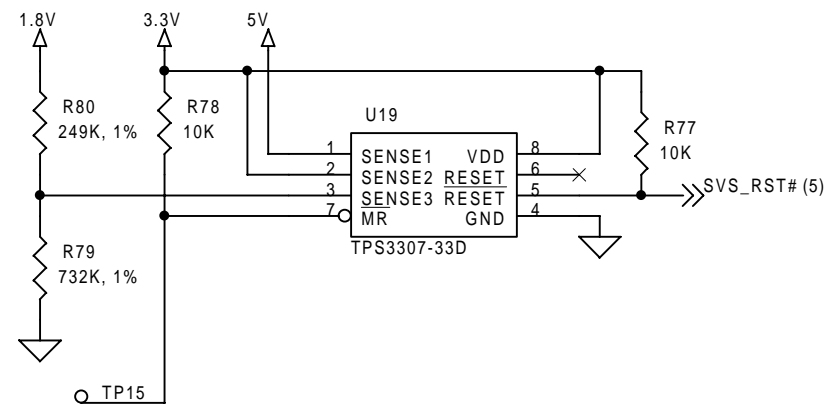
## DAUGHTERBOARD STANDOFF GROUNDING



## DB-25 CONNECTOR GROUNDING



## POWER SUPERVISION



POWER-INPUT, SEQUENCING, & SUPERVISION

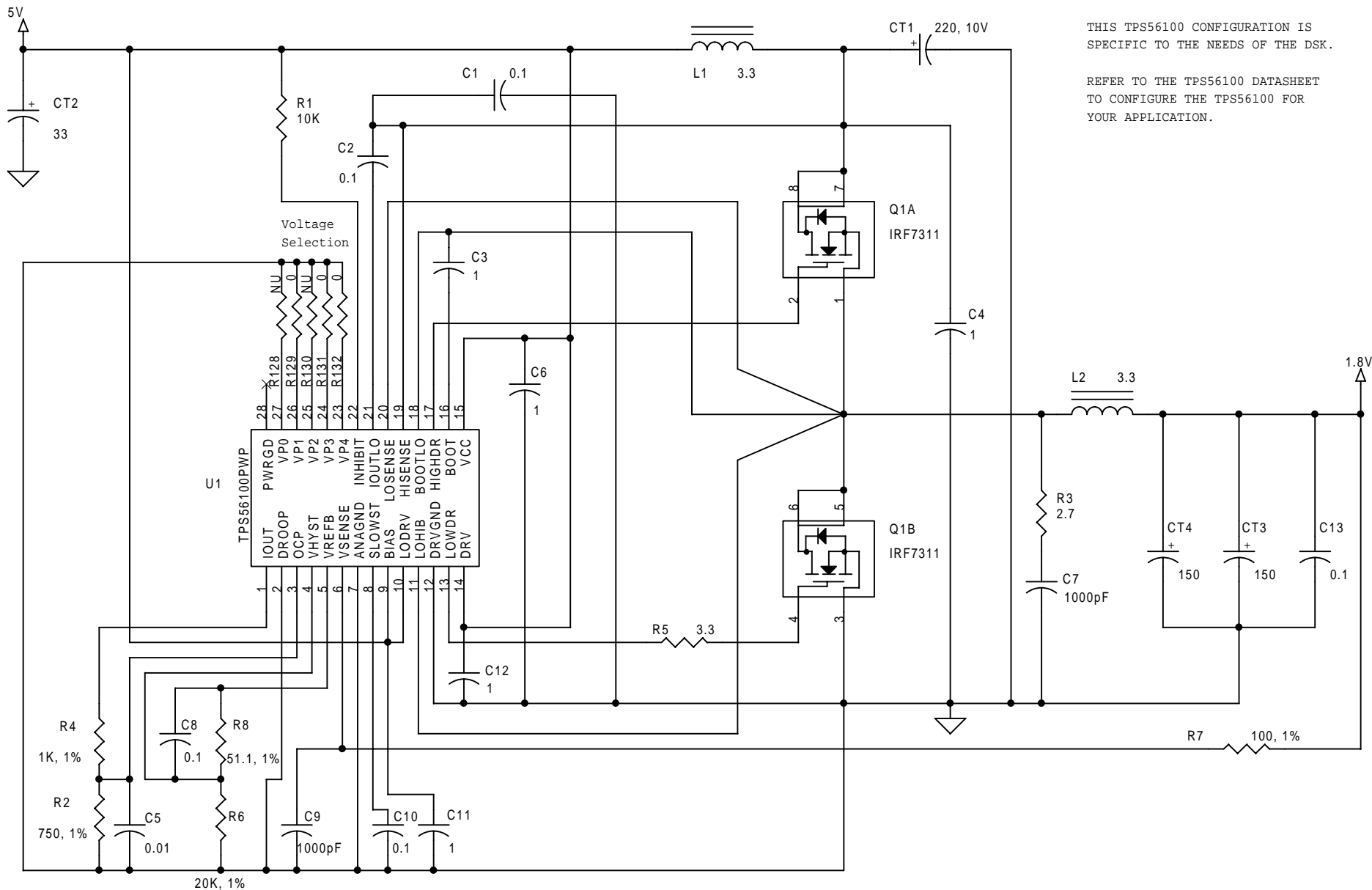
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THIS TPS56100 CONFIGURATION IS  
SPECIFIC TO THE NEEDS OF THE DSK.

REFER TO THE TPS56100 DATASHEET  
TO CONFIGURE THE TPS56100 FOR  
YOUR APPLICATION.

POWER- 1.8V REGULATOR

TEXAS INSTRUMENTS INCORPORATED

Title

TMS320C6711 DSK

Size  
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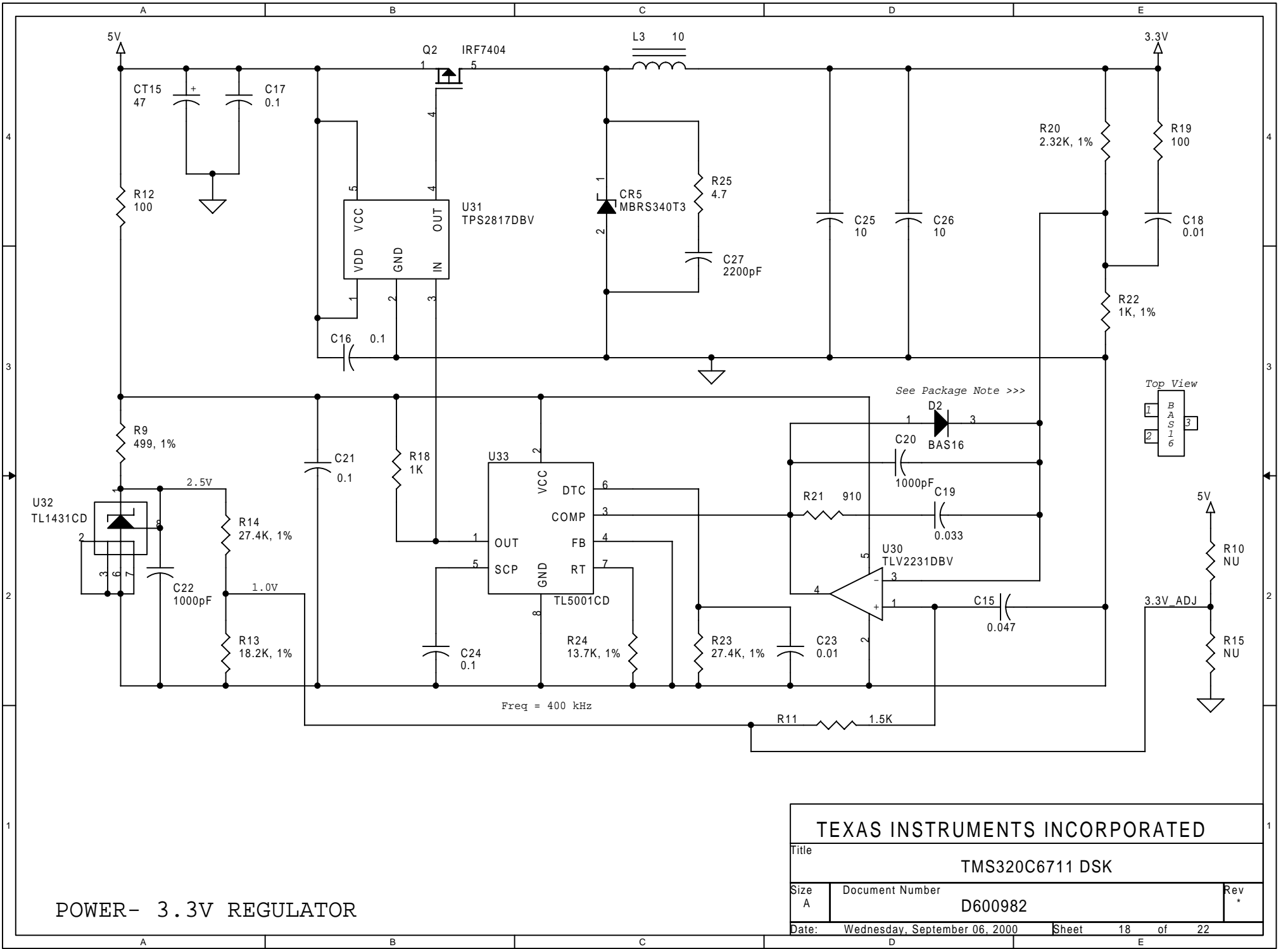
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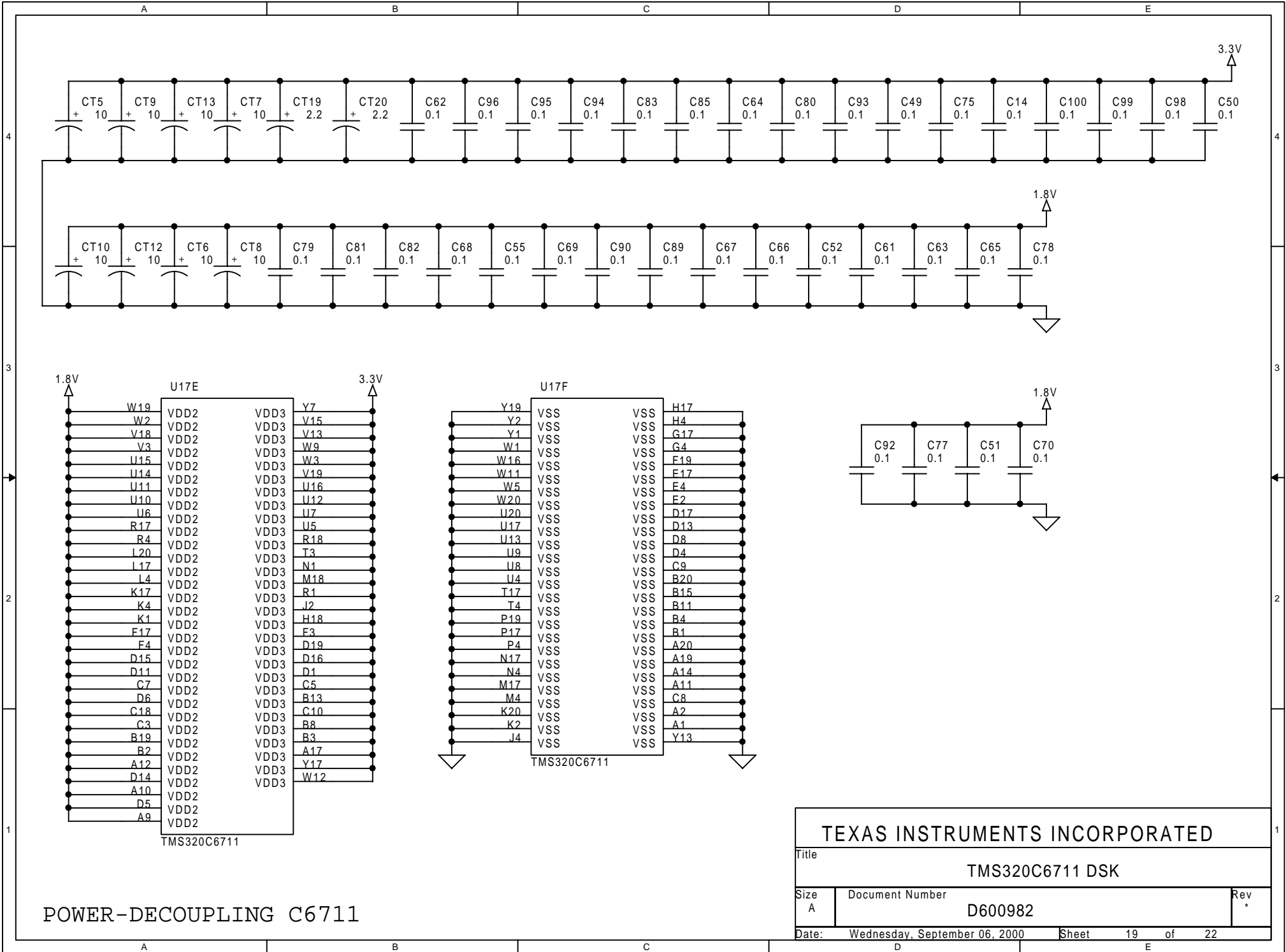
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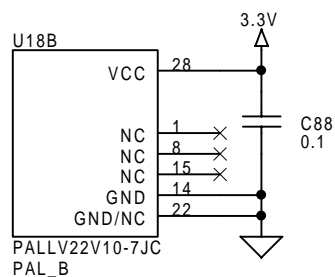
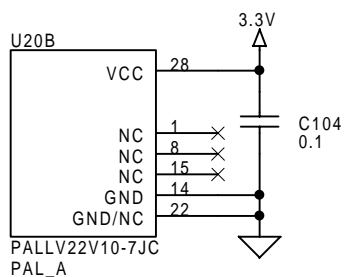
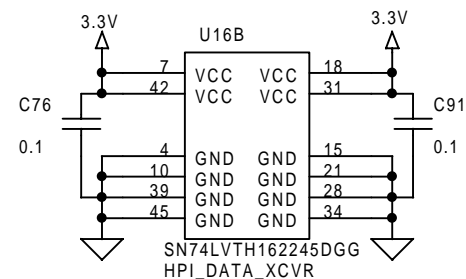
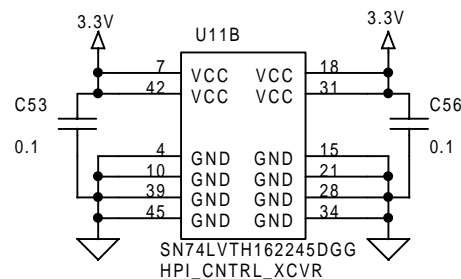
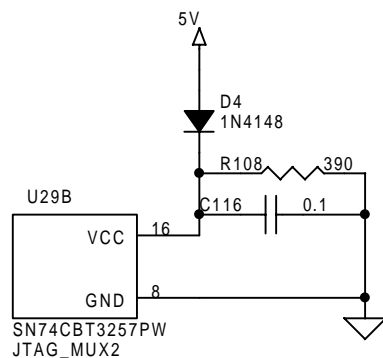
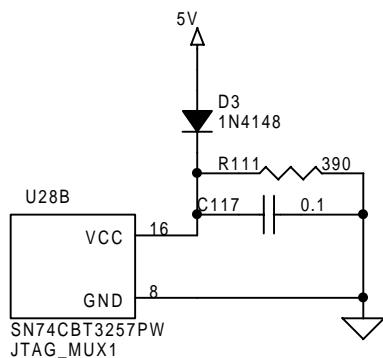
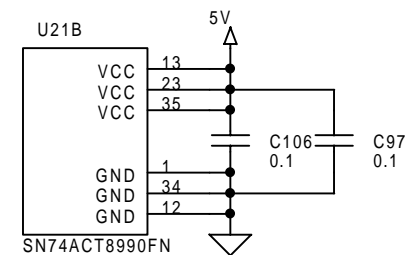
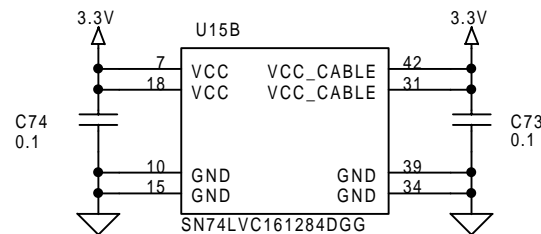
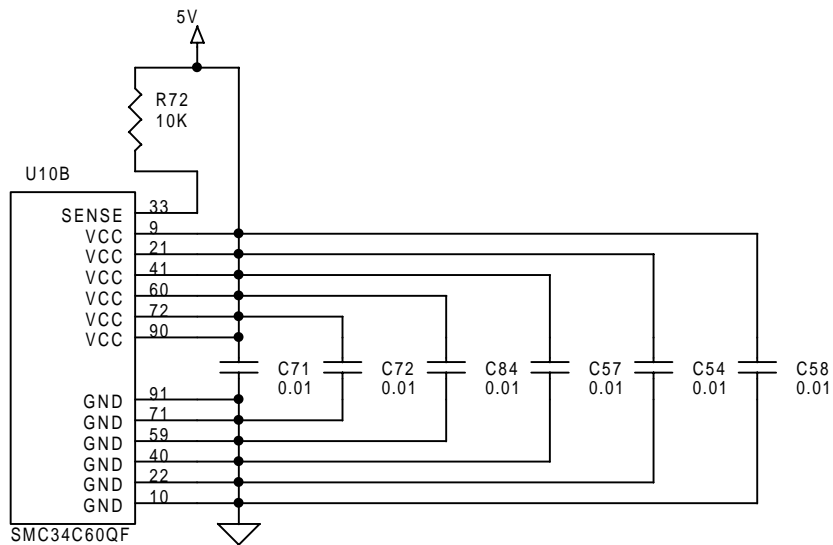
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POWER-DECOUPLING C6711

|                                |                               |  |                |
|--------------------------------|-------------------------------|--|----------------|
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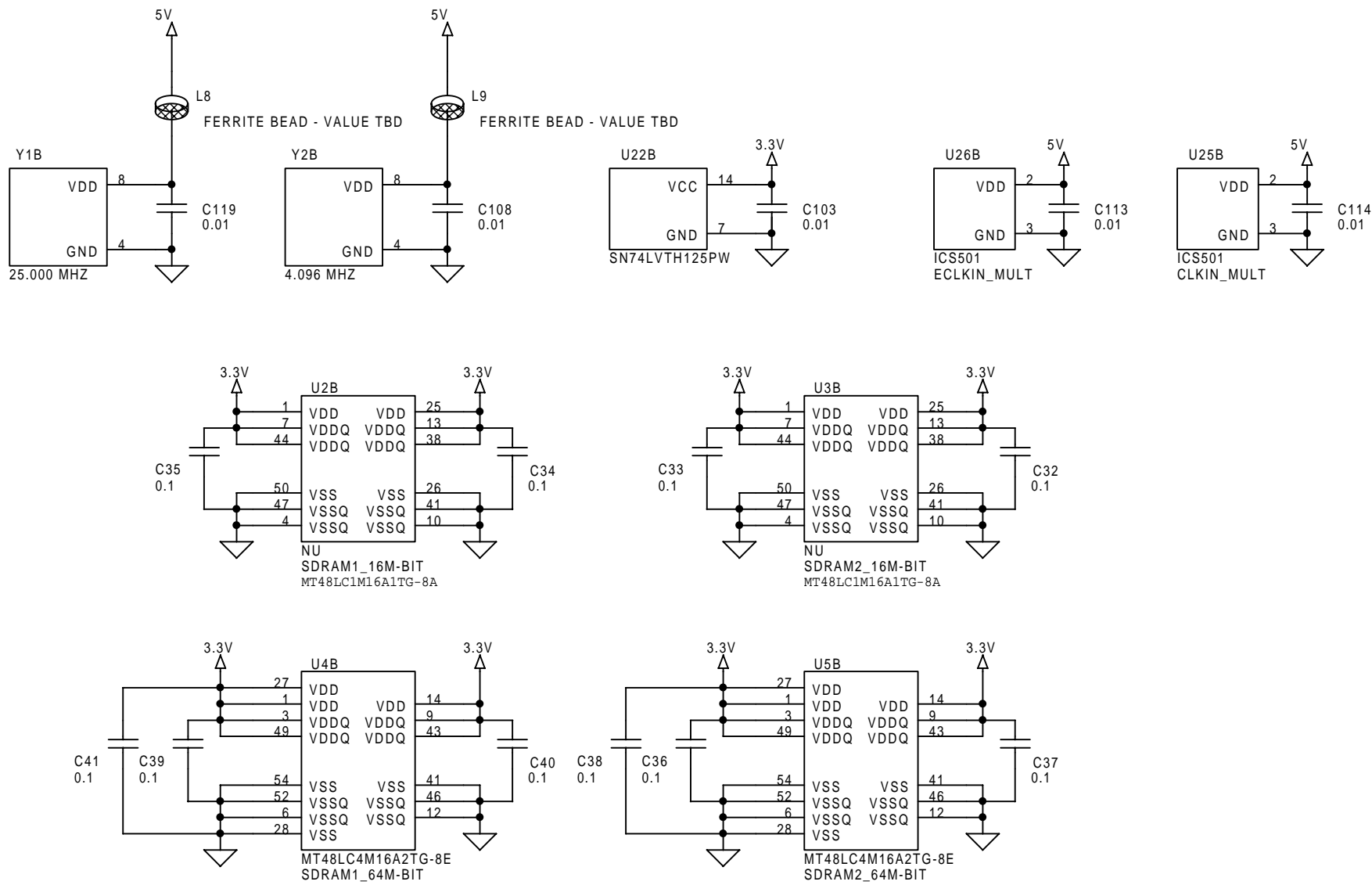
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TMS320C6711 DSK

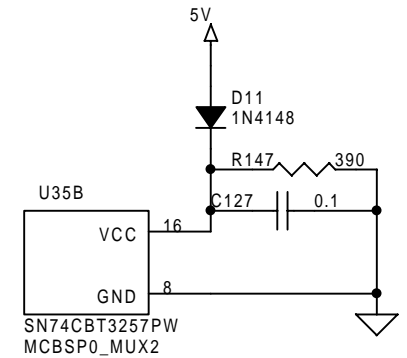
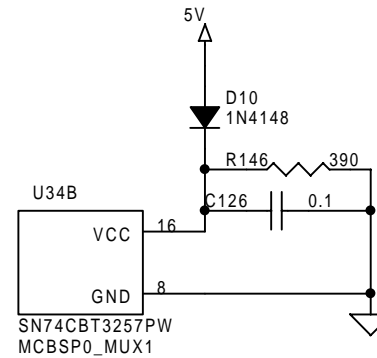
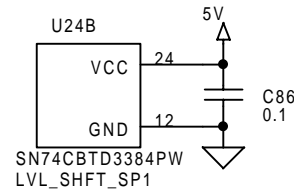
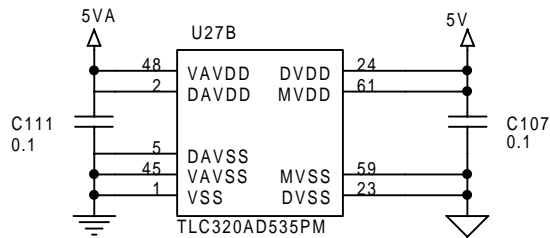
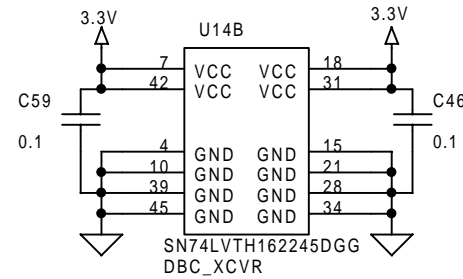
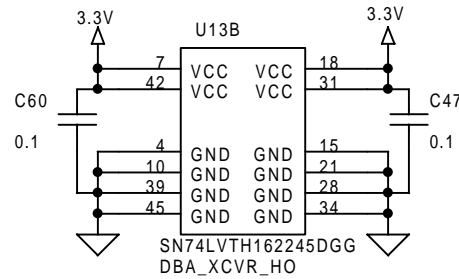
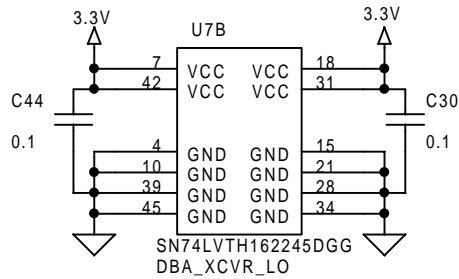
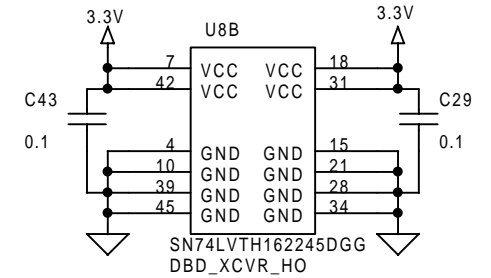
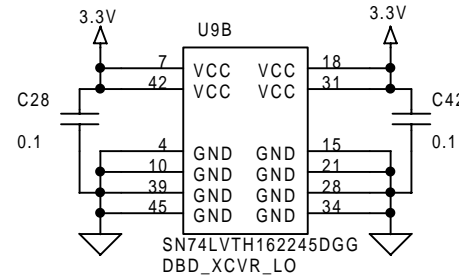
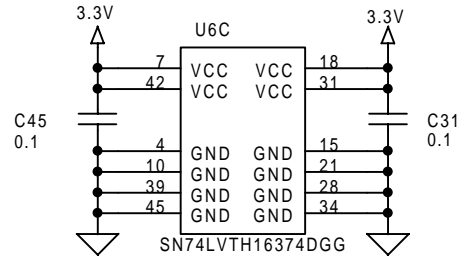
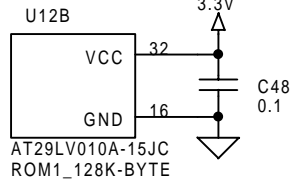
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POWER-DECOUPLING (SHEETS 2-5)





POWER-DECOUPLING (SHEETS 10-16)

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