

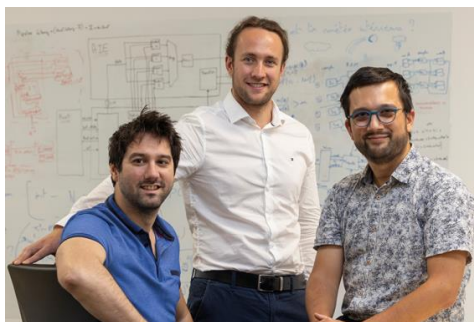
Investors deck
05.2024

Raphael Frisch, CEO

Trusted AI for embedded systems

Who are we?

- Based in **Grenoble**, France
- Incorporation : Feb **2019**
- Spin-off from CNRS and Inria
 - Two major research centers in France
- Team of 12 people lead by a trio
- **Expertises** :
 - probabilistic AI
 - hardware design

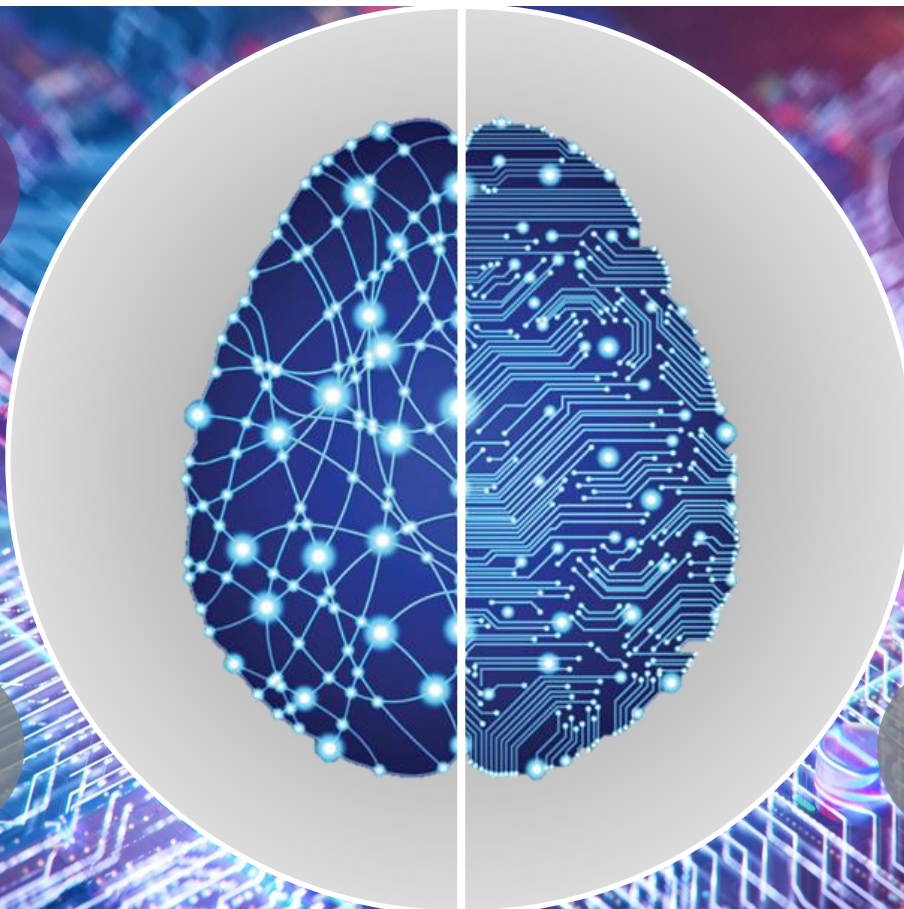


The best of both worlds for efficient embedded systems

Probabilistic AI

Frugal

Explainable



Hardware (IP)

Efficient

Embedded

Autonomous industrial AI needs new HW/SW paradigm

Ability to handle
uncertainty

high



Deep
learning
dedicated
hardware

Deep learning

neural networks, reinforcement
learning, neuro-symbolic AI, ...

Lot of training data
High training costs
Low explainability

Edge AI in critical systems

Mission Planning

Risk analysis

Pred. maintenance



dedicated
hardware

Probabilistic AI

Bayesian, active inference, ...

Machine learning

kernel-based, regression, clustering, ...

Low training data
Low training costs
High explainability

The next
AI frontier

Expert
systems

No training data
High explainability

The leadership team

CEO & co-founder



Raphael FRISCH

10 years exp.

- BizDev / AI embedded systems
- Int. M.Sc. in CS at Karlsruhe Institute of Tech. and Ensimag
- PhD Applied maths - Univ. Grenoble Alpes



COO & co-founder



Marvin FAIX

10 years exp.

- Project Management (> 5M€)
- Computing systems for AI
- M. Sc. Phelma - Grenoble INP
- PhD Applied maths - CNRS



CTO & co-founder



Jean SIMATIC

10 years exp.

- EDA and H/W design
- Project management R&D
- Scrum expert
- M. Sc. Ecole Polytechnique
- PhD Semiconductors TIMA/CNRS



VP Sales



Christian VERBRUGGE

25 years in Industry (robotics)

5 years in startups (semicon)

- Experienced sales profile
- Engineering background
- Various management positions incl. in GML sold to Snap Inc.



TEAM 15 people

100+ years team experience

in delivering worldwide leading technologies for probabilistic AI

10 PhDs experts 2 patents

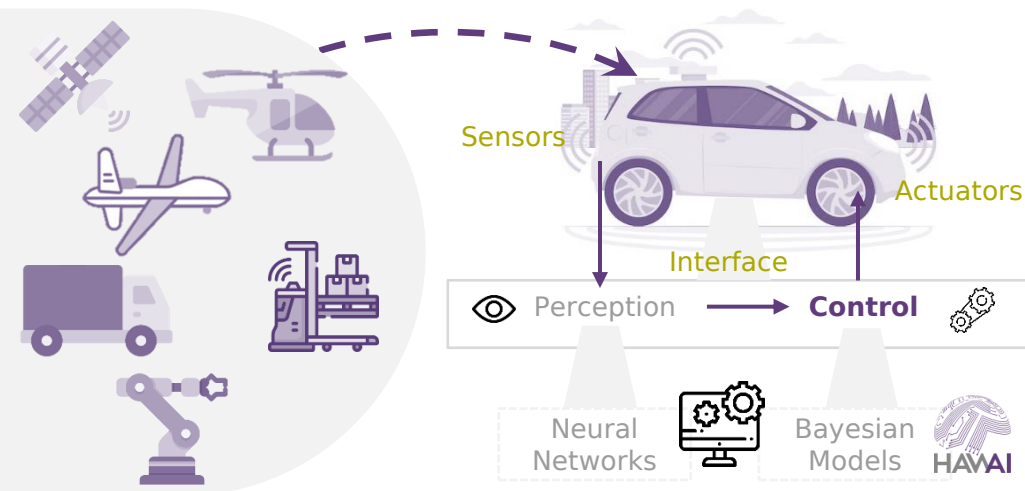
- Bayesian modelling
- Compiling and software simulation
- Hardware design

Over 20 publications

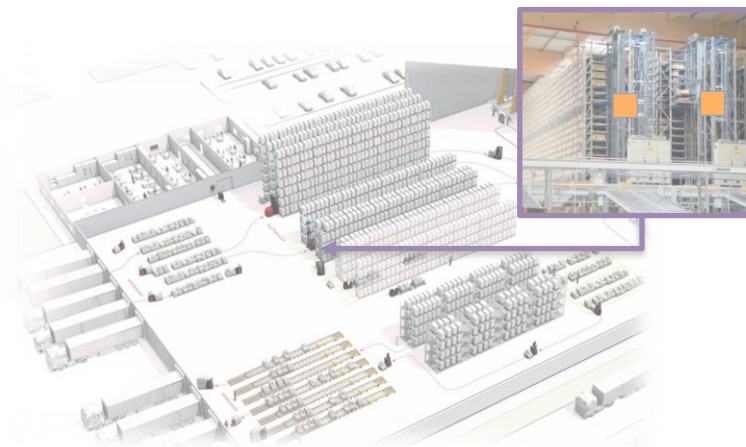
incl. in *Nature electronics*

nature

Decision making & mission planning



Maintenance & monitoring



- Onboard mission planning
- **Projects with Safran and others defense entities**
- Real time embedded computing
- No learning approach



- Predictive maintenance in logistic centers
- **France2030 project**
- Continuous local monitoring
- Integrate expert knowledge



Flying

TAM : 2.5 B€

SAM : 743 M€

SOM : 2.2 M€



Road

TAM : 4.5 B€

SAM : 1.5 B€

SOM : 8.1 M€



Robots

TAM : 458 M€

SAM : 74 M€

SOM : 4.6 M€



Our latest demo: mission planning in uncertain environment



Problem

- Robot transports pallets in a warehouse, a dynamic and evolving environment
- Needs HW acceleration to **increase performance** and **autonomy**



solution

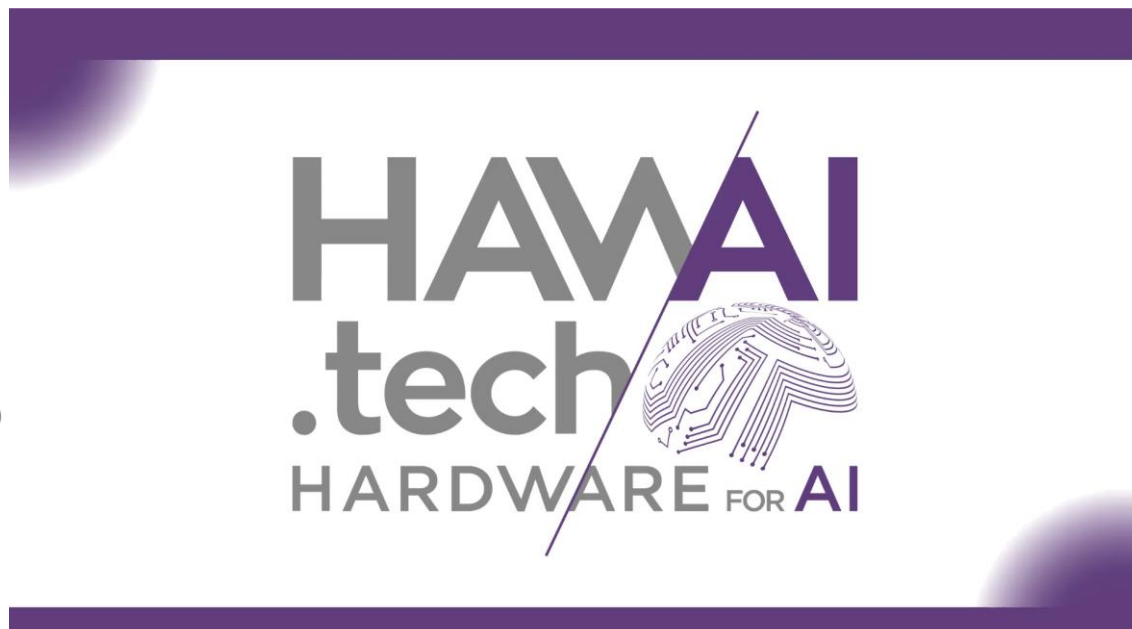
- **AI system** based on **probabilistic AI** (SSP, POMDP)
- **AI accelerator** at the **edge**, powerful and energy efficient
- A **custom-made solution** developed to respond to complex use-cases in uncertain environments



Competitive advantages

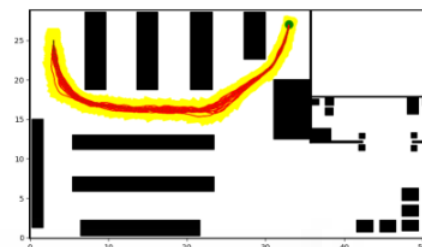
- **Real-time**: Continuous update
- **Uncertainty handling** : Adapt its behavior under constraints
- Hardware **more efficient** than classical GPU:

x6.4 more explorations vs. Nvidia Jetson Orin

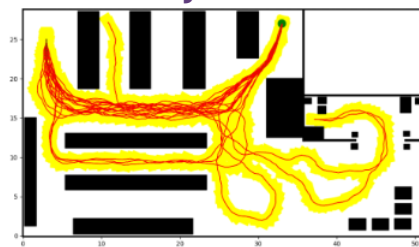


Full video at hawai.tech/demo

HawAI.tech



Nvidia Jetson Orin



Our offer: a full stack for rapid deployment

1. FPGA IP

Release Q2 2024

- **Flexible** FPGA architecture based-on Xilinx Versal cards (ACAP)
- **X6.4** energy efficient vs NVidia GPU (see demo)



2. Software suite

- Plug to standard languages with **probabilistic libraries** incl. sample apps
- Specific **Compilation** to optimize edge inference and learning
- **Guidance tools** for designing and analyzing Probabilistic models

One shot (NRR)
One per asset

\$\$

Recurrent revenue
One per sets of asset

\$\$

How do customers use our products?

PRODUCTS

1. BUILD SPECIFIC HARDWARE COMPUTATION PLATFORM



Compilation tool

provides design help for the specific chip

2. DEPLOY THE COMPUTATION PLATFORM



FPGA chip

enables fast execution

3. USE PROBABLISTIC AI & ACCELERATION PLATFORM



Guidance tool

probabilistic AI through computation platform



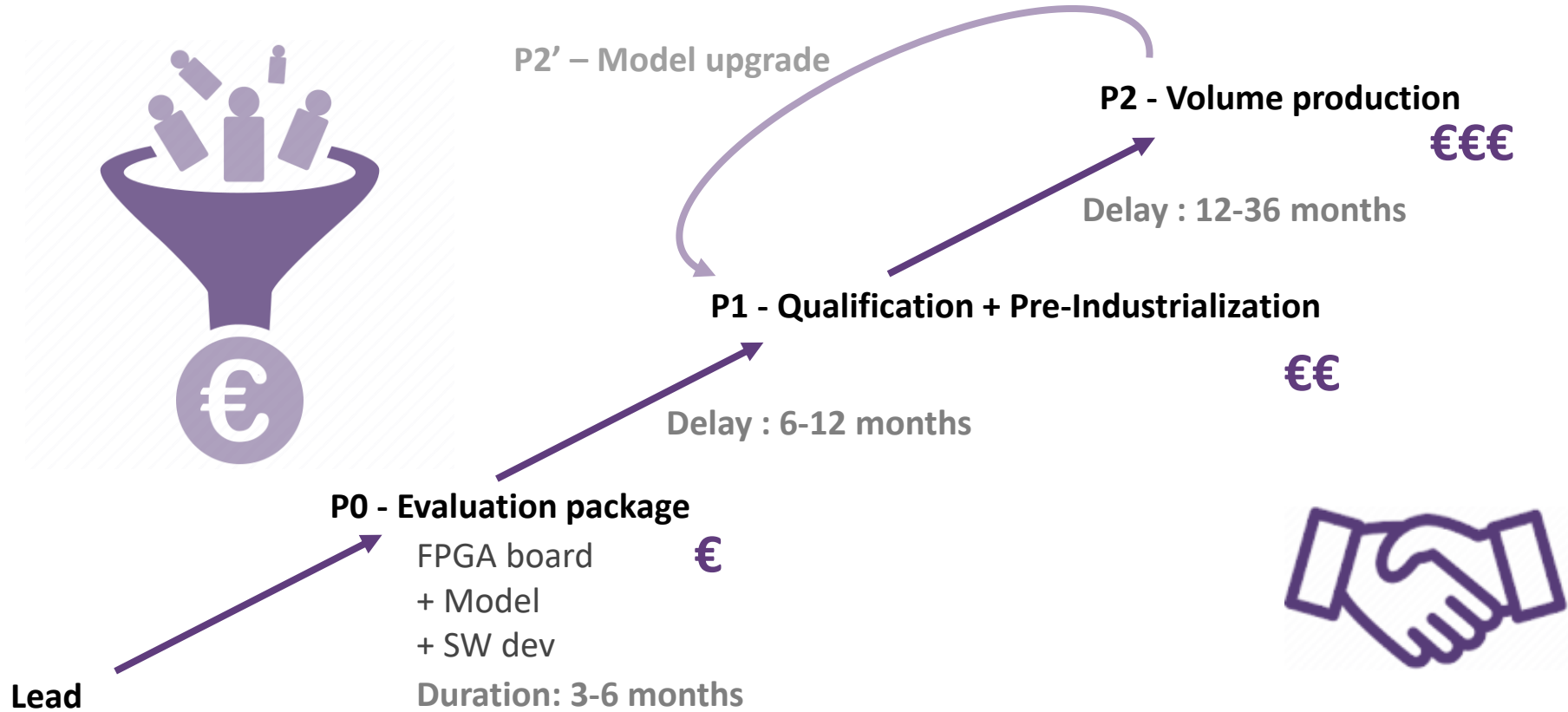
User

- Expert user: **describes data and model** and lets the tool configure the corresponding hardware platform
- Only **focuses on their problem**, their data, their model

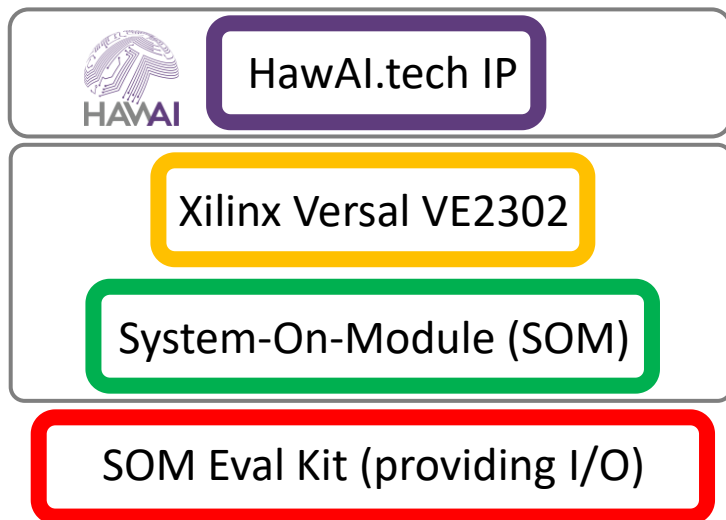
- Client owns its computing devices dedicated to its problem
- **Deployment at the edge** of these devices at the client site: vehicles, logistics, robotics, equipment, small computing centers...

- Expert user: configures a **meaningfull set of information to retrieve** from the model
- Regular users: receive alert, consult dashboard and take action

Client's projects steps

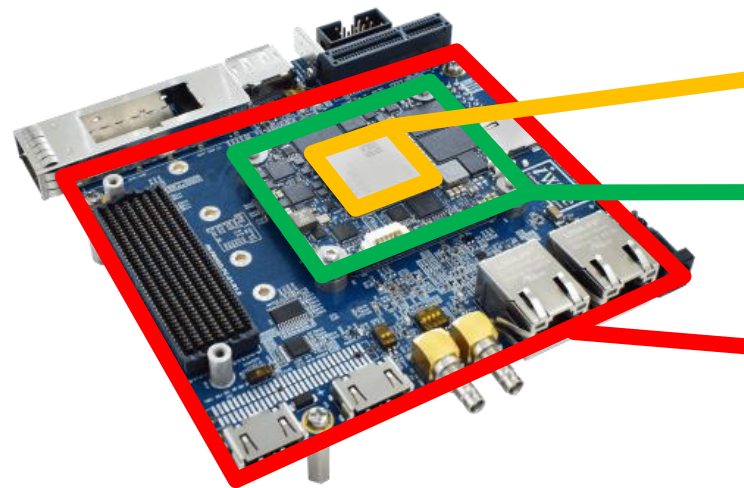


Pricing of HawAI.tech solution



450€

850€



Xilinx Versal
VE2302

System-On-
Module (SOM)

SOM Eval Kit
(providing I/O)

Total price for **HawAI.tech solution: 1300€**

more than **20% cheaper** than **Nvidia Jetson Orin AGX 64GB SOM (1650€)**

while being **more than x6 times more efficient** (see demo)

Competitive positioning: data center performance at the edge

TYPE	COMPETITORS	Explainability	Frugal training data set	Real-time capacity	Volume production	Reach & partners
IoT AI H/W	Greenwaves, Grai Matter Labs, Innaterra, Recogni, Syntiant, STM32, Renesas	×	×	≈	≈	≈
Embedded AI H/W	Vsora, Sima.AI, Kalray, Cornami, Hailo, Blaize, Ambarella, Nvidia Jetson	×	×	✓	≈	≈
HPC inference AI H/W	Neureality, Inferentia, UntetherAI, Esperanto, Nvidia A2	×	×	≈	≈	≈
HPC learning H/W (using probabilistic AI)	Google TPU, Nvidia A100, Normal, Ludwig	≈	≈	≈	✓	✓
Specific S/W	Covariant, AMP Robotics, Locus Robotics	×	×	✓	≈	×
Hardware & Software	HAWAI .tech	✓	✓	✓	×	×

A fast paced take-off

Pre-seed
350 k€
07.2019



Funding round

- 2.5M€ in equity
- Grants and BPI AR already secured

Q1 2019

2020

2021

2022

2023

2024

CNRS + INRIA
15 years

academic research
~60 Person-Years

BPI
BFTE

BPI
ADD

bpifrance

1st Patent
100% HawAI.tech

2 industrial
partnerships

PoC starting in
06.2024

French defense
industry leader

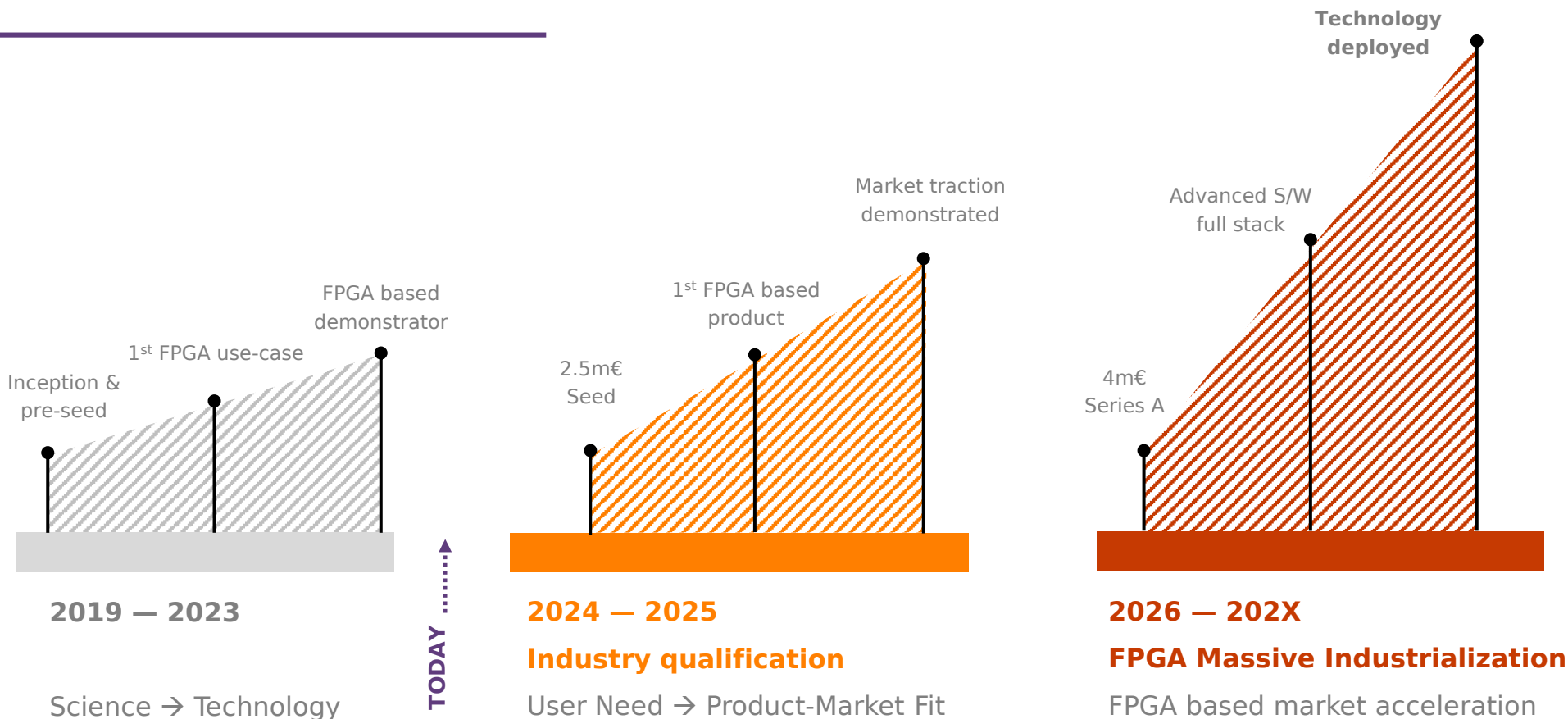


Framework contract
with EDF energy utility



(ongoing)

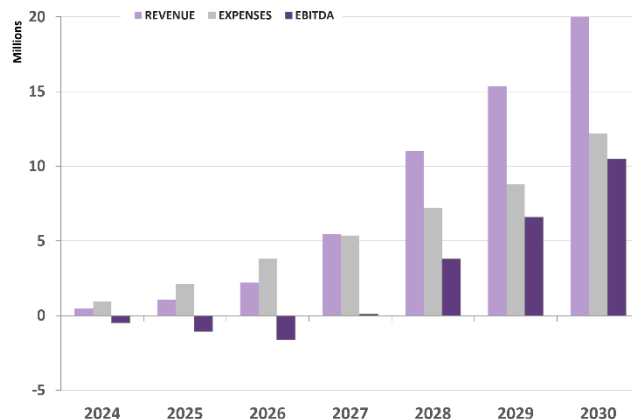
Roadmap



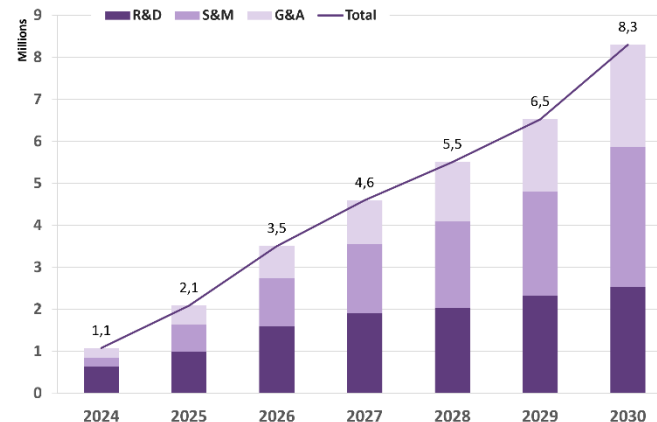
* FPGA: w/ MCMCE IP + custom accelerators

Financial forecast – Quadrigraph

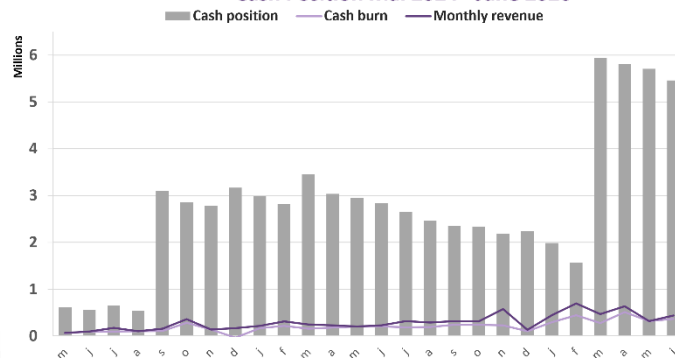
Sales - Expenses - EBITDA 2024-2030



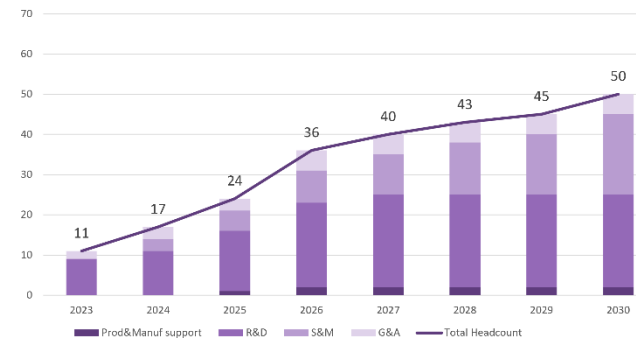
Total Operating Expenses 2024-2030



Cash Position Mai 2024 - June 2026



Staff 2023-2030



Investment thesis: 2,5M€ fundraising

Financing need (Q3 2024 - Q1 2026)

• Capital increase	2,5 M€	
• Debt & cash advances	0,8 M€	(already secured)
• Grants	0,7 M€	(already secured)
Total	4,0 M€	

Investment considerations

- Fast-growing global market for explainable AI
- Proven traction - ind. leaders as customers/partners
- A clear technology leadership
- Expensive & complex market to entry

Already secured innovation programs :

On period 2022-2027

• Grants	2,7 M€
• Debt	1,1 M€

Use of proceeds

		Hirings
• HR	2,0 M€	18
• R&D	1,3 M€	10
• S&M	0,6 M€	5
• G&A	0,1 M€	2
• Production	0,1 M€	1
• External charges	1,0 M€	
• IP	0,2 M€	
• Capex	0,1 M€	

CapTable

Founders/Management	90 %
Probayes (Groupe La Poste)	10 %

HawAI.tech in the media

Présences
LE MAGAZINE DES ENTREPRISES DU SUD-ISÈRE

LE DAUPHINÉ
libéré

Challenge
L'économie de demain est l'affaire de tous

cnrs

B SMART

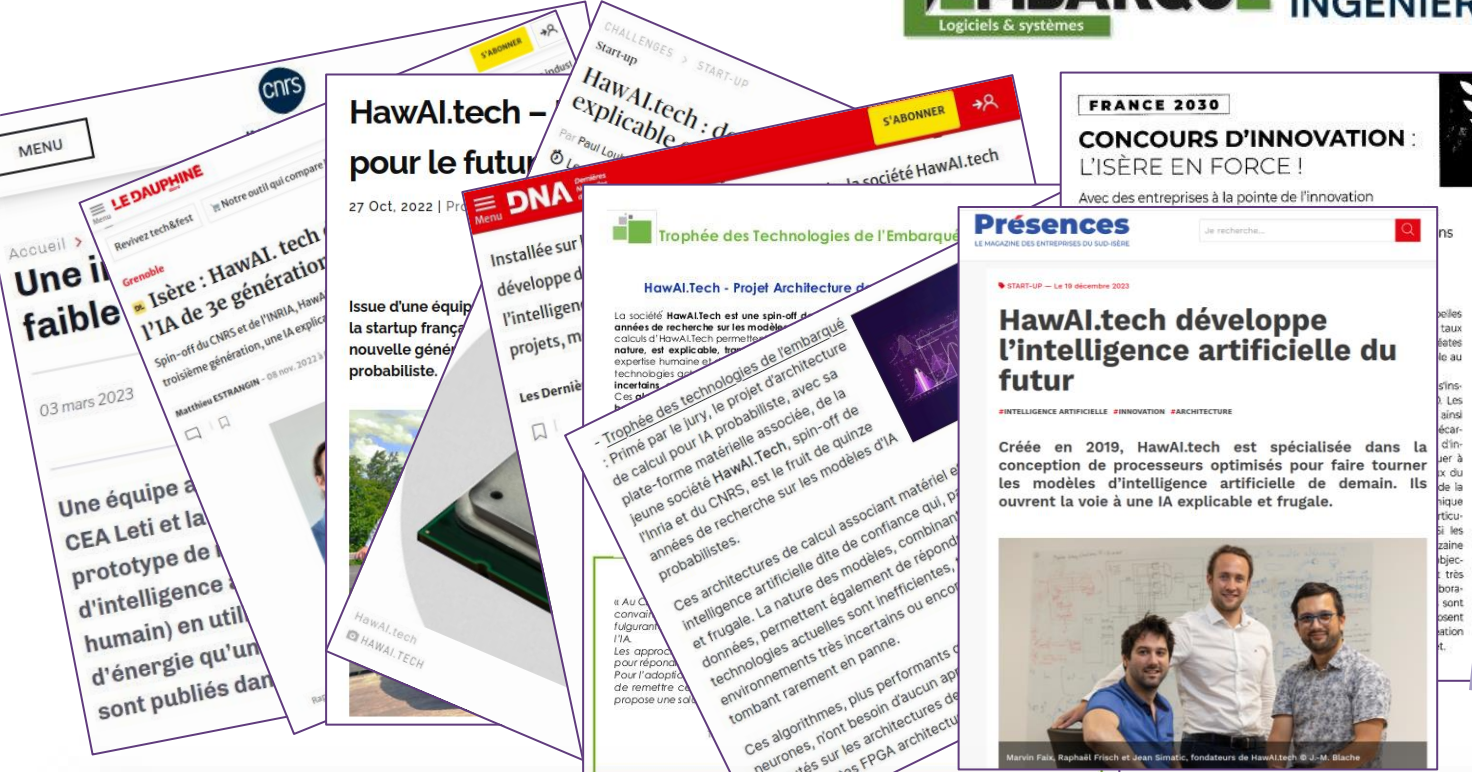
**BFM
BUSINESS**

EMBARQUÉ
Logiciels & systèmes
INGÉNIERIE



Milkshake Valley
Frappés d'innovation

DNA Dernières
Nouvelles
d'Alsace





CONTACT

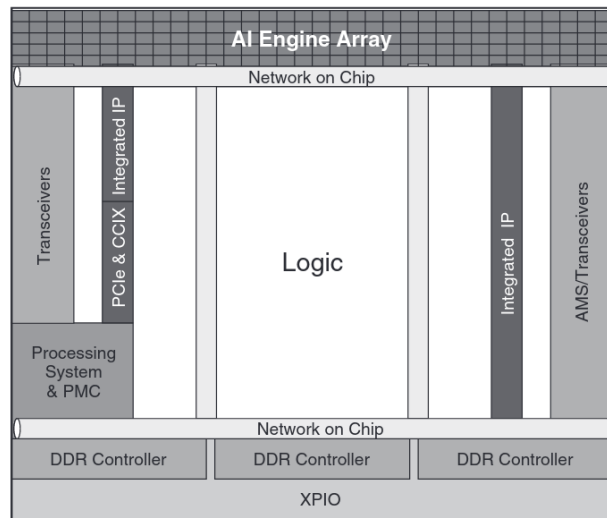
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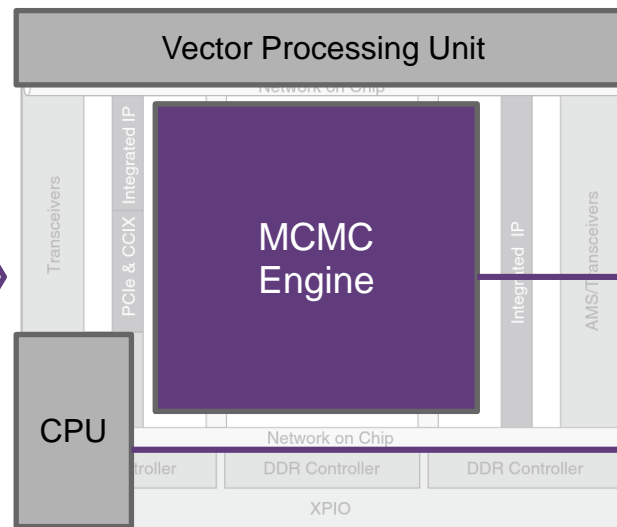
Sampling dedicated architecture

Xilinx Versal ACAP



Proprietary architecture

Inference machine



Classical multiprocessor architecture with DSP/DL capabilities as well as Audio/Video codecs.

Very efficient sampling from simple to complex distributions by integrating custom accelerators and mem hierarchy.

Application processor with platform management drivers

Sampling efficiency

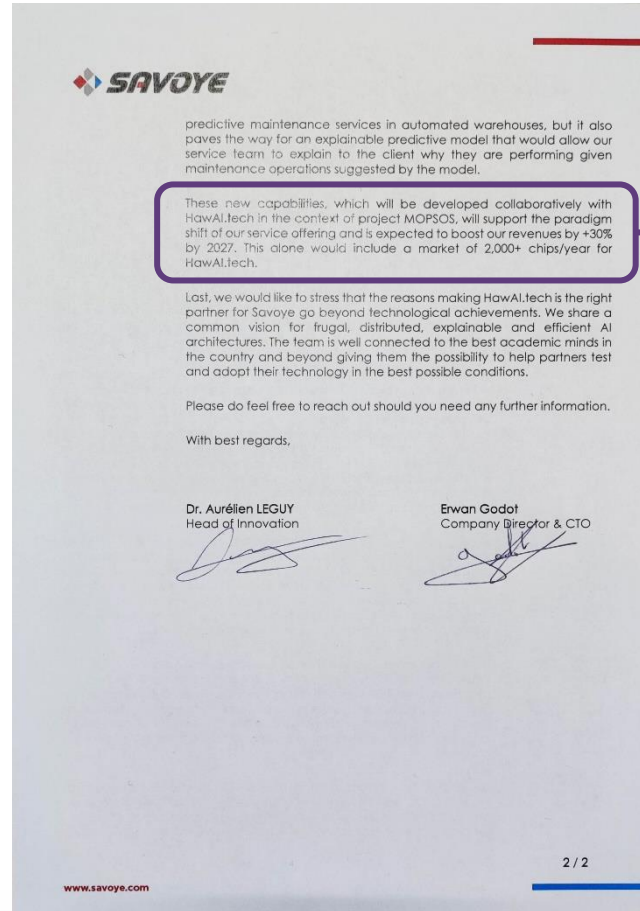
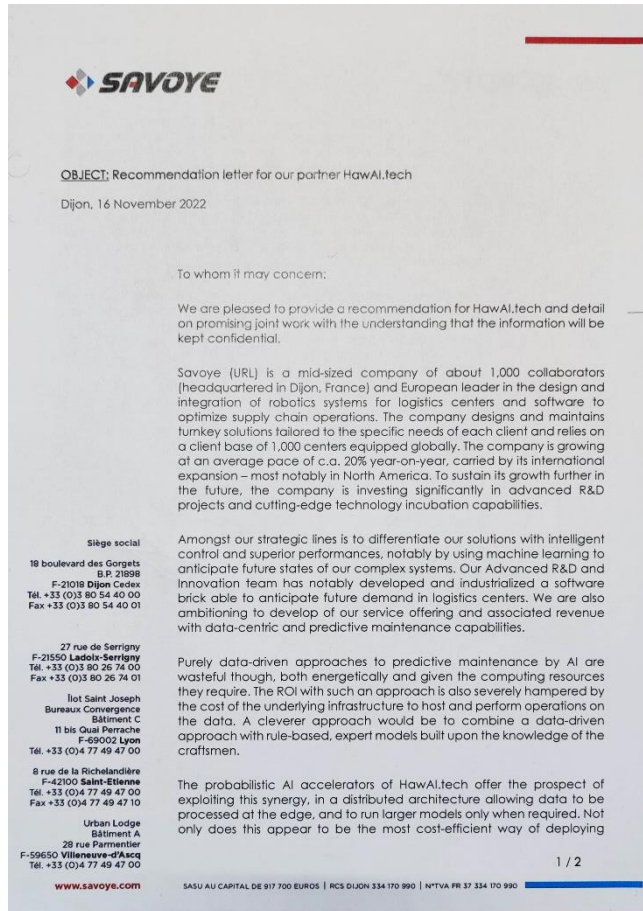
- 1-D Gaussian: 1 Gsample / s (x55)
- 32-D Gaussian: 4 Msample / s (x200)
- Non-parametric sampling (e.g. particle filters):
x5000 efficiency vs MCU (Belot 2022, Nature electronics 2023)

System level advantage

- Bayesian matrix factorization:
On FPGA: 100 Ksamples/s/W (x4.8)
On ACAP: 210 Ksamples/s/W (x10)
- Benchmark on POMDP solving: x6.4 vs Nvidia Jetson
See full video at hawaii.tech/demo

Except indicated otherwise, benchmarks are run on a Xilinx VCK500 @ 500MHz and compared to an Nvidia RTX 2080

Recommandation letter from Savoye



**Savoye needs:
2 000+ units/year
for 5 years min.**

