













1V82V53V35V	3	1V82V53V35V
I2CSPI DTRRTS		I2CSPI DTRRTS
1V82V53V35V	3	1V82V53V35V
I2CSPI TxDRxD		I2CSPI TxDRxD



1V82V53V35V	3	1V82V53V35V
I2CSPI DTRRTS		I2CSPI DTRRTS
1V82V53V35V	3	1V82V53V35V
I2CSPI TxDRxD		I2CSPI TxDRxD



1V82V53V35V	3	1V82V53V35V
I2CSPI DTRRTS		I2CSPI DTRRTS
1V82V53V35V	3	1V82V53V35V
I2CSPI TxDRxD		I2CSPI TxDRxD

1V82V53V35V	3	1V82V53V35V
I2CSPI DTRRTS		I2CSPI DTRRTS
1V82V53V35V	3	1V82V53V35V
I2CSPI TxDRxD		I2CSPI TxDRxD

1V82V53V35V	3	1V82V53V35V
I2CSPI DTRRTS		I2CSPI DTRRTS
1V82V53V35V	3	1V82V53V35V
I2CSPI TxDRxD		I2CSPI TxDRxD

1V82V53V35V	3	1V82V53V35V
I2CSPI DTRRTS		I2CSPI DTRRTS
1V82V53V35V	3	1V82V53V35V
I2CSPI TxDRxD		I2CSPI TxDRxD

1V82V53V35V	3	1V82V53V35V
I2CSPI DTRRTS		I2CSPI DTRRTS
1V82V53V35V	3	1V82V53V35V
I2CSPI TxDRxD		I2CSPI TxDRxD

1V82V53V35V	3	1V82V53V35V
I2CSPI DTRRTS		I2CSPI DTRRTS
1V82V53V35V	3	1V82V53V35V
I2CSPI TxDRxD		I2CSPI TxDRxD