

Lab 01 - Gates

Direction: Submit typed work on github.

For this lab, your objective is to create gate classes. Gates are the circuits used to create computer hardware. The gates are logic gate which means operate like to relational operators (they take true [charge] and false [no charge] values). The gates you create are

- ☐ AND gate
- ☐ Or gate
- ☐ NAND gate
- ☐ XOR gate
- ☐ NOT gate
- ☐ NOR gate