

COMP 270 HW 1

① Convert the following numbers to hexadecimal + then to binary:

② 716.67_{10}

$$\frac{716}{16} = 44 \text{ r } 12 \rightarrow \frac{94}{16} = 5 \text{ r } 14 \rightarrow \frac{2}{16} = 0 \text{ r } 2$$

$$\rightarrow 2CC$$

$$0.67 \times 16 = 10.72 \rightarrow 0.72 \times 16 = 11.52 \rightarrow 0.52 \times 16 = 8.32$$

$$0.32 \times 16 = 5.12 \rightarrow 0.12 \times 16 = 1.92 \rightarrow 0.92 \times 16 = 14.72 \rightarrow$$

$$0.72 \times 16 = 11.52 \rightarrow 0.52 \times 16 \rightarrow 0.\overline{AB851E}$$

$$2CC.\overline{AB851E} \quad * \quad 001011001100.101010111000010100011110$$

③ 66.17_{10}

$$\frac{66}{16} = 4 \text{ r } 2 \rightarrow \frac{4}{16} = 0 \text{ r } 4 \rightarrow 42_{16}$$

$$0.17 \times 16 = 2.72 \rightarrow 0.72 \times 16 = 11.52 \rightarrow 0.52 \times 16 = 8.32$$

$$0.32 \times 16 = 5.12 \rightarrow 0.12 \times 16 = 1.92 \rightarrow 0.92 \times 16 = 14.72$$

$$\rightarrow 2A851E$$

$$\rightarrow 42.\overline{2A851E}_{16} \quad * \quad 01000010.001010111000010100011110$$

④ 28.35_{10}

$$\frac{28}{16} = 1 \text{ r } 12 \Rightarrow \frac{12}{16} = 0 \text{ r } 12 \rightarrow 1C$$

$$0.35 \times 16 = 5.6 \rightarrow 0.6 \times 16 = 9.6 \rightarrow 0.6 \times 16 = 9.6 \} 59$$

$$1C.\overline{59}_{16} \quad * \quad 00011100.01011001_2$$

⑤ 12.81

$$12 = C \rightarrow .81 \times 16 = 12.96 \rightarrow 0.96 \times 16 = 15.36 \rightarrow 0.36 \times 16 = 5.76$$

$$0.76 \times 16 = 12.16 \rightarrow 0.16 \times 16 = 2.56 \rightarrow 0.56 \times 16 = 8.96$$

$$\rightarrow C.C\overline{F5C28}_{16} \quad * \quad 1100.110011110101110000101000_2$$

COMP 270 HW CONT.

(2) Add, subtract, & multiply in binary

(a) $1111 + 1010$

$$\begin{array}{r} 1111 \\ + 1010 \\ \hline 11001_2 \end{array}$$

$$\begin{array}{r} 1111 \\ - 1010 \\ \hline 0101_2 \end{array}$$

$$\begin{array}{r} 1111 \\ \times 1010 \\ \hline 11110 \\ 00000 \\ 111100 \\ 1111000 \\ \hline 10010110_2 \end{array}$$

(b) 11010×11101

$$\begin{array}{r} 11010 \\ + 11101 \\ \hline 1010011_2 \end{array}$$

$$\begin{array}{r} 11010 \\ - 11101 \\ \hline 11001_2 \end{array}$$

$$\begin{array}{r} 11010 \\ \times 11101 \\ \hline 11010 \\ 110100 \\ 1101000 \\ 11010000 \\ 110100000 \\ \hline 1100001110_2 \end{array}$$

(c) 100100×10110

$$\begin{array}{r} 100100 \\ + 10110 \\ \hline 111010_2 \end{array}$$

$$\begin{array}{r} 100100 \\ - 10110 \\ \hline 01110_2 \end{array}$$

$$\begin{array}{r} 100100 \\ \times 10110 \\ \hline 000000 \\ 1001000 \\ 00000000 \\ 100100000 \\ 1001000000 \\ \hline 110001100_2 \end{array}$$

3. (a) Assume the integers below are in 1's Complement form. Find the decimal values of these numbers

(i) $00000000_2 = 0_{10}$

(ii) 11111111_2
flip bits
 $00000000_2 = 0_{10}$

(iii) 0110011_2
 $= 2^0 + 2^1 + 2^4 + 2^5 = 51_{10}$

(iv) 10000000_2

flip bits $\Rightarrow 01111111_2$
 $15 + 2^4 + 2^5 = 15 + 16 + 32 = 63_{10}$

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(b) Repeat 3(a), assuming the binary numbers are in 2's complement form

(i) $0000000_2 = 0_{10}$ (ii) 1111111_2 flip bits $\rightarrow 0000000_2$ add one $= -1_{10}$
 (iii) $0100011_2 = 5_{10}$ (iv) 1000000_2 flip bits $= 0111111$ add one $= 1000000 = -64_{10}$

(c) Find the binary 2's complement representation of the following decimal values

(i) $28_{10} = 0111000_2$ (ii) $-7_{10} = 0111$ flip bits $\rightarrow 1000$ add one $= 1001 (= 1001_2)$
 (iii) $-42_{10} = 0101010_2$ flip bits $\rightarrow 1010101_2$ add one $\rightarrow 1010110_2 (= 1010110_2)$
 (iv) $-15_{10} = 01111_2$ flip bits $\rightarrow 10000_2$ add one $= 10001_2$

(4) Prove the following theorems using truth table

(a) $X + XY = X$

| X | Y | XY | X + XY |
|---|---|----|--------|
| 0 | 0 | 0 | 0 |
| 0 | 1 | 0 | 0 |
| 1 | 0 | 0 | 1 |
| 1 | 1 | 1 | 1 |

SAME OUTCOMES, SO THIS $X = X + XY$

(b) $X + YZ = (X + Y)(X + Z)$

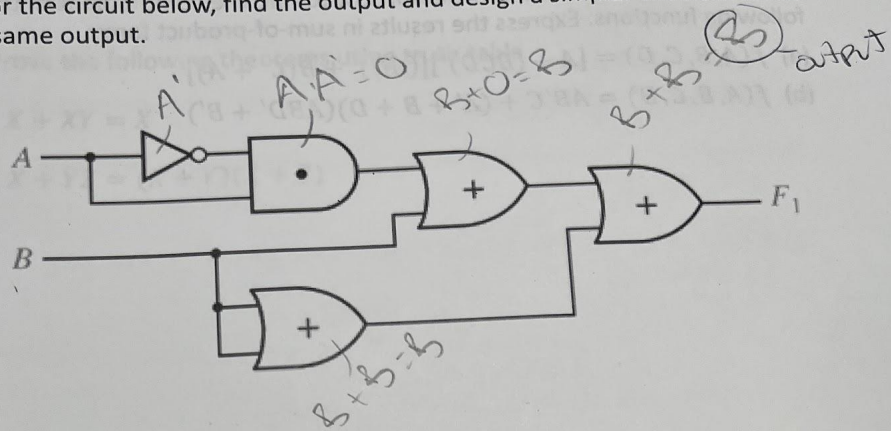
| X | Y | Z | YZ | X + Y | X + Z | (X + Y)(X + Z) | X + YZ |
|---|---|---|----|-------|-------|----------------|--------|
| 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 |
| 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 |
| 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |
| 1 | 0 | 0 | 0 | 1 | 1 | 1 | 1 |
| 1 | 0 | 1 | 0 | 1 | 0 | 0 | 0 |
| 1 | 1 | 0 | 0 | 1 | 1 | 1 | 1 |
| 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |

SAME ANSWERS, THUS

$(X + Y)(X + Z)$

$= X + YZ$

5. For the circuit below, find the output and design a simpler circuit that has the same output.



$B = F_1$

⑥ Use DeMorgan's law + Inclusion law to find the complement of the following function. Express the results in sum of product form.

$$\begin{aligned}
 \textcircled{a} f(a,b,c,d) &= [A + (BCD)'] [(AD)' + B(C' + A)] \\
 &= [A + (BCD)'] [(AD)' + B(C' + A)] \\
 &\rightarrow ([A + (BCD)'])' + ([AD]' + B(C' + A))' \\
 &\rightarrow (A'(BCD)) + (AD(B(C' + A)))' \\
 &\rightarrow (A'(BCD)) + (AD(B' + (C' + A))) \rightarrow (A'(BCD)) + (AD(B' + CA)) \\
 &\rightarrow A'BCD + AB'D + ACDA \rightarrow 0 \\
 &= A'BCD + AB'D
 \end{aligned}$$

$$\begin{aligned}
 \textcircled{b} f(A,B,C,D) &= ABC + (A' + B'D)(ABD' + B') \\
 &= (ABC + (A' + B'D)(ABD' + B'))' \\
 &\rightarrow (ABC)'((A' + B'D)(ABD' + B'))' \\
 &\rightarrow (A' + B + C')((A' + B'D)' + (ABD' + B'))' \\
 &\rightarrow (A' + B + C')((AB'D)' + ((ABD')'B)) \\
 &\rightarrow (A' + B + C')((AB'D)' + (A' + B'D)B) \\
 &\rightarrow (A' + B + C')((AB'D)' + (A'B + B'B'D + BD)) \\
 &\rightarrow A'AB'D + A'AB + ABD + AB'D + A'B + BBD + C'AB'D + CA'B + C'BD \\
 &\rightarrow A'B + ABD + AB + BD + ABC'D' + A'BC' + BC'D \\
 &\rightarrow A'B + BD + ABC'D' + BC'D' + A'BC'
 \end{aligned}$$