

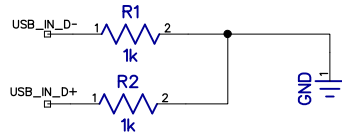
2

1

REVISIONS

ZONE	REV	DESCRIPTION	DATE	APPROVED
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USB PULL UP POP. OPTIONS



3.3v

R3

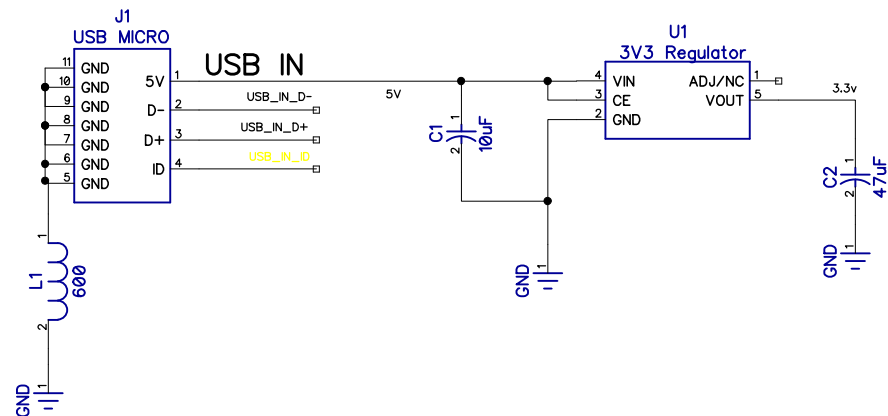
300

D1

GND

POWER LED

Power Reg.



Power Distribution

Tessel working schematic  
Technical Machine

SIZE	FSCM NO.	DWG NO.	REV
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SCALE	Power Dist.
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2

1

2

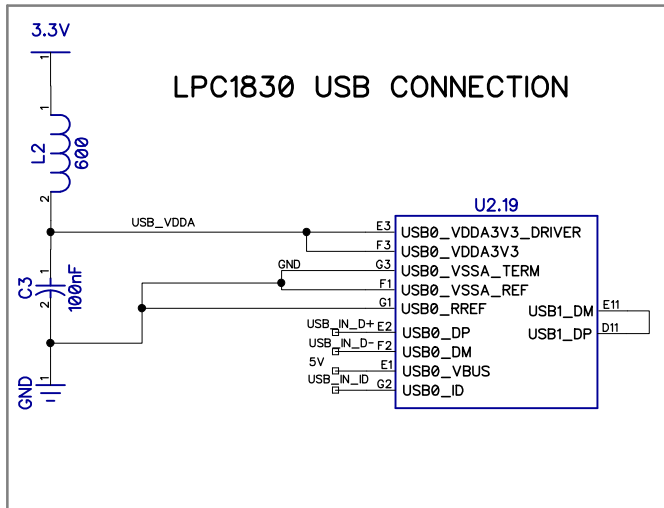
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REVISIONS

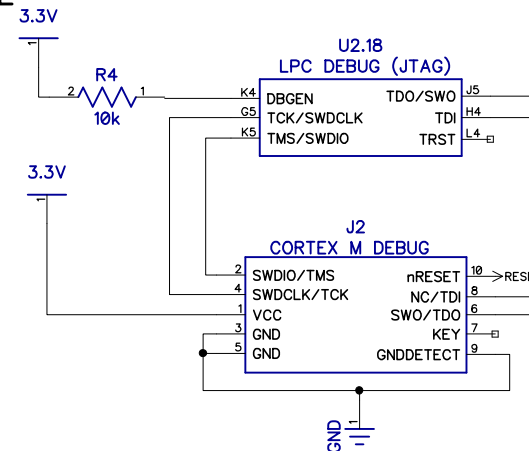
ZONE	REV	DESCRIPTION	DATE	APPROVED
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B

B



JTAG INTERFACE



A

A

LPC1830 Comms

Tessel working schematic  
Technical Machine

SIZE	FSCM NO.	DWG NO.	REV
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SCALE

LPC1830 Comms

2

1

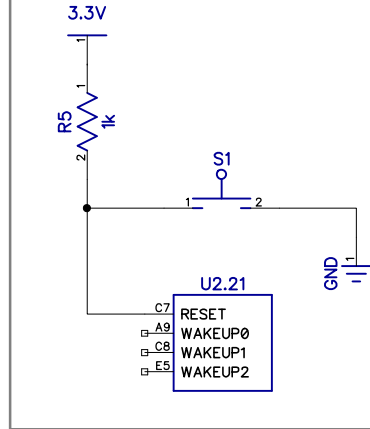
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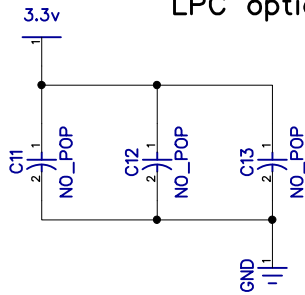
REVISIONS

ZONE	REV	DESCRIPTION	DATE	APPROVED
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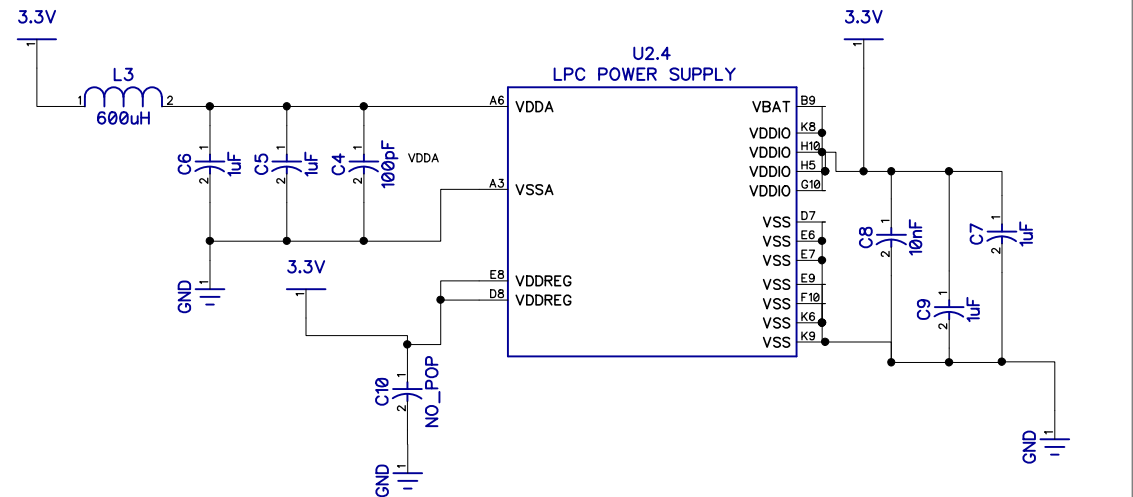
Reset switch



LPC optional decoupling bank



LPC power supply



LPC1830 Power

Tessel working schematic  
Technical Machine

SIZE	FSCM NO.	DWG NO.	REV
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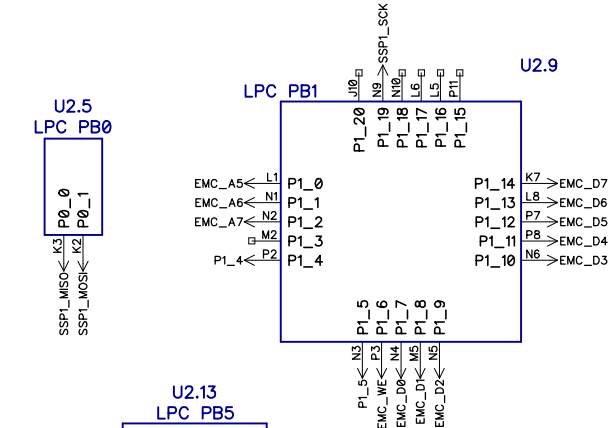
SCALE	LPC1830 Power
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2

1

REVISIONS				
ZONE	REV	DESCRIPTION	DATE	APPROVED

U2.22 LPC ADC		
ADC_0<B6	ADC0_0/ADC1_0/DAC	ADC0_7/ADC1_7
ADC_1<C4	ADC0_1/ADC1_1	ADC0_6/ADC1_6
ADC_2<B3	ADC0_2/ADC1_2	ADC0_5/ADC1_5
B4	ADC0_3/ADC1_3	ADC0_4/ADC1_4

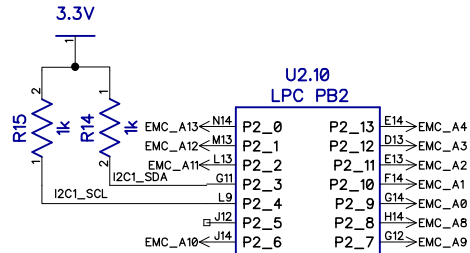
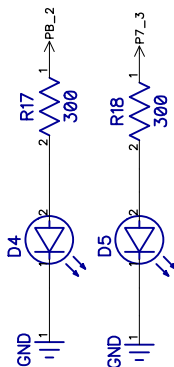


U2.13 LPC PB5		
EMC_D12<L2	P5_0	P5_7
EMC_D13<M1	P5_1	P5_6
EMC_D14<M3	P5_2	P5_5
EMC_D15<P6	P5_3	P5_4

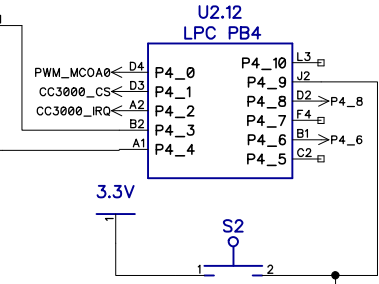
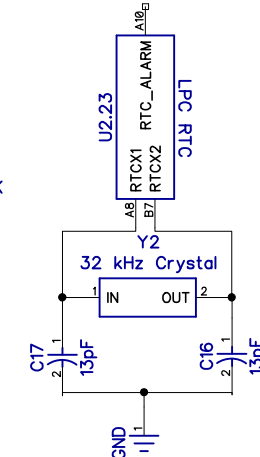
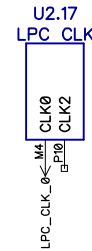
U2.16 LPC PB9		
P9_0<P1	P9_6	M9
SSP0_MISO<P4	P9_5	L7
SSP0_MOS<M6	P9_4	M8
	P9_3	P5

U2.14 LPC PB6		
M10	P6_0	P6_12
P14	P6_1	P6_11
K11	P6_2	P6_10
N13	P6_3	P6_9
M14	P6_4	P6_8
L14	P6_5	P6_7
	P6_6	P6_6

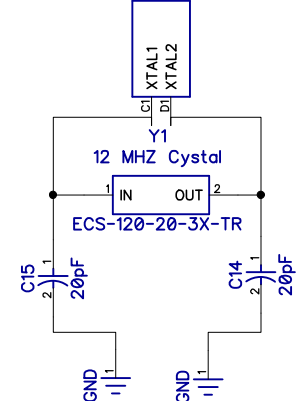
Debug LEDs



U2.7 LPC PB3		
SSP0_SCK<D12	P3_0	P3_8
D10	P3_1	P3_7
D9	P3_2	P3_6
B13	P3_3	P3_5



SimpleLink Switch

U2.24  
LPC CRYSTAL

LPC1830 Ports

Tessel working schematic  
Technical Machine

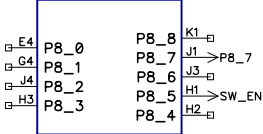
SIZE	FSCM NO.	DWG NO.	REV
SCALE	LPC1830 Ports		

2

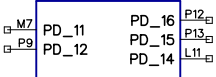
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REVISIONS				
ZONE	REV	DESCRIPTION	DATE	APPROVED

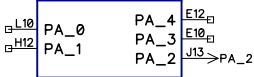
U2.25  
LPC PB8



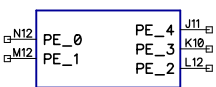
U2.28  
LPC PBD



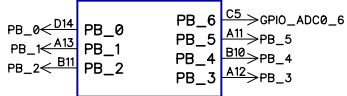
U2.26  
LPC PBA



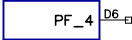
U2.29  
LPC PBE



U2.27  
LPC PBB



U2.30  
LPC PBF



LPC1830 misc. ports

Tessel working schematic  
Technical Machine

SIZE	FSCM NO.	DWG NO.	REV
SCALE	LPC1830 extra ports		

2

1

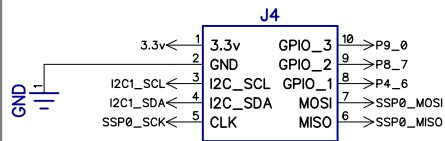
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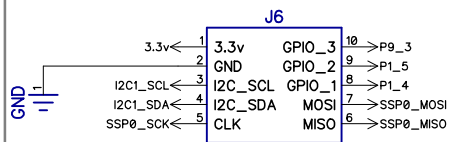
REVISIONS

ZONE	REV	DESCRIPTION	DATE	APPROVED
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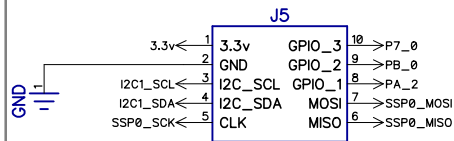
Lower Left (A)



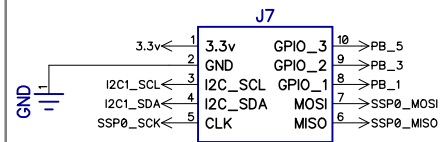
Lower Right (B)



Upper Right (D)

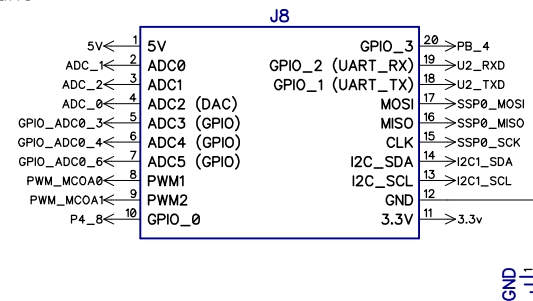


Upper Left (C)



GPIO bank (E)

Not a mistake



Modules and GPIO

Tessel working schematic  
Technical Machine

SIZE	FSCM NO.	DWG NO.	REV
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SCALE	Peripherals
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2

1

2

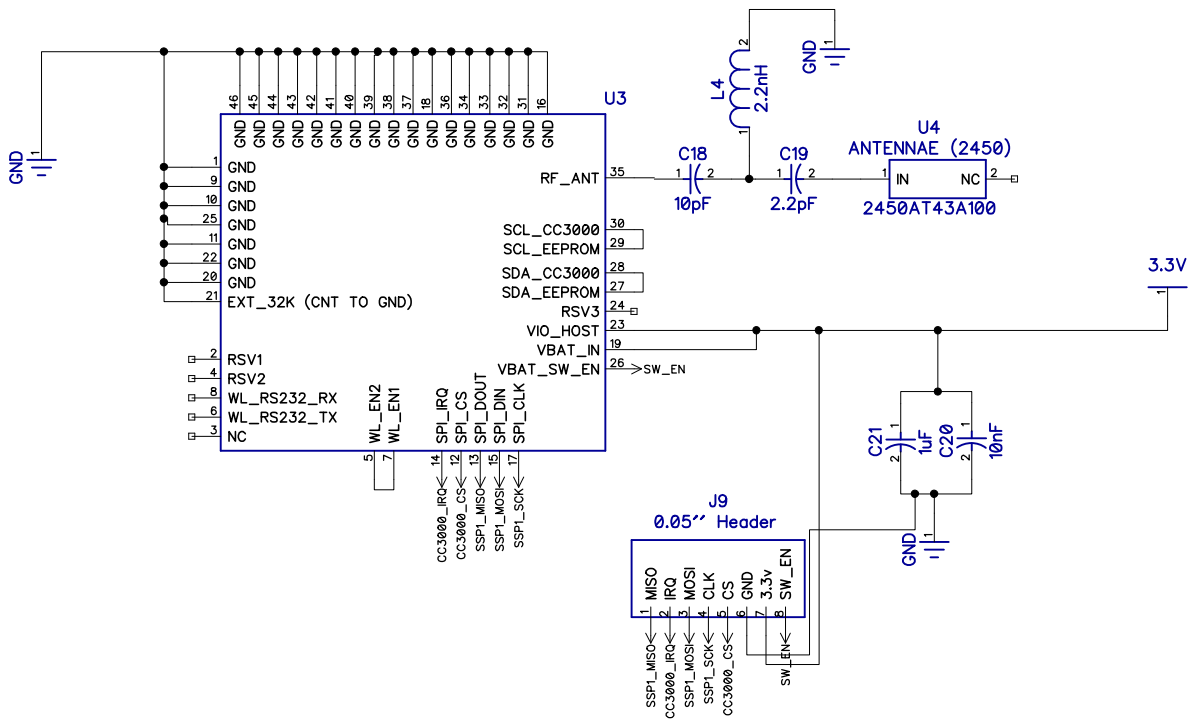
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REVISIONS

ZONE	REV	DESCRIPTION	DATE	APPROVED
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B

B



A

A

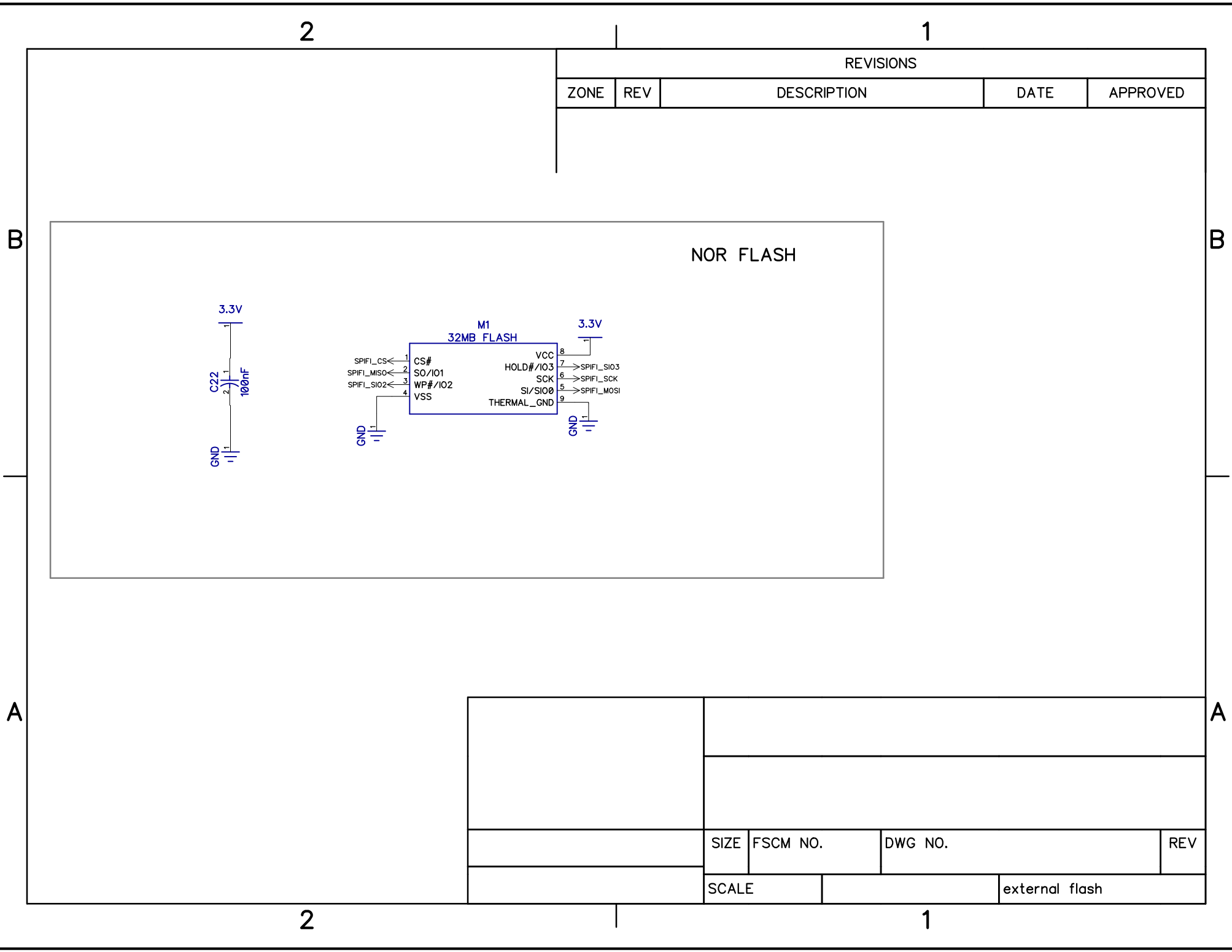
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1

CC3000

Tessel working schematic  
Technical Machine

SIZE	FSCM NO.	DWG NO.	REV
SCALE			CC3000





2

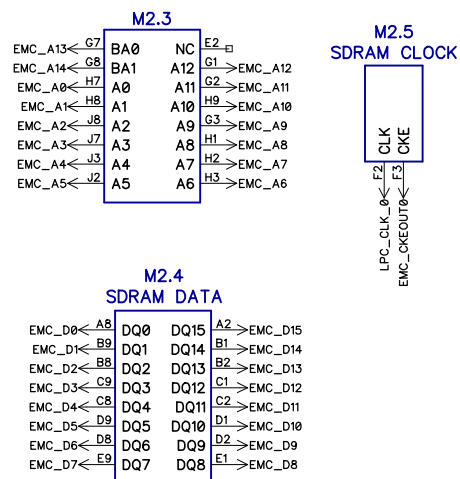
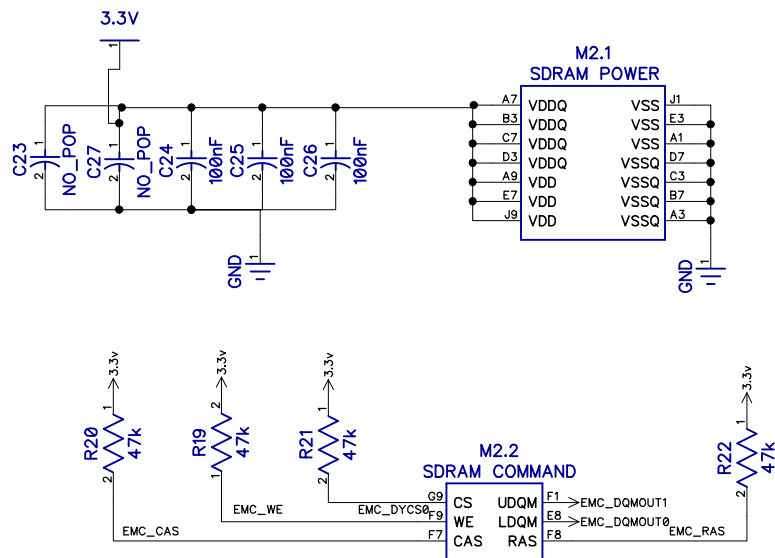
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## REVISIONS

ZONE	REV	DESCRIPTION	DATE	APPROVED
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B

B



A

A

2

1

SIZE	FSCM NO.	DWG NO.	REV
SCALE	external ram		