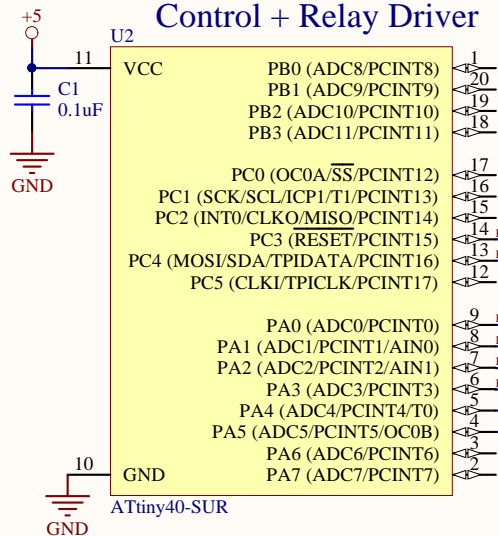


Control + Relay Driver



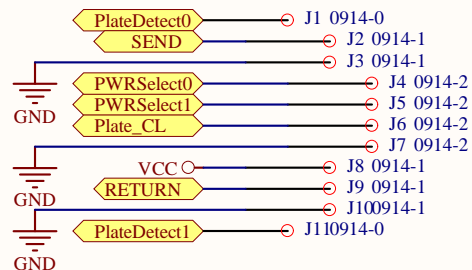
The currently unused pins of the uC are for the second receiver, which is not being implemented for the first prototype. All unused pins should have their pullups enabled to prevent them from floating.

Be sure to enable input pullups

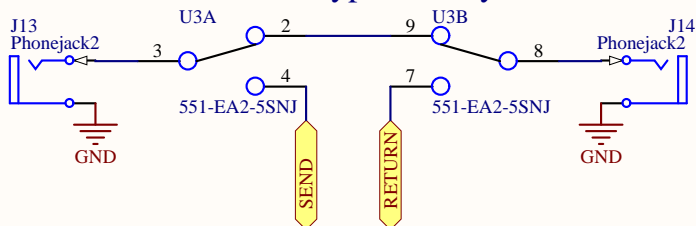
Be sure to enable input pullups

The LV8548 has built in flyback diodes and shoot-through protection.

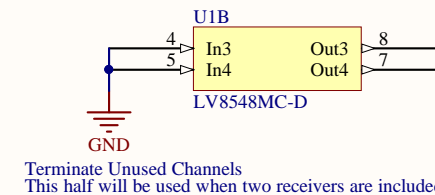
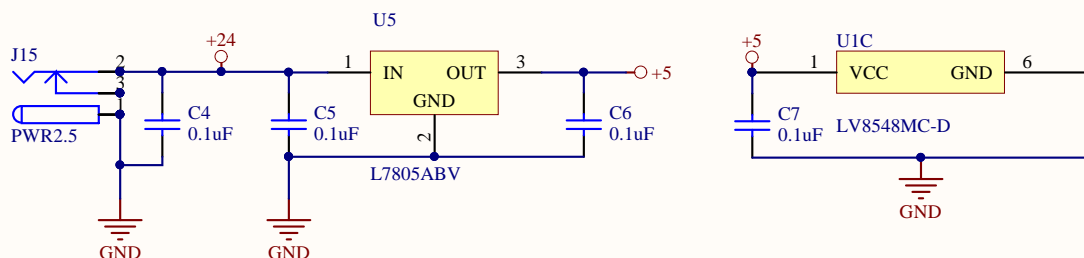
Pogo Pins



Hard Bypass Relay

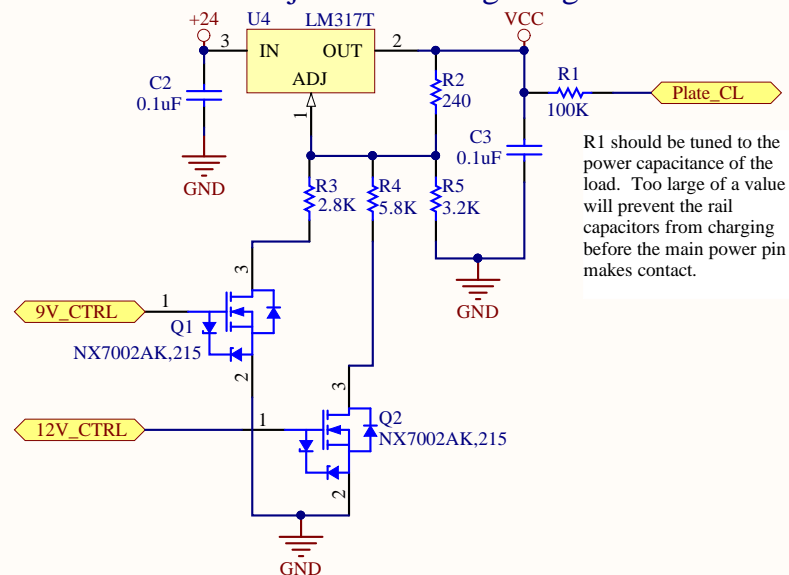


Logic Level 5V Regulator



Terminate Unused Channels
This half will be used when two receivers are included

Adjustable Voltage Regulator



R1 should be tuned to the power capacitance of the load. Too large of a value will prevent the rail capacitors from charging before the main power pin makes contact.

Title		
Size	Number	Revision
A		
Date:	11/5/2018	Sheet of
File:	C:\Users\...\Sheet2.SchDoc	Drawn By: