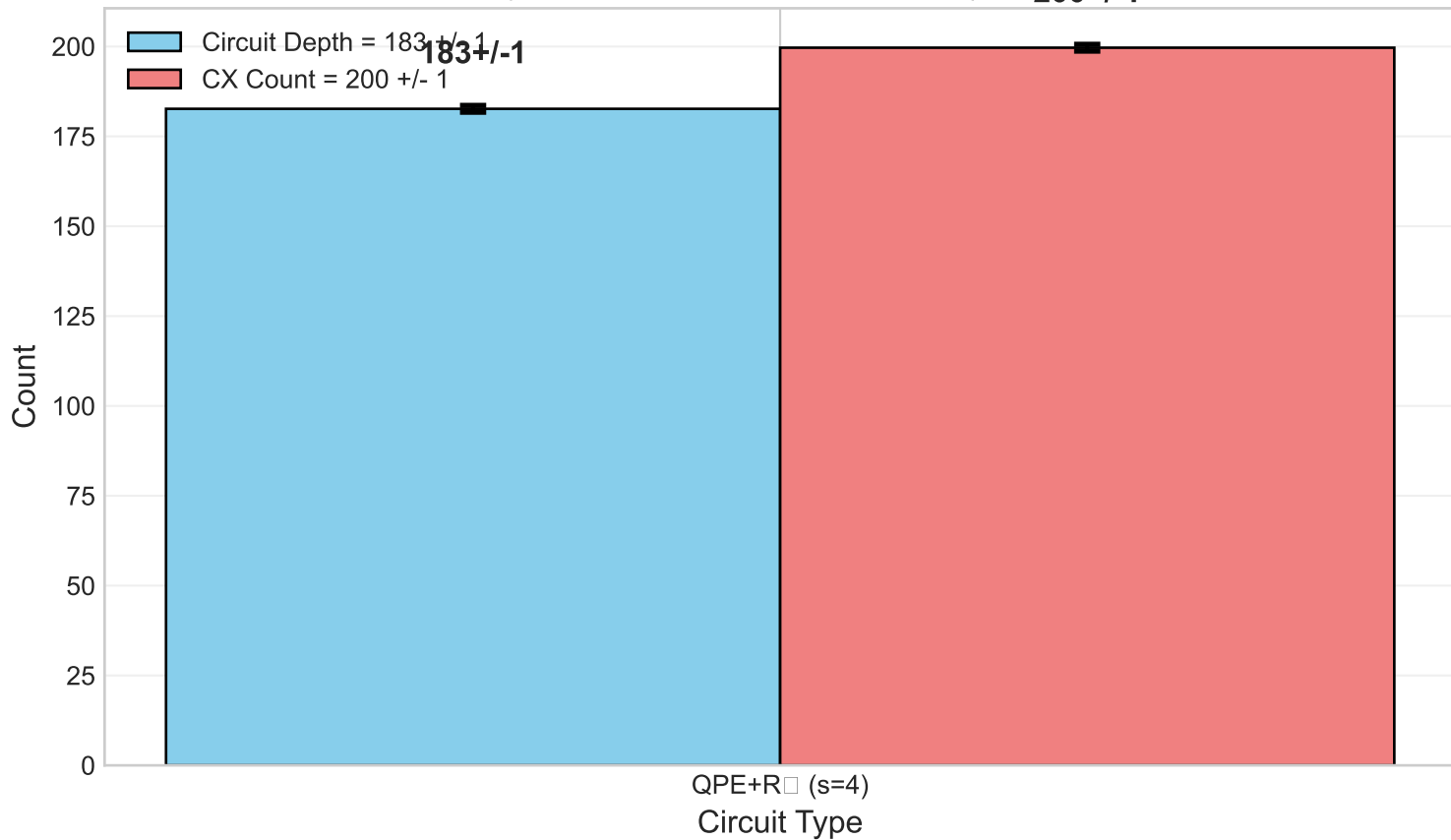


# Circuit Complexity after optimization\_level=3 (s=4) on aer\_simulator



Circuit complexity after optimization\_level=3 for QPE+R (s=4) on aer\_simulator.  
Depth = 183 +/- 1, CX = 200 +/- 1 (variations from compile-time jitter).