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Secret Code: 00100101

Files:
ReadMe
Lab3_370.clf
Lab3CS370.cct
Lab3CS370.bak

Overview:

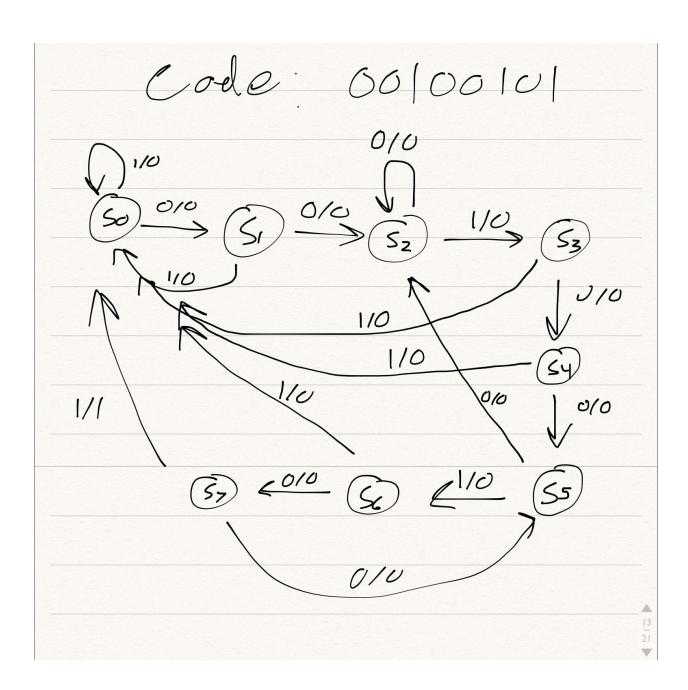
The goal for this circuit was to create a Finite State Machine that detected a certain eight digit binary sequence in a continuous serial input stream. When the correct sequence is detected, the single output signal is logical true, in all other cases it is logical false. This circuit has a shut off switch. If 16 bits are toggled in without the secret code being observed, the system shuts off. There is also a reset switch that when pressed allows the system to again accept input bits.

Users should use the buttons to input their desired bits. If the secret code of 00100101 is inputted within 16 bits, the output will display 1. If the secret code is not inputted within 16 bits, the system will lock and the user must reset the system if they want it to output 1.

components of secret code:

This file takes in the 2 inputs of 0 or 1 and puts them through an SR latch. This allows the system to "remember" which button was just pressed and the output of this latch is the input. There is also a pulse that is created by ORing the two input buttons, This creates a "clock" for the flip flops. This pulse is also used within the lock to detect when 16 bits have been inputted. One feature of the lock is that it resets when the secret code has been inputted 16 times. In order to accomplish this I ORed the output of z, with the reset switch. This allows the lock to be reset when the code is observed OR the switch is hit.

Diagrams, Tables, K maps, Equations:



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