

## Blackboard Notes for lecture - 3

slide 8:

A theoretical relationship between  $i_c$  and  $V_{CE}$  is that  $i_c$  is parallel with  $V_{CE}$ . When  $V_{CE}$  reaches a certain value, but in practical product, when you measure a NPN transistor, the  $i_c$  is not parallel with  $V_{CE}$  anymore. We call it early effects. To model the effects:

$$I_c = I_s \exp \frac{V_{BE}}{V_T} \left( 1 + \frac{V_{CE}}{V_A} \right). \quad V_A \text{ is early voltage.}$$

slide 9:

if we have a small perturbation ( $\Delta V$ ) on  $V_{CE}$ , then  $V_{CE} + \Delta V \rightarrow V_{CE}$

$$\begin{aligned} I_c &= I_s \exp \frac{V_{BE}}{V_T} \left( 1 + \frac{V_{CE} + \Delta V}{V_A} \right) \\ &= I_s \exp \frac{V_{BE}}{V_T} \left( 1 + \frac{V_{CE}}{V_A} + \frac{\Delta V}{V_A} \right) \\ &= \underbrace{I_s \exp \frac{V_{BE}}{V_T} \left( 1 + \frac{V_{CE}}{V_A} \right)}_{I_{c0}} + I_s \exp \frac{V_{BE}}{V_T} \cdot \frac{\Delta V}{V_A} \\ &\approx I_{c0} + \underbrace{I_s \exp \frac{V_{BE}}{V_T} \cdot \frac{\Delta V}{V_A}} \end{aligned}$$

it means with the inclusion of early effects, there is one more current ( $I_s \exp \frac{V_{BE}}{V_T} \cdot \frac{\Delta V}{V_A}$ ) in the collector lead. and the current is

$$\Delta I = I_s \exp \frac{V_{BE}}{V_T} \cdot \frac{\Delta V}{V_A} \Rightarrow \frac{\Delta V}{\Delta I} = \frac{V_A}{I_s \exp \frac{V_{BE}}{V_T}} \approx \frac{V_A}{I_c}$$

then can be modeled with a resistor ( $r_o$ )

Slide 18:

when  $V_s = 0$ , Base and Emitter will be short,

the transistor will operate at cutoff state.