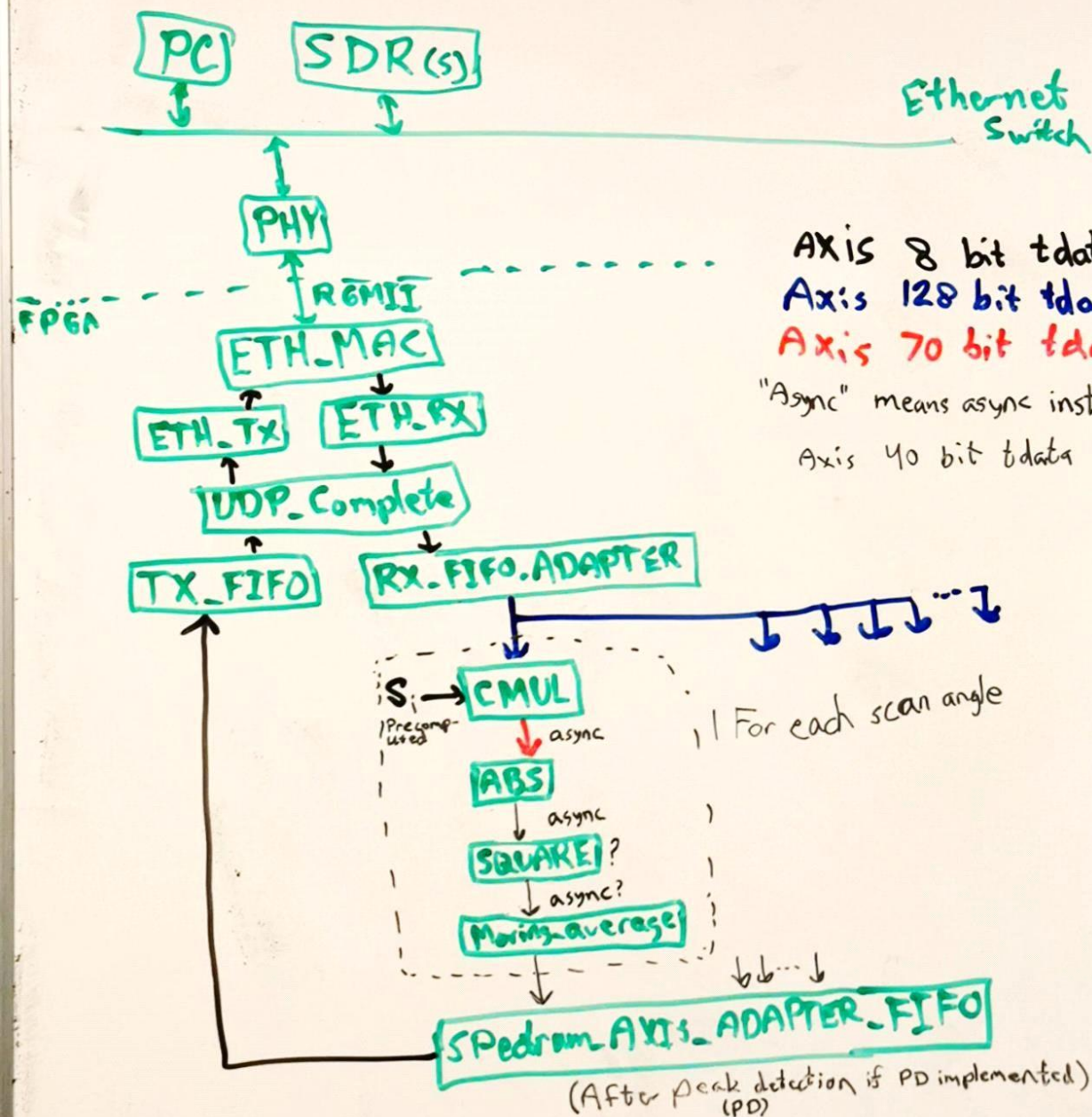


FPGA MVP

(Missing peak detection)



Axis 8 bit tdata

Axis 128 bit tdata

Axis 70 bit tdata

"Async" means async instead of axis

Axis 40 bit tdata