#### Advertisement





## DESIGN < HTTPS://WWW.EDN.COM/CATEGORY/DESIGN/> DESIGN IDEAS < HTTPS://WWW.EDN.COM/CATEGORY/DESIGN/DESIGN-IDEA/>

#### Precision DC motor speed control via pot or DAC

OCTOBER 4, 2022 <
HTTPS://WWW.EDN.COM/PRECISION-DCMOTOR-SPEED-CONTROL-VIA-POT-OR-DAC/>
BY STEPHEN WOODWARD <
HTTPS://WWW.EDN.COM/AUTHOR/STEPHENWOODWARD/>

₱ COMMENTS 11 <
HTTPS://WWW.EDN.COM/PRECISI
DC-MOTOR-SPEED-CONTROLVIA-POT-OR-DAC/#COMMENTS>



#### Advertisement

Ideally (in that magical land where theory and practice are the same thing), the speed of a permanent magnet DC motor is exactly proportional to the input voltage: **Speed = K\_SV**, where constant  $K_S$  is specific to the motor in question. But since real motors exist alongside us in the practical world, their behavior differs from this theoretical ideal, mostly because real motors include resistance: Mechanical resistance (friction), and electrical (winding resistance =  $R_W$ ). To produce the torque needed to overcome the former, motors must draw current (I) and when they do, current passing through  $R_W$  decreases the effective driving voltage (-

**IR**<sub>W</sub>). Therefore, when a motor is loaded by internal and/or external friction, current draw increases and effective voltage decreases. So, it slows down, as predicted by:

Speed = 
$$K_S(V - IR_W)$$

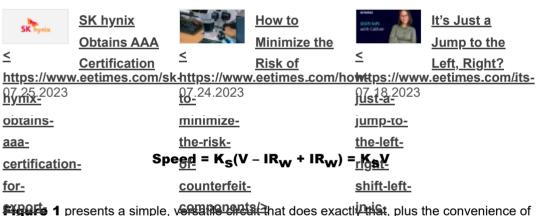
Wow the engineering world with your unique design: <u>Design Ideas Submission Guide < https://www.edn.com/design-ideas-submission-guide/?</u>
utm\_source=Aspencore&utm\_medium=EDN&utm\_campaign=link>

#### Advertisement

One way to reduce (or eliminate) this effect and make motor speed constant, despite varying frictional loading, is to actively cancel  $\mathbf{R}_{\mathbf{W}}$  by sensing current  $\mathbf{I}$  and adding a proportional compensating term to  $\mathbf{V}$ , thereby forcing:

#### Advertisement

#### **PARTNER CONTENT**



**Spatial**ng one of the motor connections. Here's how it works. designcompliance/>
enablement/>

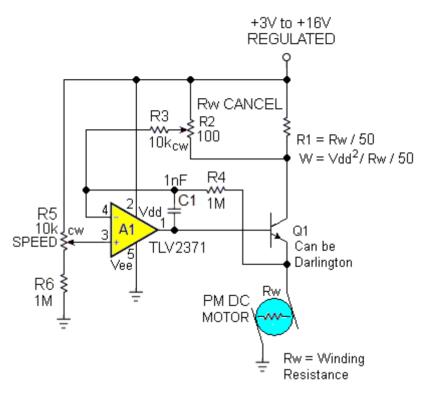


Figure 1 The R<sub>W</sub>-cancelling motor drive.

Rail-to-rail op-amp A1 compares the speed setpoint from potentiometer R5, variable from  $(0.99)V_{dd}$  to  $V_{dd}$ , to the motor drive voltage  $\mathbf{V_S}$  that is output by Q1 via the 100:1 R4/R3 voltage divider feedback loop, and servos Q1 base current accordingly.  $\mathbf{V_S}$  is thereby settable from a minimum of zero (R5 fully CCW), to a maximum of  $V_{dd}$  minus the sum of the saturation voltages of A1 and Q1 (fully CW). The usable limits of  $V_{dd}$  are 3V to 16V, corresponding with A1's TLV2371 datasheet ratings. The  $V_{dd}$  and Q1 should be chosen for compatibility with motor drive ( $\mathbf{V_S}$  and current) requirements.

#### But what about R<sub>W</sub> cancellation?

Motor current **I**, as reflected in Q1's collector current, is sensed by R1. An adequately accurate estimate for R1, given that cancellation is ultimately fine-tuned with R2, can be based on a simple multimeter measurement of  $R_W$  while the motor shaft is held stationary. Typical R1 resistances (R1 =  $R_W$  / 50) will lie in the milliohm range, making implementation of R1 with a simple circuit-board-trace meander feasible. This is a good thing because a circuit board trace, being copper, will have a temperature coefficient similar to the (likewise copper) motor wiring: ~ 3930ppm/ $^{\rm O}$ C, which will improve the stability of  $R_W$  cancellation versus temperature, provided that motor and R1 are in similar thermal environments. Note that R1's voltage drop due to its connection to Q1's collector isn't subtracted from max motor drive, but instead makes due with the (otherwise wasted) difference between Q1's base and collector saturation voltages. Note that the wattage rating needed for  $R1 = V_{cld}^2/R_W/50$  and a multiple of the IR1 voltage developed (as set by R2) is added to  $V_S$  via the +40dB R4/R3 feedback loop.

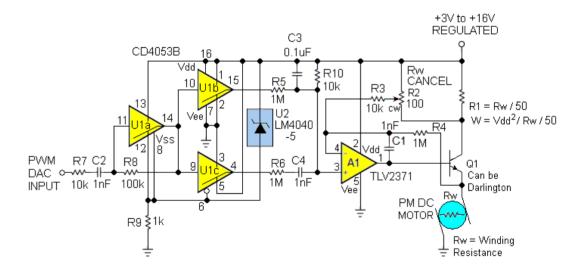
Proper adjustment of R2 for Rw cancellation and constant speed independent of friction will thereby result in the (magical?):

$$K_S(V - IR_W + IR_W) = K_SV$$

R2 adjustment can be accomplished in a variety of ways. For example, motor speed can be quantitatively measured (e.g., with an inexpensive optical tachometer) where R2 is adjusted for a constant speed while frictional load is varied. Or it can be done subjectively (by ear, listening to the pitch of the spinning motor's whine) for a similarly accurate result—if you can read music.

The Figure 1 circuit can thus generate a dialed-in motor drive voltage and compensate it for winding resistance, thereby achieving, and holding an accurate and stable set speed. But that speed is manually set by a pot wiper position. What if we need a more automated source of motor control?

**Figure 2** answers this question with a variant of Figure 1 that replaces potentiometer R5 with an inexpensive ripple-canceled PWM-DAC.



**Figure 2** PWM DAC control of R<sub>W</sub>-cancelling motor drive.

Gate U1a of triple-switch U1 regeneratively translates the logic level PWM input at R7 into a square wave output for gates U1b and U1c.  $V_{dd}$  voltages between 3 and 5V pass directly to U1 pin 8 ( $V_{SS}$ ) *via* R9, while  $V_{dd}$  > 5V causes shunt regulator U2 to kick in, limiting U1's  $V_{dd}$  - $V_{SS}$  to a logic compatible 5V.

U1b generates an uninverted PWM waveform for low-pass averaging by the R5, C3, and R10 network and output of  $\mathbf{V_S}$ , while U1c cranks out an AC-coupled inverted version suitable

for analog subtraction ripple cancelation *via* R6C4 per the scheme described in this **design** idea < https://www.edn.com/cancel-pwm-dac-ripple-with-analog-subtraction/>.

<u>Stephen Woodward < https://www.edn.com/author/stephen-woodward/></u>'s relationship with EDN's DI column goes back quite a ways. In all, a total of 64 submissions have been accepted since his first contribution was published in 1974.

#### **Related Content**

- Cancel PWM DAC ripple with analog subtraction 
   https://www.edn.com/cancel-pwm-dac-ripple-with-analog-subtraction/>
- Fast PWM DAC has no ripple < https://www.edn.com/fast-pwm-dac-has-no-ripple/>
- Phased-array PWM DAC < https://www.edn.com/phased-array-pwm-dac/>
- <u>Fast-settling synchronous-PWM-DAC filter has almost no ripple <</u>
   <u>https://www.edn.com/fast-settling-synchronous-pwm-dac-filter-has-almost-no-ripple/></u>
- Cooling down with fan-speed control < https://www.edn.com/cooling-down-with-fan-speed-control/>
- Motor control: The variable-speed revolution 
   https://www.edn.com/motor-control-the-variable-speed-revolution/>

## 11 COMMENTS ON "PRECISION DC MOTOR SPEED CONTROL VIA POT OR DAC"



#### Konstantin Kim

October 6, 2022

Why C3 and C4 have not same value in this case?

I guess that design may be simplified a bit by low side driving with PNP (or Sziklai ) and ungrounded motor.

BTW: Your ripple cancelation idea is genius.

**→** Log in to Reply



#### Konstantin Kim

October 6, 2022

I'm a bit confused by the dead zone at the top of the input voltage. TLV271 Common-mode input voltage is 0 .. VDD −1.35

#### **Log in to Reply**



#### **WStephenWoodward**

October 6, 2022

Oops! I think I misread the TLV271 datasheet as being rail-to-rail output AND input. I was looking for a cost-effective alternative to the OPA2156 dual. Thanks for the correction.

#### **Log in to Reply**



#### **WStephenWoodward**

October 6, 2022

The TLV2371 is RRIO and therefore should work. Sorry for the typo and losing that "3" in the part number.

Thanks again for the correction.

#### **Log in to Reply**



#### **WStephenWoodward**

October 6, 2022

C3 and C4 have different values to achieve the same ripple-subtraction timeconstant. R10C3 = R6C4.

= 1ms.

And thanks!

#### **Log in to Reply**



#### **Konstantin Kim**

October 7, 2022

I would love to try low side sensing with LM358 for 24V supply.

#### **→** Log in to Reply



#### **WStephenWoodward**

October 7, 2022

If we're going with classics, why not stick with high-side sensing and pop in an LM301?

#### **Log in to Reply**



#### WStephenWoodward

October 7, 2022

...or for something not (quite) as archaic, how 'bout a TL071?



#### **Konstantin Kim**

October 7, 2022

Let say LM301 is a bit too classic. 1967!!! ;). LM358(321) is actually just good cheap working horse.

LM301 will limit minimum supply by 10V. It may require output pulldown because LM301 not RRO.

Price of high-side sensing also is: input AC coupling + floating Vss of 4053, which is a bit annoying;). I would prefer to ground 4053 instead of motor.

On the other hand, choosing something from the modern, I would pay attention to those with less offset. like TLV9151 instead of TLV2371 or TLV07 instead of 358.

Vos of TLV2371 (or 358) is 5mV which is 10% of input control range. But this is just little polishing details...

This is not a call to action. just reflections. Idea is clear and amusing. Thanks 🙂

BTW: decoupling caps for U2 and A1 are expected.



#### Konstantin Kim

October 7, 2022

+ low side sensing may not require 4053. Most of the time, a PWM source can provide you with an inverted signal for free.



#### **WStephenWoodward**

October 7, 2022

Many aspects of this DI would become easier if the requirement of using only a single, variable PS and making motor drive proportional to it, were abandoned.

Sidebar: I'm pretty sure the high-side-sensing LM301 is rated for 30V, as is its internally compensated kissing cousin, the 307. Ditto for the TL071. True that none is RRO, but then neither is the LM358 — for sinking current greater than a few 10s of uA.

But thanks for a stimulating discussion!

**Log in to Reply** 

#### **LEAVE A REPLY**

You must <u>Sign in < https://login.aspencore.com/d045a02d-767d-4f3f-80d7-</u>695dce25d142/login/authorize?

response\_type=code&scope=profile%20email%20openid&client\_id=cf5f876c-2b69-4672-892d-1a3f9cd881ea&state=72689b7bfaf982552d6cbe8c52461a79&redirect\_uri=https%3A%2F%2Fwww.connect-authorize> or Register < https://login.aspencore.com/d045a02d-767d-4f3f-80d7-695dce25d142/login/authorize?

response\_type=code&scope=profile%20email%20openid&client\_id=cf5f876c-2b69-4672-892d-1a3f9cd881ea&state=0cca4de6213738d1f9815aab2ef25dff&redirect\_uri=https%3A%2F%2Fwww.econnect-authorize> to post a comment.

### <u>PREVIOUS POST < HTTPS://WWW.EDN.COM/ON-BOARD-CHARGER-DESIGNS-CALL-FOR-PURPOSE-BUILT-MCUS/></u>

NEXT POST < HTTPS://WWW.EDN.COM/LTE-MODULES-SPEED-CELLULAR-IOT-PRODUCT-DEVELOPMENT/>

#### **PARTNER CONTENT**



SK hynix Obtains AAA Certification for Export Control Compliance

<u><</u>

https://www.eetimes.com/sk-

hynix-

obtains-

<u>aaa-</u>

certification-

for-

export-

control-

compliance/>

07.25.2023



How to Minimize the Risk of Counterfeit Components

<u><</u>

https://www.eetimes.com/how-

<u>to-</u>

minimize-

the-risk-

of-

counterfeit-

components/>

07.24.2023



It's Just a Jump to the Left, Right? Shift Left in IC Design Enablement

\_

https://www.eetimes.com/its-

just-a-

<u>jump-to-</u>

the-left- right- shift-left- in-ic- design- enablement/> 07.18.2023	PIVI		Precision DC motor speed control via pot of DAC - E	אוט
		right- shift-left- in-ic- design- enablement/> 07.18.2023	Precision De motor speed control via pot or DAC - E	

Advertisement

#### Advertisement

#### **Recent Posts**

A short primer on optoelectronic transistors, their apps < https://www.edn.com/a-short-primer-on-optoelectronic-transistors-their-apps/> / Cadence develops next-gen Xtensa LX processor < https://www.edn.com/cadence-develops-next-gen-xtensa-lx-processor/> / LTE Cat 4 module runs under Linux OS < https://www.edn.com/lte-cat-4-module-runs-under-linux-os/> / Near-field flash LiDAR caters to diverse applications < https://www.edn.com/near-field-flash-lidar-caters-to-diverse-applications/> / JEDEC crafts base standard for CXL memory modules < https://www.edn.com/jedec-crafts-base-standard-for-cxl-memory-modules/>

# Archives Select Month Categories Select Category

#### **PODCAST**

#### **()** 15:38

## **Special Report: Enabling t Factory of 2030**

Join us as we discuss the exciting developments this report covers ar

VIEW ALL EPISODES

**LISTEN** →

Search ...
SEARCH

#### **Recent Comments**

Brian park On <u>Dissecting a battery-backed LED light bulb < https://www.edn.com/dissecting-a-battery-backed-led-light-bulb/#comment-30580></u>

bdcst ON <u>Adventures with a remote heart monitor < https://www.edn.com/adventures-with-a-remote-heart-monitor/#comment-30579></u>

Bob Baylor on Heathkit transceiver recalled after meeting specs!  $\leq$ 

https://www.edn.com/heathkit-transceiver-recalled-after-meeting-specs/#comment-30578>

bdcst On <u>Dissecting a battery-backed LED light bulb < https://www.edn.com/dissecting-a-battery-backed-led-light-bulb/#comment-30577></u>

wd73383 ON <u>Heathkit transceiver recalled after meeting specs! < https://www.edn.com/heathkit-transceiver-recalled-after-meeting-specs/#comment-30576></u>

Advertisement