Overview

- PMOS
  - conducting if A connected to GND

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- NMOS
  - conducting if connected to Vdd

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- similar to switches

  PMOS: A=0 → switch is open;   A=1 → switch is closed

  NMOS: A=0 → switch is closed;   A=1 → switch open

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- combine multiple transistors for computational logic and storage
  - [[Logic Gates]]