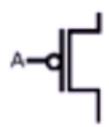
Overview

- PMOS
 - conducting if A connected to GND



- NMOS
 - conducting if connected to Vdd



• similar to switches

PMOS: $A=0 \rightarrow$ switch is open; $A=1 \rightarrow$ switch is closed

NMOS: $A=0 \rightarrow$ switch is closed; $A=1 \rightarrow$ switch open

- combine multiple transistors for computational logic and storage
 - [[Logic Gates]]