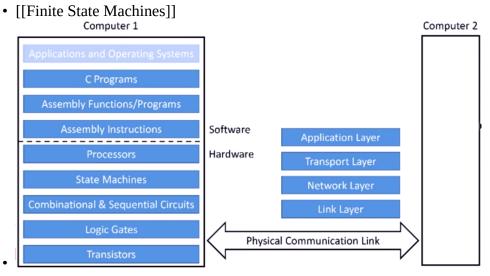
## **Abstraction Levels**

- [[Transistors]]
- [[Logic Gates]]
- [[Combinational Circuits]]
- [[Sequential Circuits and Registers]]



- [[Networking]]
- [[Processor Programming]]