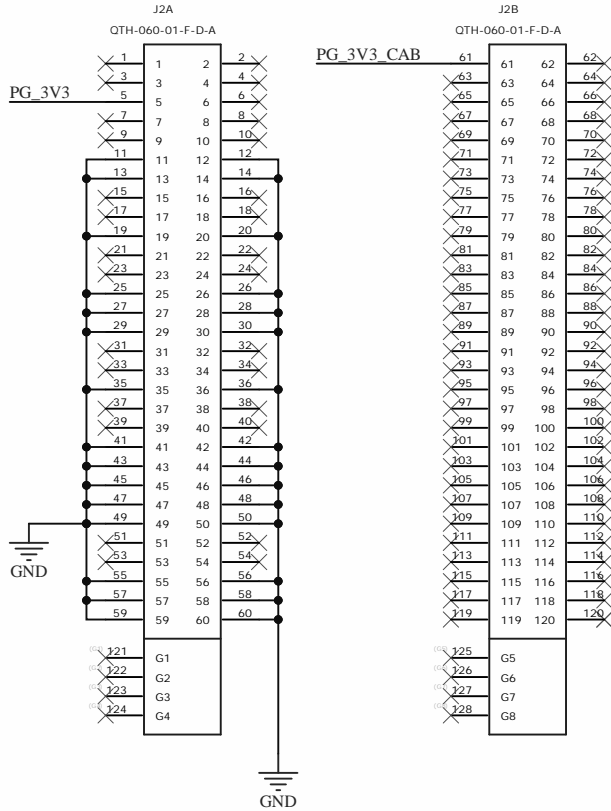


CONNECTORS 1

BANK13 on CI-CORE1 must be connected to 3.3V
R133 = mounted ; R134 = unmounted

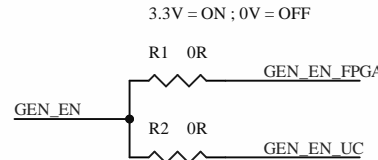
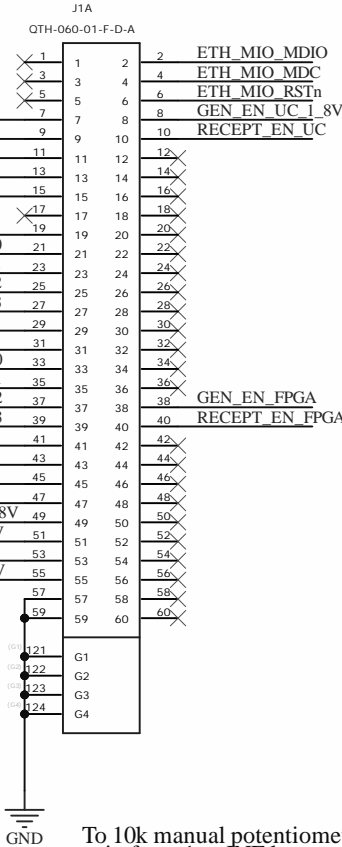


I2C0 SCL (MIO10)
I2C0 SDA (MIO11)
I2C1 SCL (MIO12)
I2C1 SDA (MIO13)

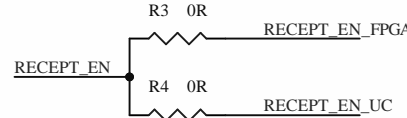
GEN_I2C_POT_SCL
GEN_I2C_POT_SDA
GEN_SCLK
GEN_I2C_POT_AD0

ETH_MIO_TX_CLK
ETH_MIO_TX_DATA0
ETH_MIO_TX_DATA1
ETH_MIO_TX_DATA2
ETH_MIO_TX_CTL
ETH_MIO_RX_CLK
ETH_MIO_RX_DATA0
ETH_MIO_RX_DATA1
ETH_MIO_RX_DATA2
ETH_MIO_RX_DATA3
ETH_MIO_RX_CTL
ETH_MIO_PHYLED2
GEN_FSYNC_1_8V
GEN_CTRL_1_8V
GEN_INTERRUPT_1_8V
GEN_STANDBY_1_8V
GEN_MSBOU1_1_8V
GEN_SYNCOUT_1_8V

Core1 mezzanine connectors

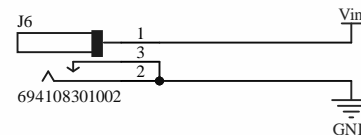


1.8V or 3.3V = RECEPTION ON
0V = RECEPTION SHORT CIRCUITED

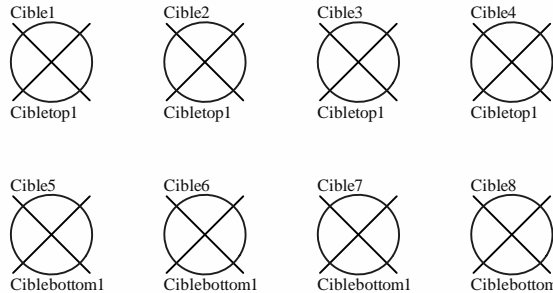
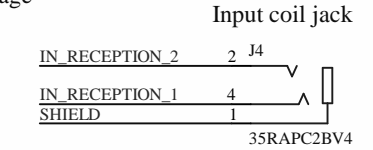
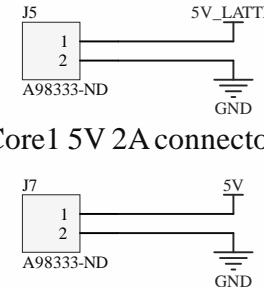


LattePanda 5V 2A connector

Vin = 24V Barrel connector 2.5x5.5
2 A

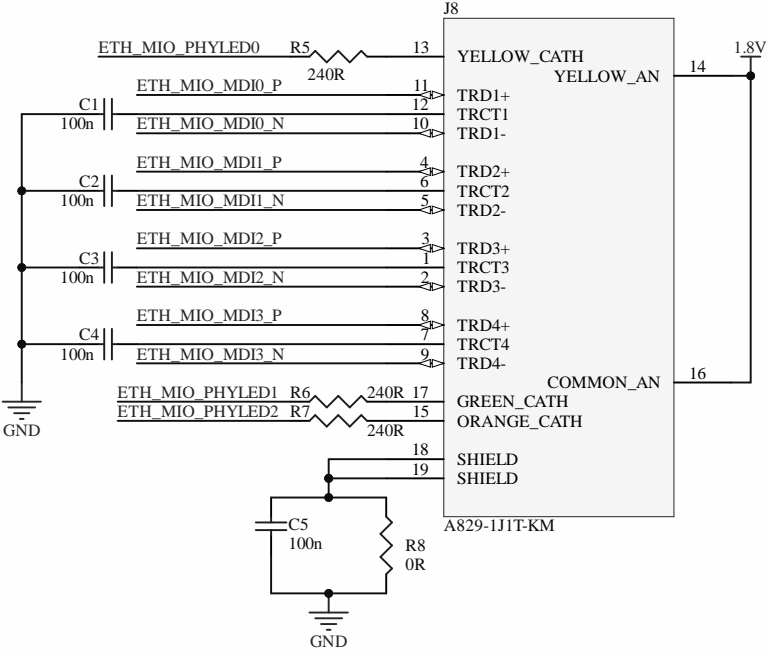


Core1 5V 2A connector



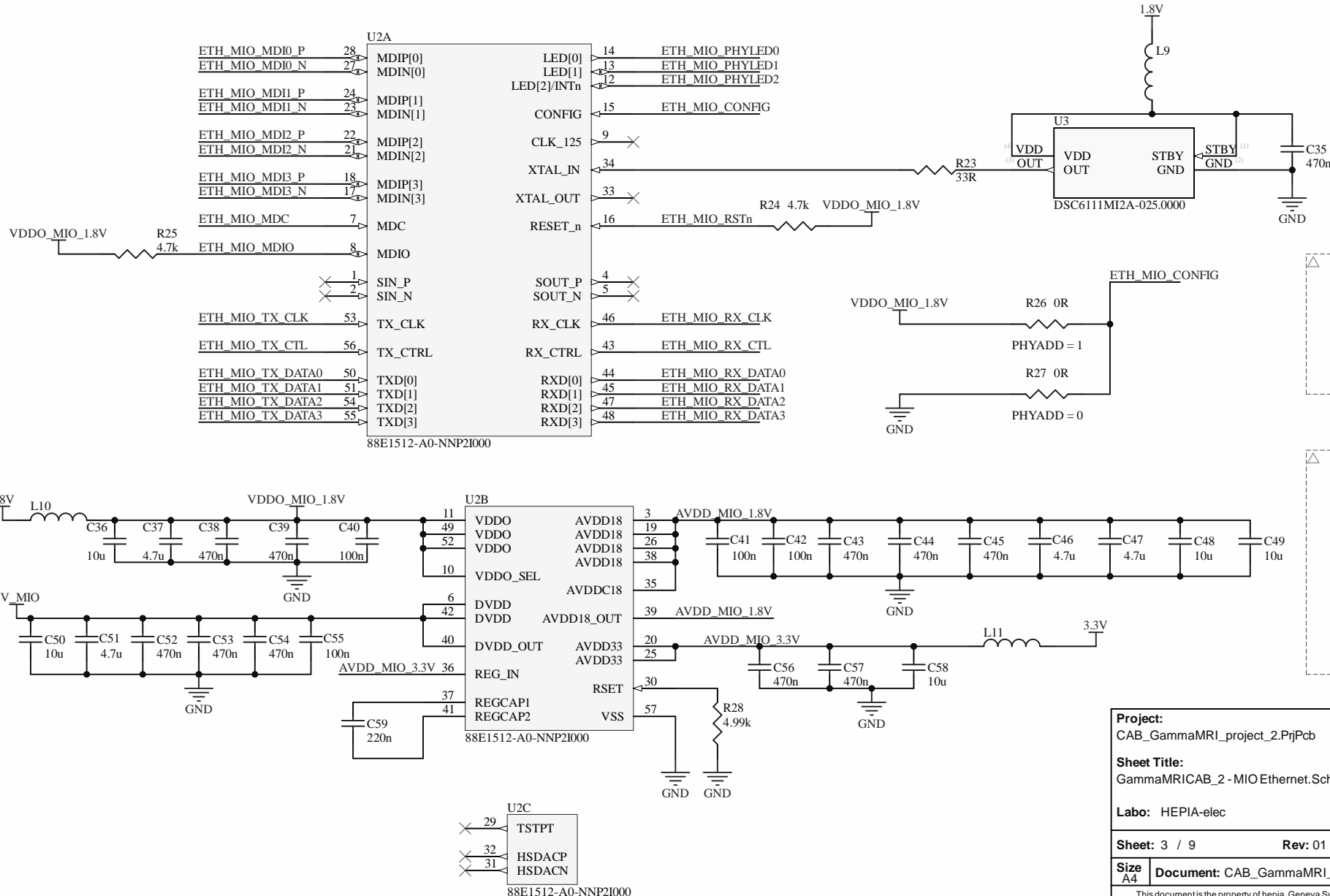
Project: CAB_GammaMRI_project_2.PrjPcb		h e p i a Haute école du paysage, d'ingénierie et d'architecture de Genève		
Sheet Title: GammaMRICAB_2 - Connectors2.SchDoc		Designer: Stoeckli H. Resp: Giandomenico N.		
Labo: HEPIA-elec		Sheet: 1 / 9 Rev: 01 Date: 03.01.2023		
Size A4	Document: CAB_GammaMRI_project_2.PrjPcb			
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Ethernet connector MIO



Project: CAB_GammaMRI_project_2.PrjPcb		h e p i a Haute école du paysage, d'ingénierie et d'architecture de Genève	
Sheet Title: GammaMRICAB_2 - Ethernet Conn.SchDoc			
Labo: HEPIA-elec		Designer: Stoeckli H. Resp: Giandomenico N.	
Sheet: 2 / 9		Rev: 01	Date: 03.01.2023
Size A4	Document: CAB_GammaMRI_project_2.PrjPcb		
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MIO Ethernet (1.8V, default PHYAD = 0)



PCB Rule

ETH_MIO_MDIO_P
ETH_MIO_MDIO_N
ETH_MIO_MDII_P
ETH_MIO_MDII_N
ETH_MIO_MDI2_P
ETH_MIO_MDI2_N
ETH_MIO_MDI3_P
ETH_MIO_MDI3_N

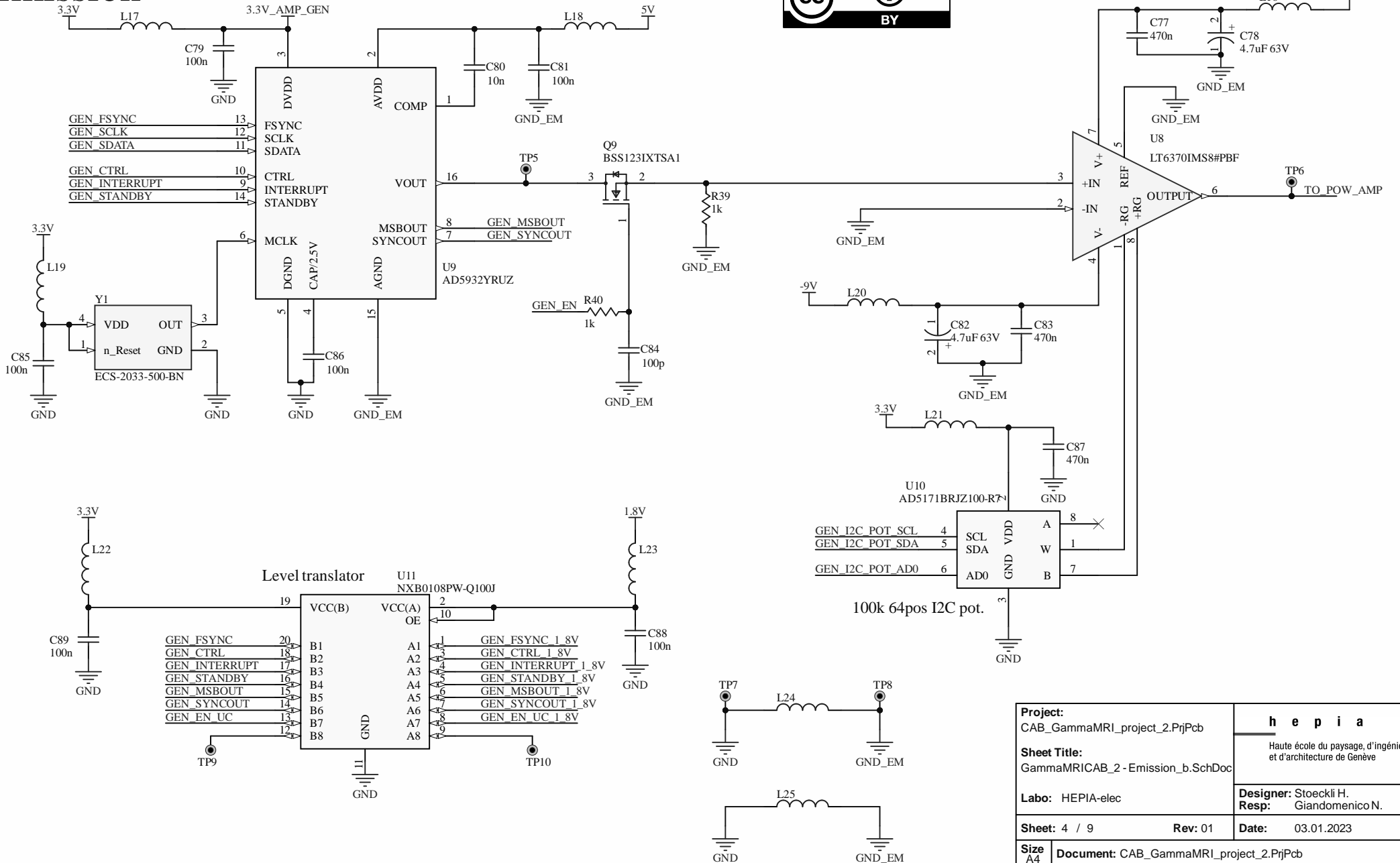
PCB Rule

ETH_MIO_RX_CLK
ETH_MIO_RX_CTL
ETH_MIO_RX_DATA0
ETH_MIO_RX_DATA1
ETH_MIO_RX_DATA2
ETH_MIO_RX_DATA3
ETH_MIO_TX_CLK
ETH_MIO_TX_CTL
ETH_MIO_TX_DATA0
ETH_MIO_TX_DATA1
ETH_MIO_TX_DATA2
ETH_MIO_TX_DATA3
ETH_MIO_MDC
ETH_MIO_MDIO

Project: CAB_GammaMRI_project_2.PrjPcb		h e p i a Haute école du paysage, d'ingénierie et d'architecture de Genève	
Sheet Title: GammaMRICAB_2 - MIO Ethernet.SchDoc		Designer: Stoeckli H. Resp: Giandomenico N.	
Labo: HEPIA-elec		Date: 03.01.2023	
Sheet: 3 / 9		Rev: 01	
Size A4	Document: CAB_GammaMRI_project_2.PrjPcb		
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Emission

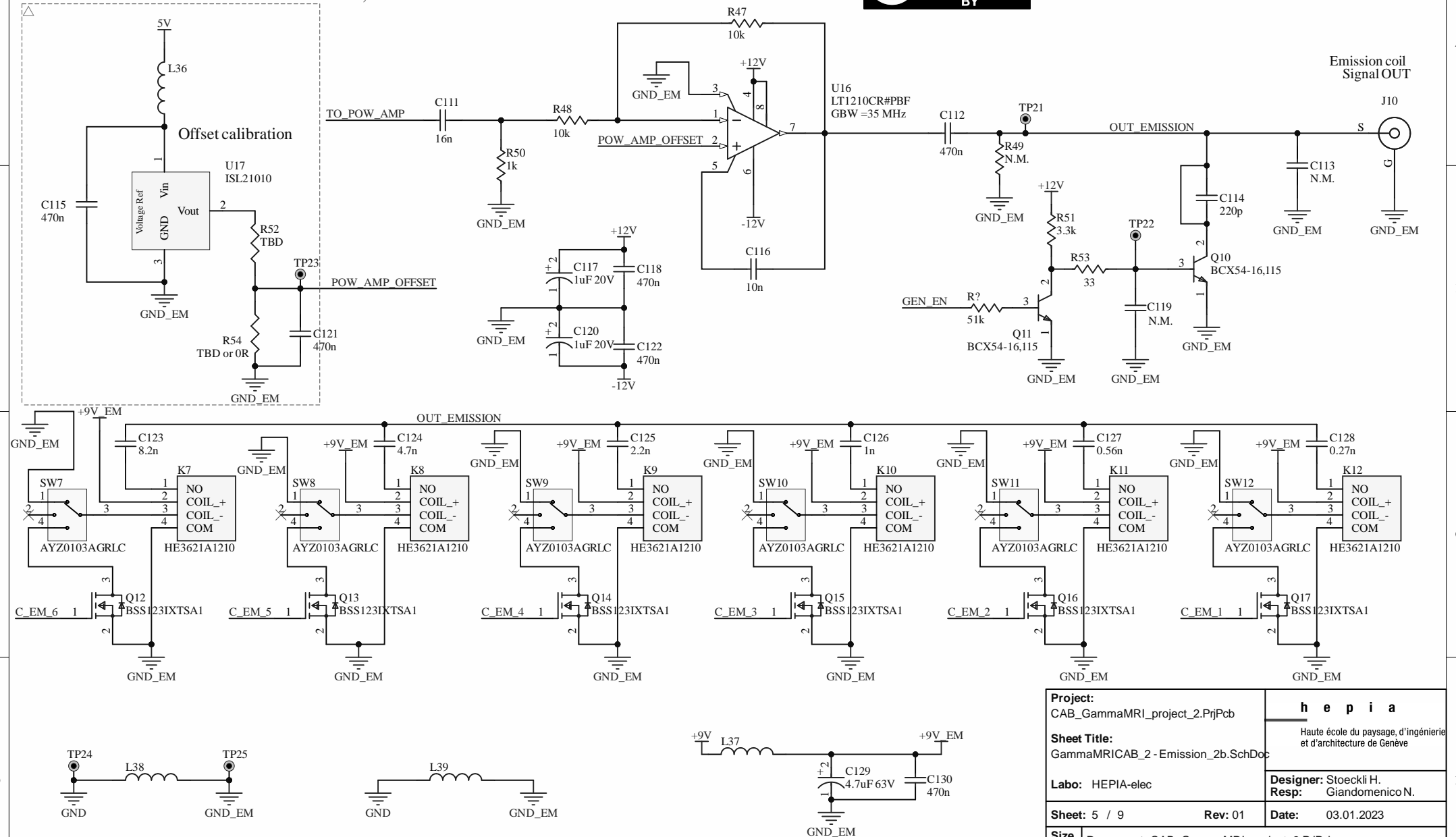
Programmable frequency scan waveform generator 25MHz



Project: CAB_GammaMRI_project_2.PrjPcb		h e p i a Haute école du paysage, d'ingénierie et d'architecture de Genève	
Sheet Title: GammaMRICAB_2 - Emission_b.SchDoc		Designer: Stoeckli H. Resp: Giandomenico N.	
Labo: HEPIA-elec		Date: 03.01.2023	
Sheet: 4 / 9		Rev: 01	
Size A4	Document: CAB_GammaMRI_project_2.PrjPcb		
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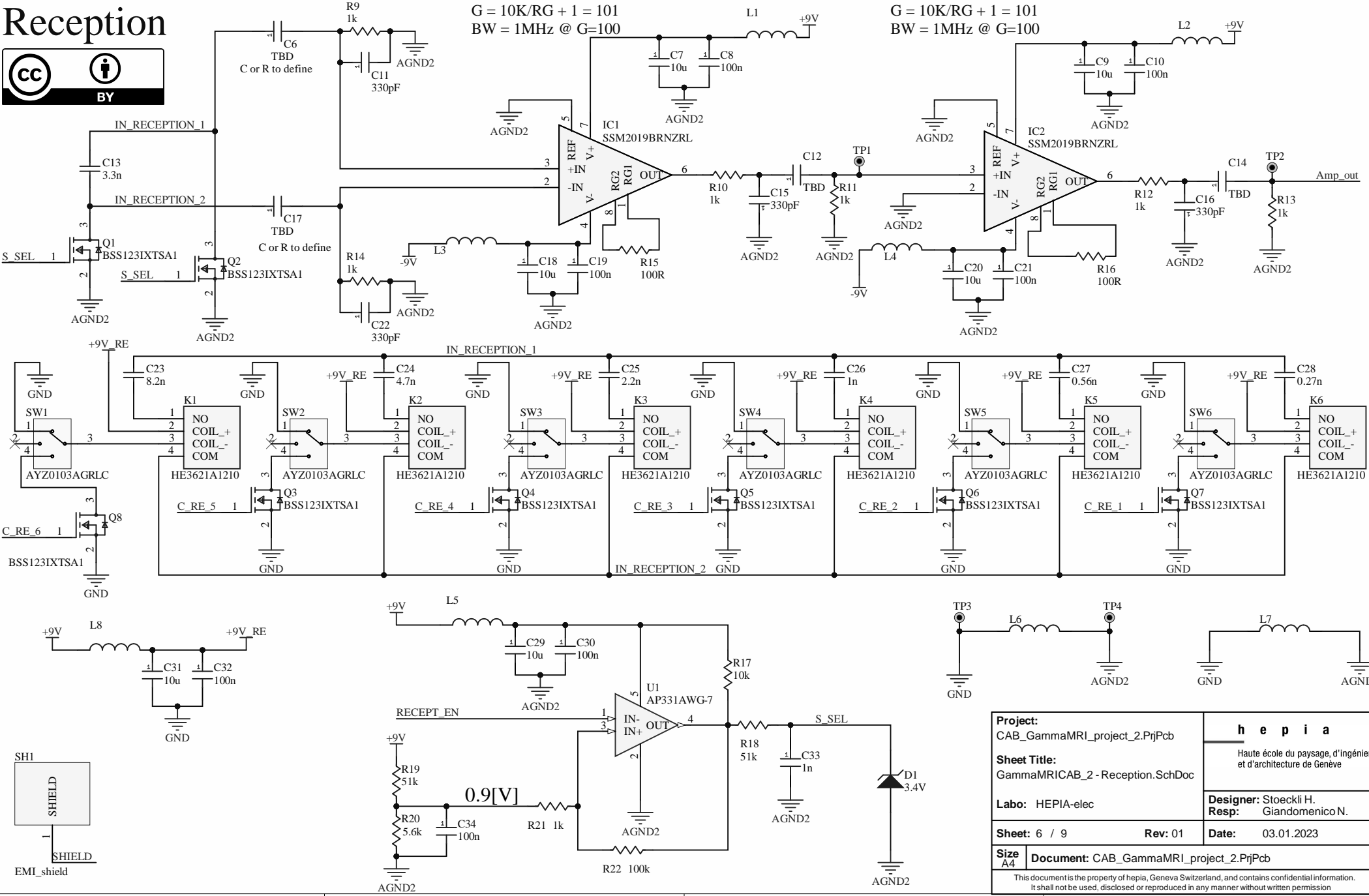
Emission 2

DO NOT MOUNT, EXCEPT R54 = 0R



Project: CAB_GammaMRI_project_2.PrjPcb		h e p i a Haute école du paysage, d'ingénierie et d'architecture de Genève		
Sheet Title: GammaMRICAB_2 - Emission_2b.SchDoc		Designer: Stoeckli H. Resp: Giandomenico N.		
Labo: HEPIA-elec		Sheet: 5 / 9 Rev: 01 Date: 03.01.2023		
Size A4	Document: CAB_GammaMRI_project_2.PrjPcb			
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Reception

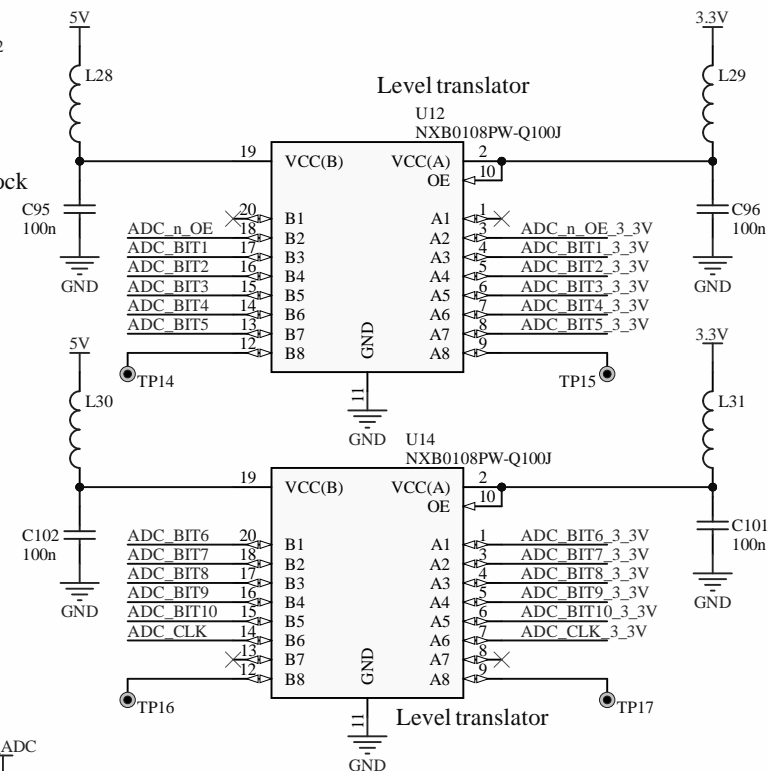
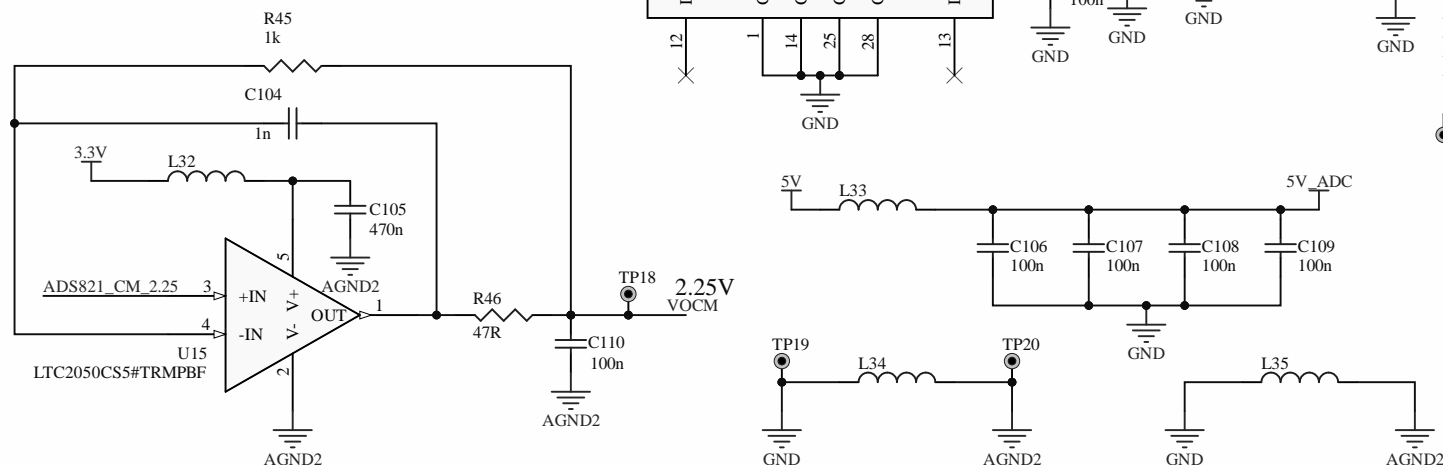
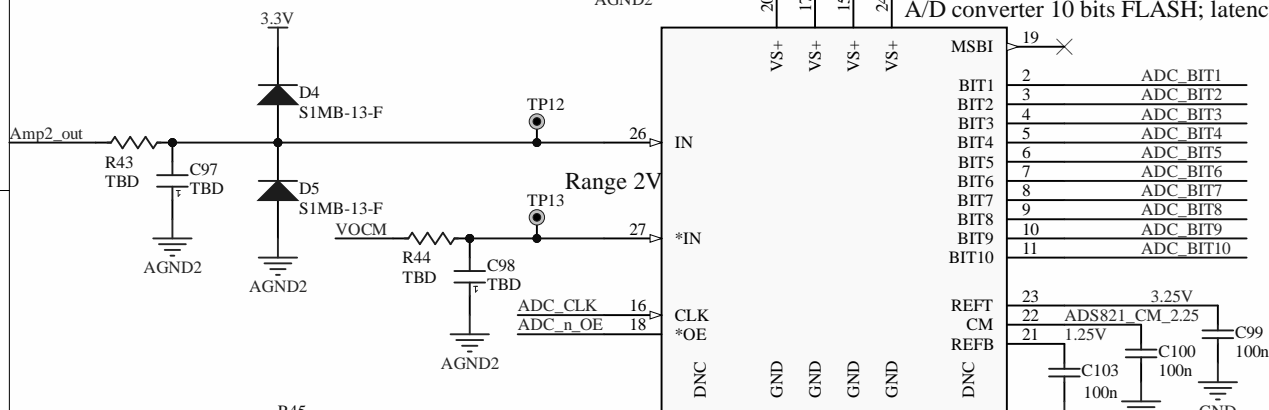
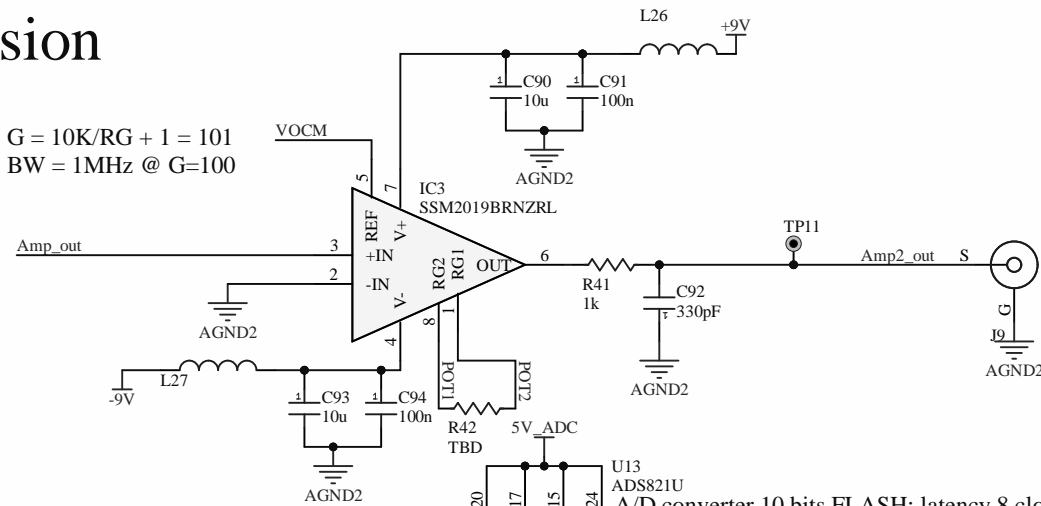


Project: CAB_GammaMRI_project_2.PrjPcb		h e p i a Haute école du paysage, d'ingénierie et d'architecture de Genève		
Sheet Title: GammaMRICAB_2 - Reception.SchDoc		Designer: Stoeckli H. Resp: Giandomenico N.		
Labo: HEPIA-elec		Sheet: 6 / 9 Rev: 01 Date: 03.01.2023		
Size A4	Document: CAB_GammaMRI_project_2.PrjPcb			
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ADconversion



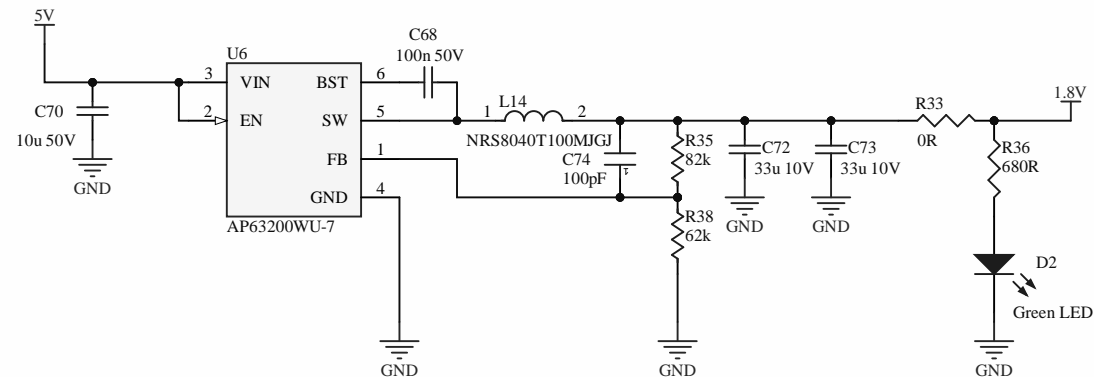
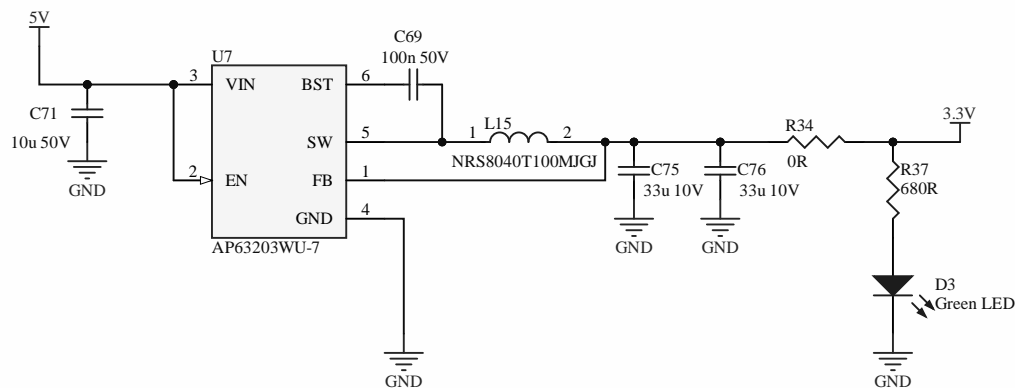
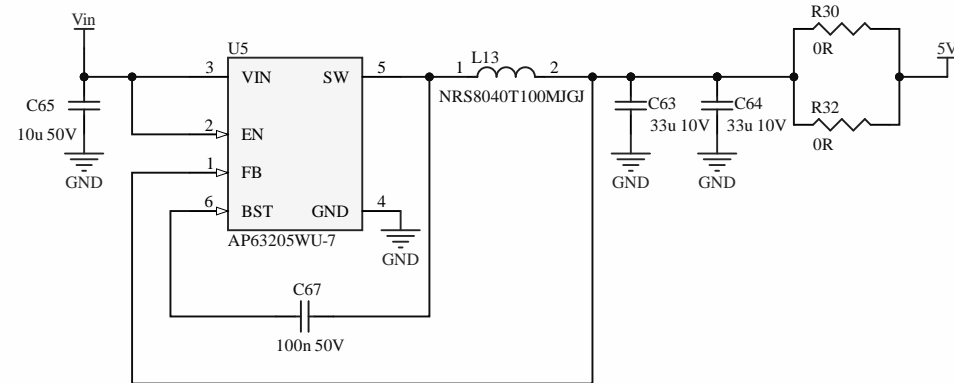
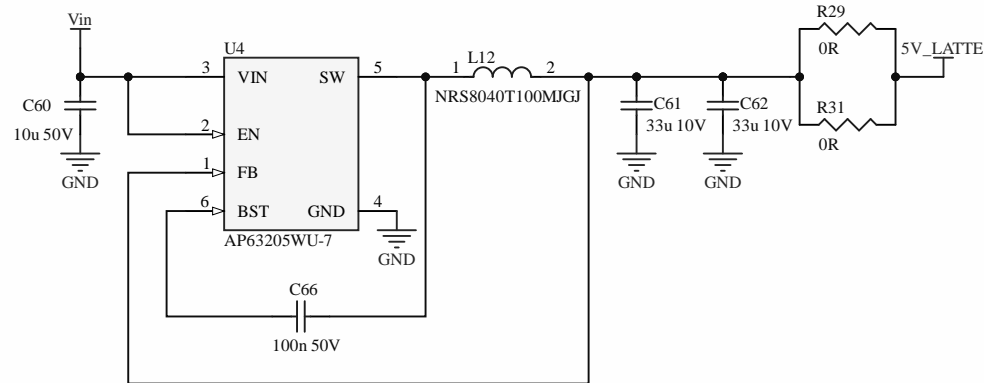
$G = 10K/RG + 1 = 101$
 $BW = 1MHz @ G=100$



Project: CAB_GammaMRI_project_2.PrjPcb		h e p i a Haute école du paysage, d'ingénierie et d'architecture de Genève	
Sheet Title: GammaMRICAB_2 - ADconversion.SchDoc		Designer: Stoeckli H. Resp: Giandomenico N.	
Labo: HEPIA-elec		Date: 03.01.2023	
Sheet: 7 / 9		Rev: 01	
Size A4	Document: CAB_GammaMRI_project_2.PrjPcb		
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Power management

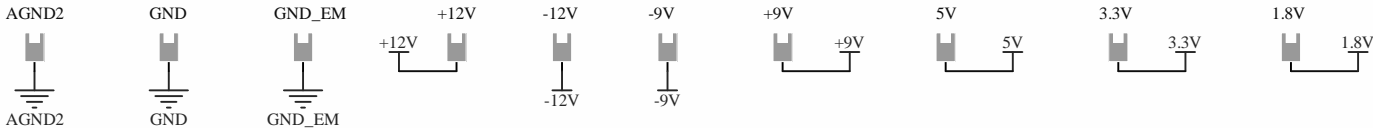
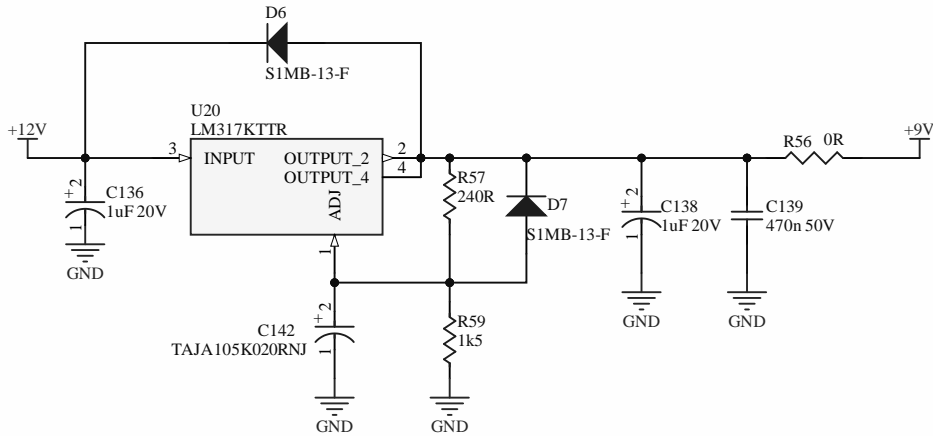
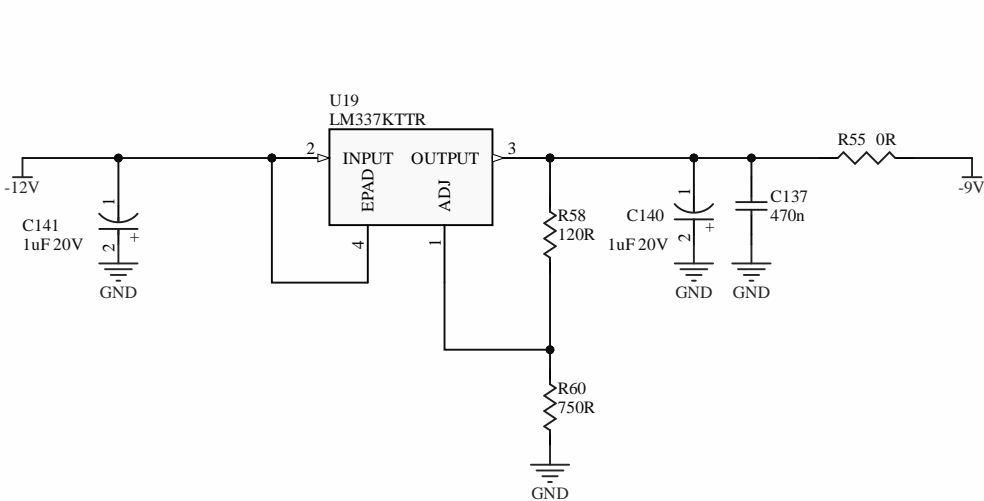
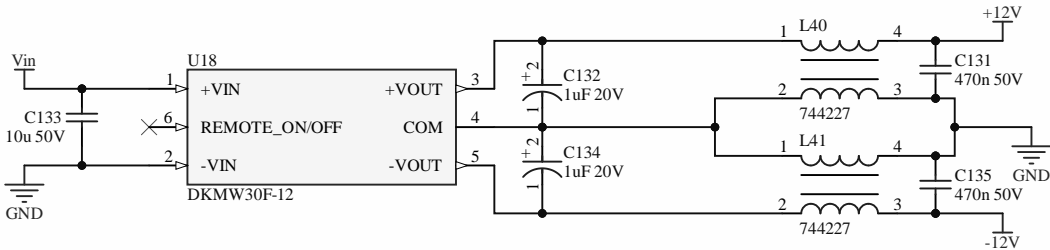
Vin = 24 [V]



Project: CAB_GammaMRI_project_2.PrjPcb		h e p i a Haute école du paysage, d'ingénierie et d'architecture de Genève	
Sheet Title: GammaMRICAB_2 - Power Management		SchDoc	
Labo: HEPIA-elec		Designer: Stoeckli H. Resp: Giandomenico N.	
Sheet: 8 / 9		Rev: 01	Date: 03.01.2023
Size A4	Document: CAB_GammaMRI_project_2.PrjPcb		
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Power management 2

Vin = 24 [V]



Project: CAB_GammaMRI_project_2.PrjPcb		h e p i a Haute école du paysage, d'ingénierie et d'architecture de Genève		
Sheet Title: GammaMRICAB_2 - Power Management2.SchDoc		Designer: Stoeckli H. Resp: Giandomenico N.		
Labo: HEPIA-elec		Sheet: 9 / 9 Rev: 01 Date: 03.01.2023		
Size A4	Document: CAB_GammaMRI_project_2.PrjPcb			
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