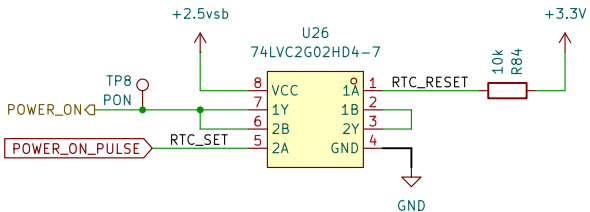
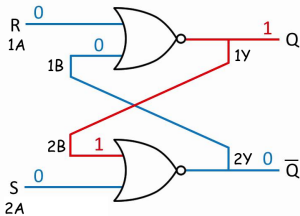


Power on latch circuit
To keep the power signal low until the 3v3 comes up



SR Latch

S	R	Q	Q̄
0	0	1	0
0	1	0	1
1	0	1	0
1	1	0	0



(Work in progress!)
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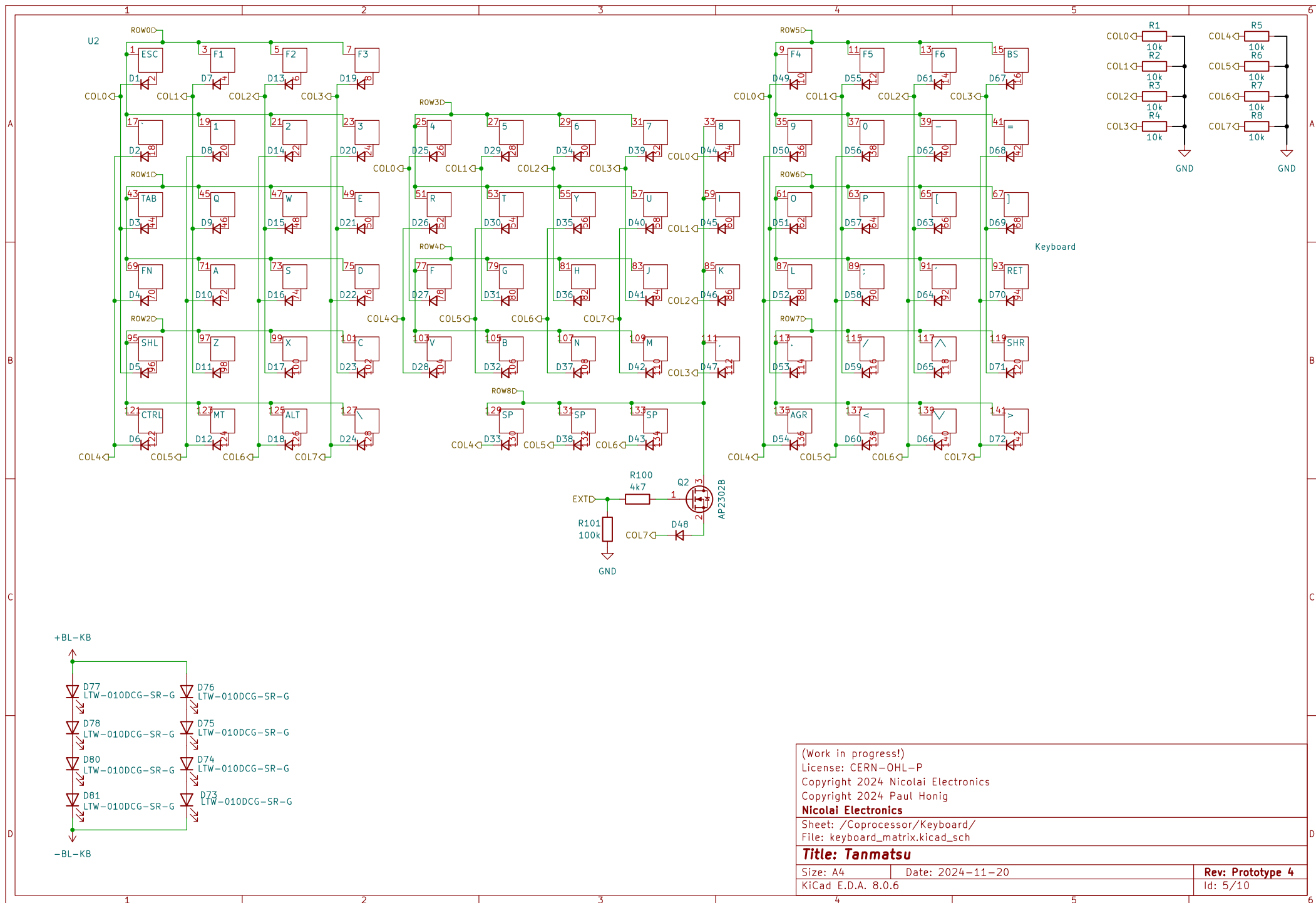
Nicolai Electronics
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Title: Tanmatsu

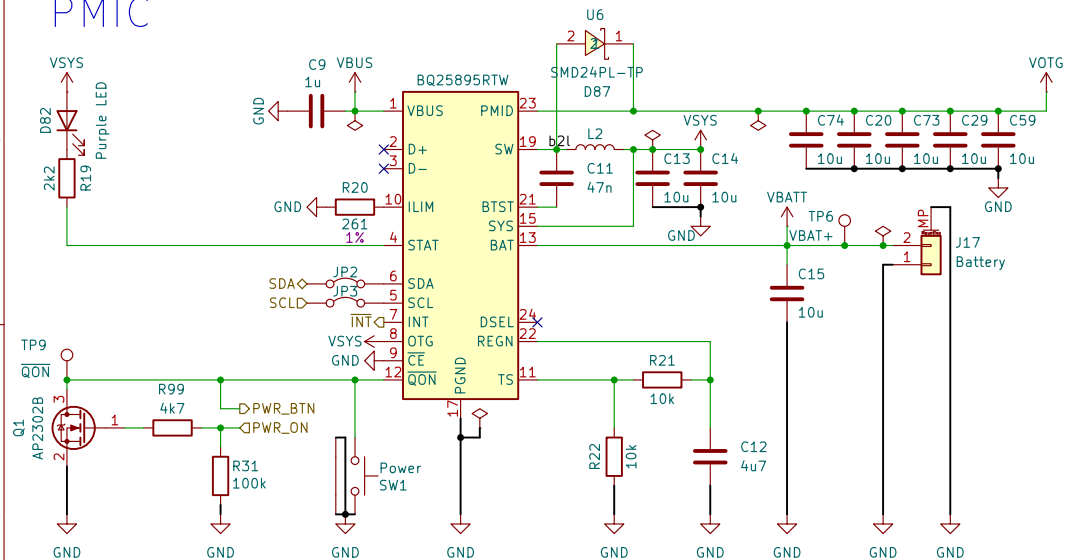
Size: A4
KiCad E.D.A. 8.0.6

Date: 2024-11-20

Rev: Prototype 4
Id: 4/10



PMIC



Watch out with the I2C bus of this device, wrong configuration can be dangerous.

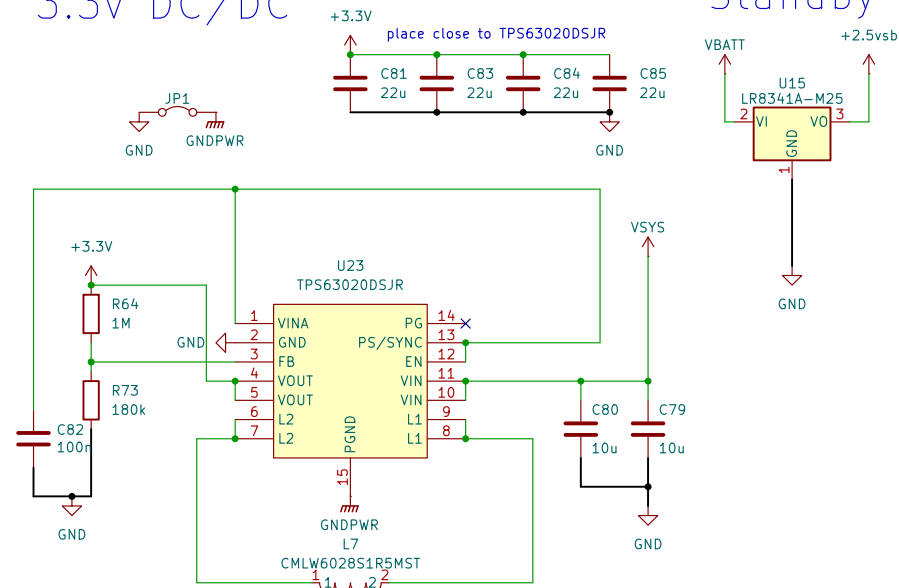
VBUS is the voltage coming from the input USB-C port. This voltage can be 5 up to 14 volts.

VOTG is 5V generated from VBAT or VBUS depending on the power source of the system.

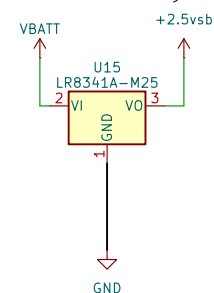
VBATT is connected to the rechargeable battery.

VSYS is an unregulated 3.5 to 4.5V output used to power the DC-DC converters for the rest of the components.

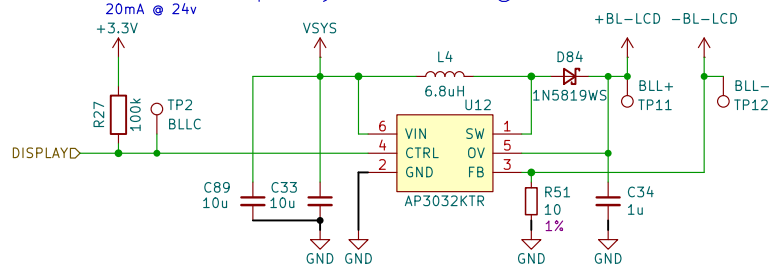
3.3v DC/DC



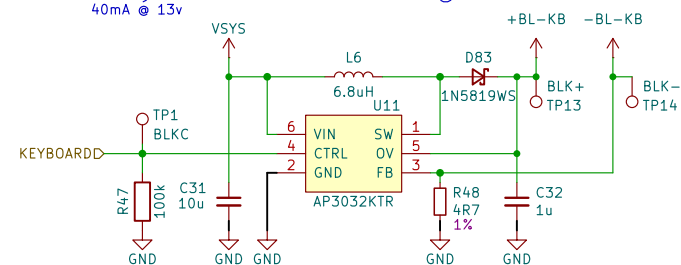
Standby



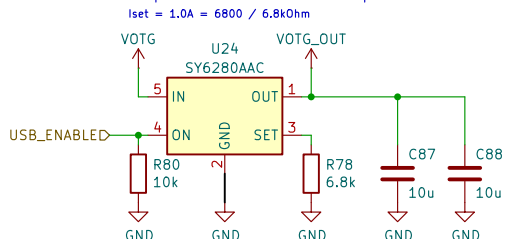
Display backlight DC/DC



Keyboard backlight DC/DC



Switched power for USB-A port



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File: power.kicad_sch

Title: Tanmatsu

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Rev: Prototype 4

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