

# Meeting summary: 21/09/18

## Present during meeting

- Lajos Török (Supervisor)
- Dezso Sera (Supervisor)
- Nicolás Murguizur
- Jesper Kloster
- Nicolai Haugaard
- Estefanía Ruiz
- Thassilo Lang
- Aitor Terán

## Agenda

- Project boundaries and limitations, what the real application and the initial conditions of the system are as well as the electrical integration between MICs.
- PV modules documentation, datasheets and number of modules to be implemented.
- Semiconductor technology to be used (GaN & Si).
- Controlling techniques, global & local.
- P0 timeline and structure discussion.
- Business perspectives.

## Meeting discussion

- Boundaries and limitations: The project consists on the creation of one MIC to be connected to a general PV. The load will be capacitive during the simulations.
- The objective is the control of the input power to the MIC while the output voltage and current will be defined by the load.
- The design of the converter will be done using a generic PV module of 250-300W. Tests will be carried out using an emulator.
- There is no need of a global control since only one panel will be used. Simulation of local controller will be done with the PLECS software, it is also possible to include simulations of MIC arrays.
- The motivation for using a bidirectional converter is the implementation of PV diagnostics since reverse current flow will produce photon emission.
- The semiconductor technology will be Si and it will not be necessary to make any comparison with GaN, although it is a possibility to include it in the project if there is enough time.
- Business perspectives are not necessary.

## Future tasks

- The group will be working on the GANTT for the full project to be sent to the supervisors as soon as possible for feedback.
- P0 writing is now a priority and the evaluation and selection of converter topologies should be included.
- The P0 will be finished one week ahead of the deadline to be sent to the supervisors for feedback, this way there will be enough time for iterations.
- After the P0 is finished, the first step is the designing of the converter to have enough time to order the components.