exam

EPSH1/PED1/WPS1
2011
November 2011/CLB/SMN

Department of

ENERGY TECHNOLOGY

Aalborg University

Written examination in

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High Voltage Engineering and Design of Switch Mode Converters

Tuesday 17th January 2011

09.00 – 13.00 (4 hours)

Please provide sufficient text description and reference to textbook and equations so your method of solution is clear and easy to follow. Statements and results will only give credit if explained thoroughly.

Both exercises in HV and SMC individually have to be passed in order to pass the course. This means that at least 50 % of both the HV exercise and the SMC exercise have to be correctly answered.

The HV exercise and the SMC exercise have the same weight.

Exercise 1 (High Voltage)

Students' pursuing the Masters at Department of Energy Technology at AAU wants to perform testing of power system equipment regarding lightning overvoltage.

For this purpose a group of students wants to design and construct a setup in the HV laboratory in order to test specific equipment. During this the following questions arises, which are to be answered at this examination.

A single stage impulse generator, type b with a maximum output peak impulse voltage V_{max} = 300 kV should be designed. Already chosen components are: Discharge capacitance C_1 = 10 nF, load capacitance C_2 = 1,2 nF, discharge resistance R_2 = 9500 Ω

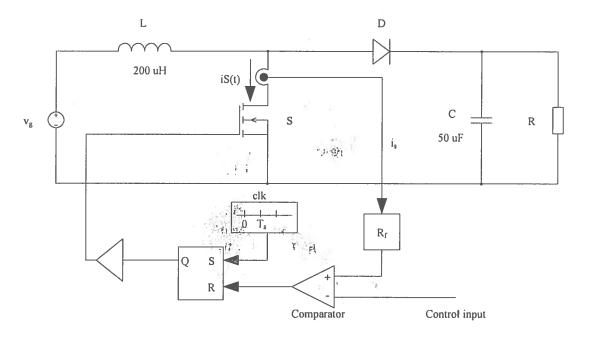
Temperature in the HV laboratory is 18°C and air pressure p = 1022 hPa

- a) Draw a single line diagram of the impulse voltage generator and explain the components and the mode of operation. Why is type b normally preferred?
- b) Calculate the value of the front resistance R_1 for a standard lightning impulse 1,2/50 μs
- c) Draw the impulse voltage generated by the impulse generator for a charging voltage of V_0 = 250 kV using a sufficient number of calculated points of the curve V(t). Determine, on the basis of this curve graphically front time T_1 and time to half T_2 and the efficiency η .
- d) Calculate (by equations) maximum voltage $V_{m \not \ni n}$ and time to reach maximum voltage t_{max} and efficiency η and compare to results from c)
- e) The generator is tested with negative polarity in the HV lab and the peak voltage is sought measured with a 25 cm sphere gap. The gap flashed over for a gap length of 80 mm. What was the peak value of the impulse voltage measured (include accuracy of the sphere gap)?
- f) Now the students want to record the impulse waveform by means of a resistive voltage divider and an oscilloscope. A resistive divider is constructed with the following data: Diameter d = 10 cm, length l = 100 cm standing on a 10 cm high foot on the floor. Resistance is R = 50 k Ω . In order to assess the transfer characteristics of the divider the students assumes that the divider can be seen as a first-order system with a truly exponential rise. On the basis of this assumption, please calculate the response time T of this divider.
- g) Finally the students are ready for testing in the HV lab \odot By means of the oscilloscope they measure a peak voltage of 218 kV for a front chopped lightning impulse with chopping time $T_c = 0.3~\mu s$. What was the actual peak voltage at the test object?

GOOD LUCK ©

Exercise 2 (Switch mode)

Current Programmed Control of Boost converter, operation steady-state.

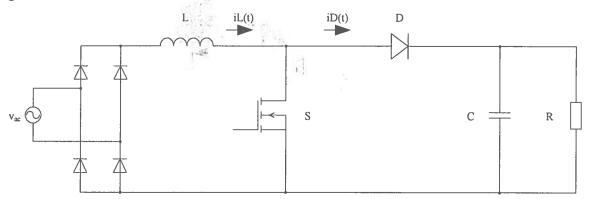


$$T_s = 10~\mu s \hspace{0.5cm} R = 20~\Omega \hspace{0.5cm} V_g = 20~V \hspace{0.5cm} V = 100~V \hspace{0.5cm} R_f = 1~\Omega$$

Assume ideal components (=> no power loss in power conversion)

- a) Calculate the average current of the switch S.
- b) Add a stabilizing ramp and calculate the minimum slope of the artificial ramp m_a that will give stability.

Design a boost converter,



Specification:

Output voltage	390 V
Output power	500 W
Rms input voltage	230 V
Efficiency	0.95
Fundamental frequency	50 Hz
Switching frequency	100 kHz

In question a, b assume converter operate in CCM and switching frequency ripple current in L is very small – so small you may ignore it.

- a) For one fundamental period sketch the current of iL(t) label the axis.
- b) For one switching period sketch the current of iD(t) assume the average current is 1 A and the duty-cycle of S is 25 %. Label the axis
- c) Calculate the value of the emulated resistance R_e.
- d) For what values of L is the converter operating in DCM?
- e) Sketch the low frequency waveform of the ripple voltage found in the output voltage v. The ripple voltage waveform for one fundamental period of the line voltage must be shown. Label the axis.

HU 2011 Exam I See fine 2.25 and explanation. Type be has hiber efficiences ? b) eg (2.38) gisha 21 = 378,3 2 c/ (2.26) 28(t) = $\frac{V_0}{k} \frac{1}{(o_2 \cdot o_1)} \left[e^{-c \cdot t} - e^{o_2 \cdot t} \right]$ K= 378. 12.10-5 = 454-10-3 - 4,074.15° 7 = 250. 454.10-9 - 44.107 = 224.5 W 7(4) = 224.5 (2 -14663.t -2469/36.t) Vmax = 216 W 301. -65 VV => 1.38.10-7 20% /95hV => 8.45.10-2 50 Tr = 169. (8,49-1,38) 157 = 1,18 ms T2 = 49,3 µs begg pin graf aflat.

2 = 216 - 0,864

(2.28) - Lanx = 16 (21) tunx = 16 (10.405.10) / (60.2-10) = 2.09 pes 2005-106-160-2-10 Passer mad pret Vance = 224.5 (-14663.2.09.106 -2469136.7.58 Deno = 216, 4 kV 2 = G = 10.10 - 089 tible 9.3: 23 => 3-20=1 206 kV $0 = \frac{?}{?3} \frac{273+6}{273+2} = \frac{1022}{10/3} \frac{238+20}{273+8} = 1,016$ 6=RAD total 3.5 3 25 => 12 = 1,00 (3.1) Vd=Vd Vb = 100-206 = 2066 Vd = 206 ± 6 6 kU

- 144 P

f) (3.33) p. 126 Ce & 27/2 = 27/8/51512 - 1/50 = 228 pt p. 152 frestado out organizat oire => 7 - 7 - R.Ce = 50005 - 228.152 - 150 ms $\frac{9}{100}$ (3.53) $\frac{139}{5.7}$ $\frac{139}{5.7}$ $\frac{7}{5}$ = 150.10 = 0.632, S= 218/03 = 727 kg/us

NV = 6 -5.7c = 3032.727 .03 = 138kg/ So wifeed with " Vnow + A V = 218+ 138 = 356 LeV