State	Date:				cCARD Pinout:	F2807x-120				HSEC Pinout:
	04-Mar-15									2.11
1			HSEC pin	MCU pin			MCU pin			
1			1	TMS			TRSTn			
1					JTAG-TCK	JTAG-TDO	TDO			
1				ADC-A0, DAC			IDI			
To 10 10 10 10 10 10 10 1				ADC-A1, DAC						
Column			15		ADC1 (and/or CMPIN+)	Rsvd		16		
Column				ADC-A3						
10 10 10 10 10 10 10 10		þΩ	21		ADC1 (and/or CMPIN+)				50	
10 10 10 10 10 10 10 10		Ö							Ö	
10 10 10 10 10 10 10 10		و		ADCIN15					a	
10 10 10 10 10 10 10 10		₹	31		ADC	Rsvd		34	Ā	
1				CMPIN6N *			ADC-D4			
1			37	CMPIN5P *	ADC			38		
10 10 10 10 10 10 10 10					Rsv			42		
1				All VREEHIC **				46 48 50		
S. Chi-0.1 Paulis Paulis Chi-0.2 S.			47		GND					
20 GPO-02										
Total			53	GPIO-02	PWM2A	PWM4A	GPIO-06	54		
20 070 020 070 040										
Company										
Control Cont			61	GPIO-10	PWM6A	PWM8A or TZ3	GPIO-14	62		
COP-015 PRISONAL CRITAL PARKET PRIVATE OF COP-12 TO COP-015				GPIO-11			GPIO-15			
Total			67		SPISIMOA	QEP1A (McBSP-MDXA)		68		
Part										
Page			73	GPIO-19	SPISTEA	QEP1I (McBSP-MCLKXA)	GPIO-23	74		
Section Company Company Company Company Section Sect										
B										
					GND	5V0		84		
1										
Part			89	GPIO-60	GPIO	GPIO	GPIO-64	90		
Total										
Total			95		GPIO					
100 GPO 50			99 101 103 105	GPIO-48		QEP2A or GPIO	GPIO-54	102 104 106		
105 GPIO-51 GPIO GPIO-52 GPIO GPIO-57 106 GPIO-52 GPIO GPIO-53 GPIO-										
100 GPIO 33 GPIO GPIO 3 GPIO				GPIO-51	GPIO	QEP2I or GPIO	GPIO-57			
117 Rev Device Reset (Active low) 120 121 131 132 132 132 132 132 132 132 133 134 135 131		_							_	
117 Rev Device Reset (Active low) 120 121 131 132 132 132 132 132 132 132 133 134 135 131		ta Ta	111		GND	5V0		112	ita	
19 Rev Device reset (ALLIVE GN) 120 121 121 128 128 129 126 126 127 128 128 129 129 129 129 129 120 12		50							<u>.</u>	
121 GPIO GPIO 124 125 GPIO GPIO 124 125 GPIO GPIO 126 GPIO 126 GPIO 128 129 GPIO GPIO 130 131 GPIO GPIO 132 131 GPIO GPIO 132 133 GPIO GPIO 134 135 GPIO 135 GPIO 136 GPIO 138 137 GPIO 138 139 GPIO GPIO 140 GPIO 144 141 GPIO GPIO 144 145 GPIO GPIO 144 145 GPIO GPIO 148 147 GPIO GPIO 148 149 GPIO GPIO GPIO 148 149 GPIO GPIO GPIO 150 GPIO 150 GPIO 150 GPIO 150 GPIO GPIO GPIO 150 GPIO GPIO GPIO GPIO 150 GPIO GP										
123			113		1.5	Bevice Reset (Neuve 1041)		120		
125			121		GPIO	GPIO		122		
127			123		GPIO	GPIO		124		
131			127		GPIO	GPIO		128		
133 GPIO 134 135 6ND 136 6PIO 138 139 GPIO 140 141 GPIO 140 141 GPIO 144 145 GPIO 144 145 GPIO 148 149 GPIO 150 GPIO 150 GPIO 151 151 GPIO 152 153 GPIO GPIO 152 153 GPIO 155 GPIO 156 TST GND TST GPIO TST TST GPIO TST										
137			133		GPIO			134		
139						Reserved (12V0?) GPIO				
143			139		GPIO	GPIO		140		
145 GPIO 146 GPIO 148			143		GPIO	GPIO		144		
149										
153			149		GPIO	GPIO		150		
155										
159			155		GPIO			156		
161 GPIO 162 GPIO 162 163 GPIO 164 165 GPIO 164 165 GPIO 164 165 GPIO 166 GPIO 166 GPIO 166 GPIO 166 GPIO 168 GPIO 169 GPIO 170 GPIO 170 GPIO 171 Rsv 173 Rsv 174 TS Rsv 175 Rsv 177 Rsv 179 GND Reserved (12707) 178 TS						5V0 GPIO				
165 GPIO 166 167 GPIO 168 169 GPIO 168 169 GPIO 170 171 Rsv 172 TS Rsv 174 TS Rsv 175 Rsv 176 TS Rsv 179 GND 179 GND 179 SVU 180 TS			161		GPIO	GPIO		162		
169			165		GPIO	GPIO		166		
171 Rsv 172 Rsv 174 Rsv 174 Rsv 175 Rsv 176 Rsv 176 Rsv 177 Rsv 179 GND Rserved (12007) 178 Sv 179 GND Sv0 1E0 Sv0 1E0 Sv0 Reserved (12007) 178 Sv0 Reserved (12007) 178 Sv0 Sv0 1E0 Sv0 Sv0 Sv0 Sv0 Sv0 Sv0 Sv0 Sv0 Sv0 Sv										
175 Rsv Rsv 176			171		Rsv	Rsv		172		
177 Rsv Reserved (12V0? 178 179 GND 5V0 180 180 180 180 180 180 180 180 180 18										
* No ADC, only comparator connectivity			177		Rsv	Reserved (12V0?)				
			179		GND	500		180		
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			NESISEU	, se altered to collin	and the pin to the pine of when in inputs					
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