

6

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NOTES:

1) USB Differential Pairs - 90 Ohm  
(A) USB-DM & USB-DP  
(B) USB0\_GPIO-42 & USB0\_GPIO-43

REVISION RECORD				
WHO:	SCH REV:	PCB REV:	NOTES:	DATE:
TI-BL	R1.0	R1.0	Draft	24-Mar-2014
TI-BL	R1.1	R1.1	SCH - 1.2V SVS circuitry fix SCH - ADC VREF circuitry improved	23-July-2014

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F2807x controlCARD

REV:

1.0

DATE:

8/4/2014

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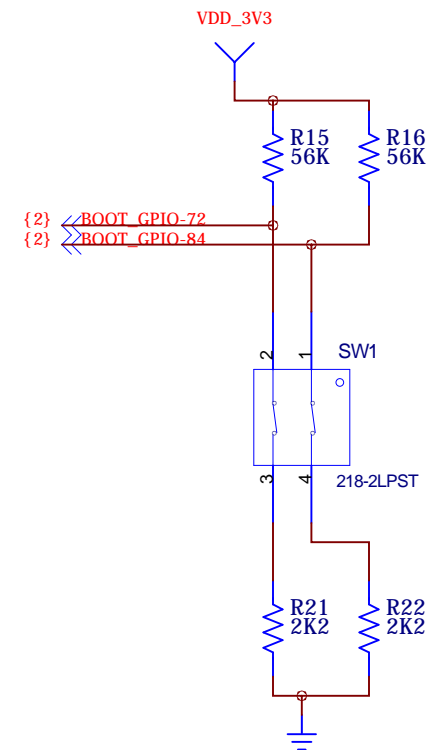
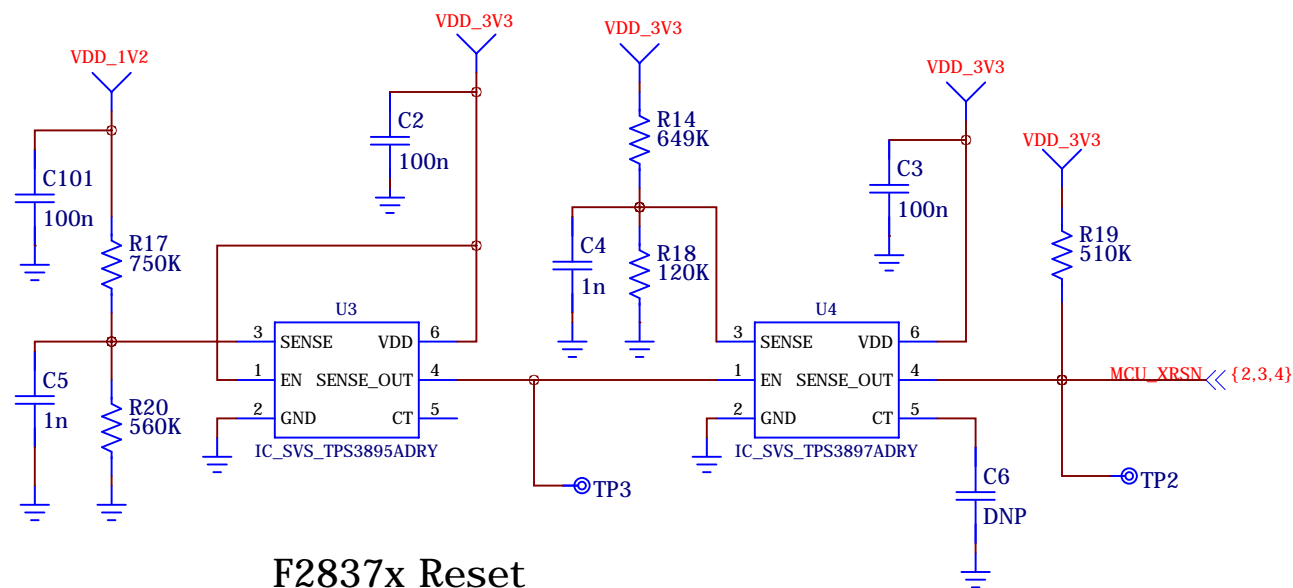
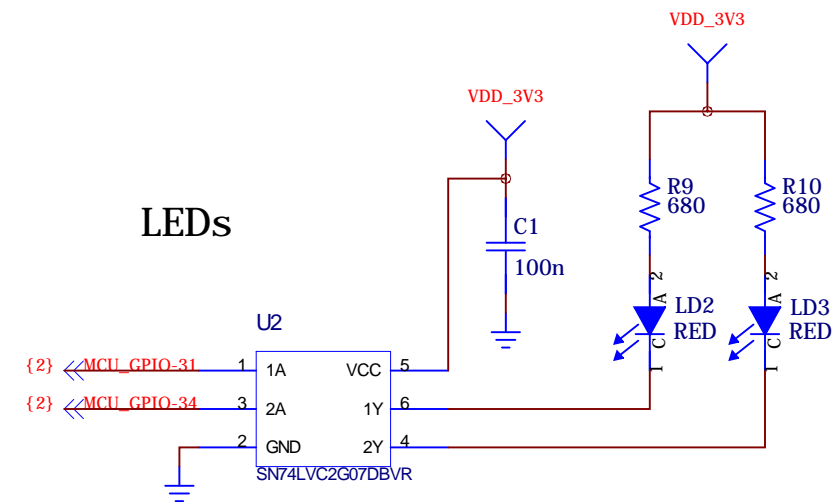
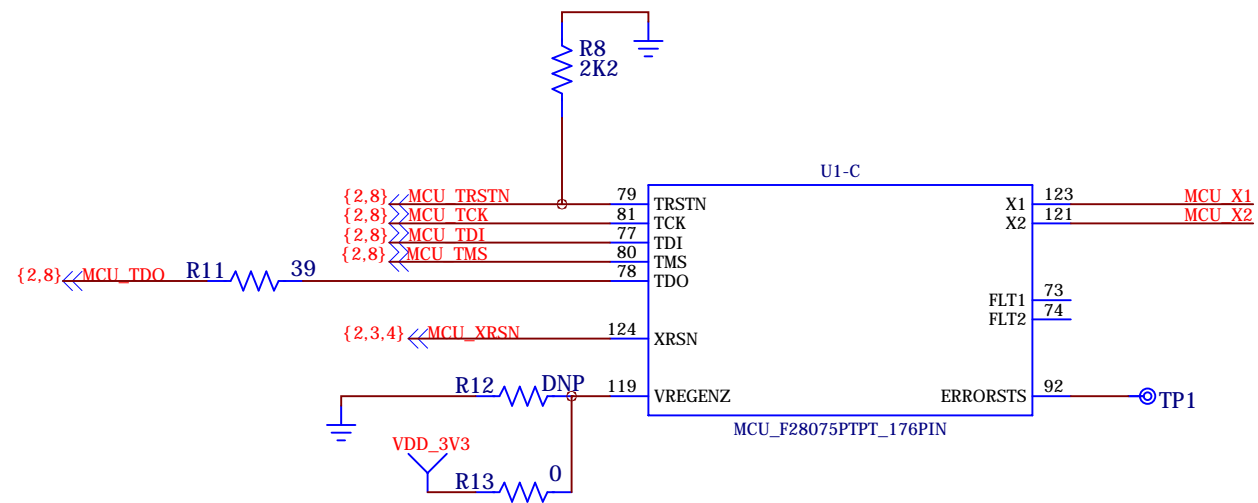
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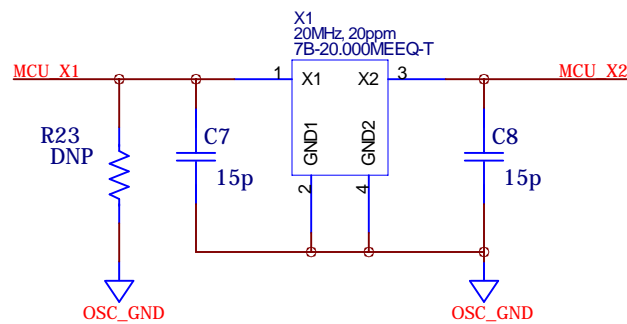
## Boot Mode Selection Switch

\*SW1 placed upside-down (so UP is open (1), DOWN is closed (0))

## Selected Boot Mode Chart

**Selected Boot Mode Chart**  
(see datasheet for other boot modes and more information)

Mode #	GPIO72	GPIO84	Boot Mode
00	0	0	Boot from Parallel GPIO
01	0	1	Boot from SCI
02	1	0	Wait Boot Mode
03	1	1	Get Mode (Flash by default)



## F2837x Clock

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## F2807x controlCARD

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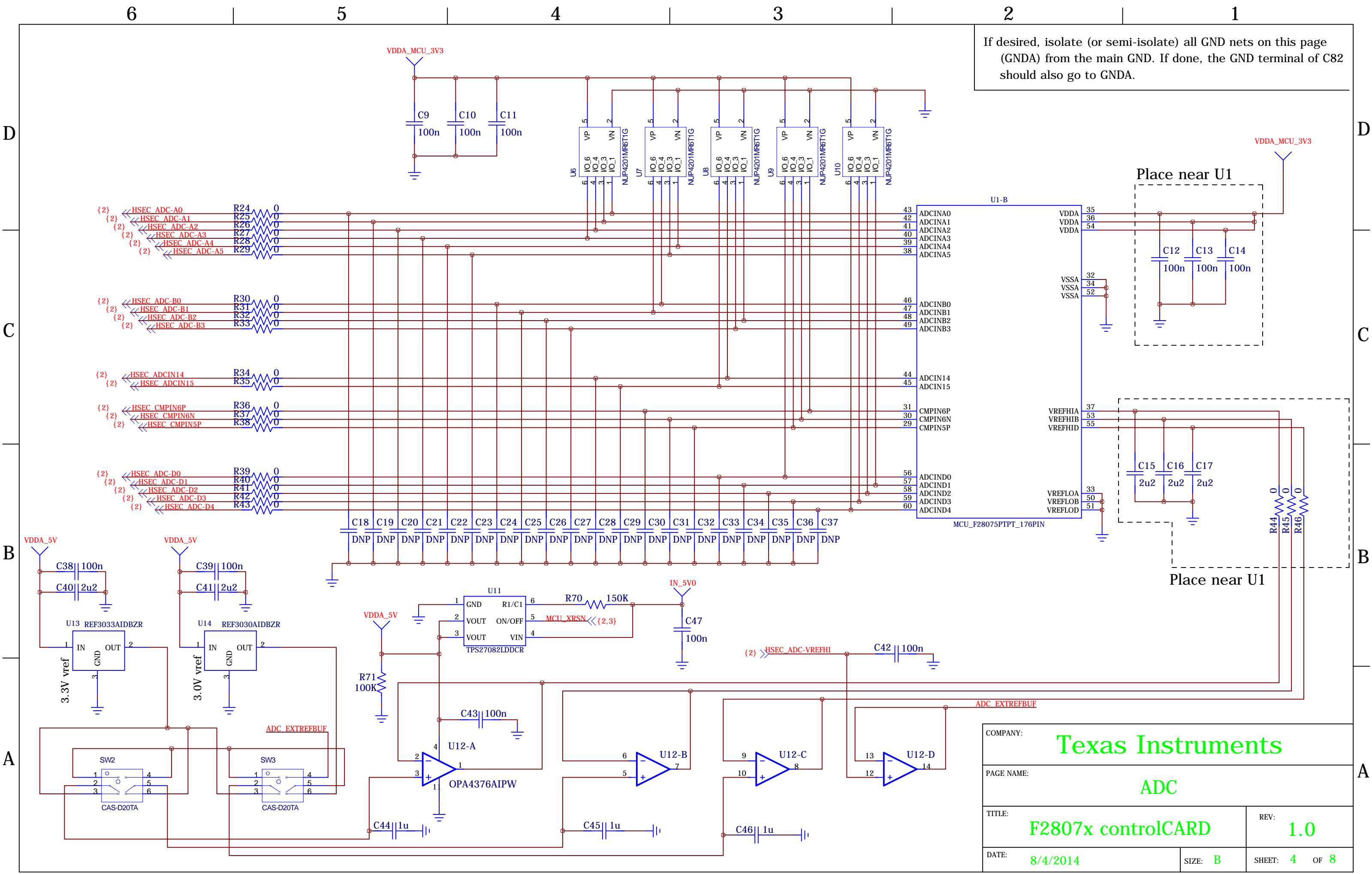
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If desired, isolate (or semi-isolate) all GND nets on this page (GNDA) from the main GND. If done, the GND terminal of C82 should also go to GNDA.

