Date:		Mapping to DIM100 pinout with Adapter card		HSEC Pinout:
4-Mar-2015		F2807x-120		2.11
Exp Kit lbl	DIM pin		DIM pin	Exp Kit lbl
	1		51	
	2		52	
	3	12V	53	
	4		54	
	5		55	
	6		56	
В0		ADC-BO ADC-AO, DAG		A0
	8		58	
B1		ADC-B1 ADC-A1, DAG		A1
_	10		60	
B2		ADC-B2 ADC-A2, COMP+		A2
	12		62	
В3		ADC-B3 ADC-A3		A3
D.4	14	ADC DO	64	0.4
B4	15 16	ADC-D0 ADC-A4, COMP- All VREFHIs **	65	A4
_ B5		ADC-D1 ADC-AS	+	A5
58		GPIO-58 GPIO-59		59
B6		ADC-D2 ADCIN14, COMP4	69	A6
60		GPIO-64 GPIO-65		61
B7		ADC-D3 ADCIN15		A7
62		GPIO-66 GPIO-133		63
00	23	GPIO-00 GPIO-01	. 73	01
02	24	GPIO-02 GPIO-03	74	03
04	25	GPIO-04 GPIO-05	75	05
06	26	GPIO-06 GPIO-07	76	07
	27	5\	77	
08		GPIO-08 GPIO-09		09
10		GPIO-10 GPIO-11		11
48		GPIO-12 GPIO-13		49
84		GPIO-14 GPIO-15		85
86		GPIO-48 5\	82	
12		GPIO-60 GPIO-60		13
15		GPIO-63         GPIO-62           GPIO-24         GPIO-25		14
24 26				25 27
26	36 37	GPIO-26 GPIO-27	87	21
16		GPIO-16 GPIO-17		17
18		GPIO-18 GPIO-19		19
20		GPIO-20 GPIO-21		21
22		GPIO-22 GPIO-23		23
87		GPIO-35 5\	92	
28		GPIO-28 GPIO-29		29
30	44	GPIO-30 GPIO-31	94	31
32	45	GPIO-32 GPIO-33	95	33
34		GPIO-34 5\	96	
	47	TD		
	48	TCK		
	49	TMS TRSTr		
	50		100	
			1	
-			1	
	* No ADC	anly comparator connectivity		
		only comparator connectivity  rs may be altered to connect this HSEC pin to the MCU's VREFHI inputs	1	
+	resisto	is may be altered to connect this riset pin to the Nico's VNEFFI Inputs		
<del>                                     </del>				
1			<b> </b>	