Tabla 1 (RR-TV, Q=3)

Proceso	Llegada	CPU	Prioridad	I/O (rec, ins, dur)	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	TR	TE
P1	0	9	1	(R1, 4, 2) (R2, 6, 3) (R1, 8 3)	>1	2	3	4	R1	R1				5	6	R2	R2	R2				7	8	R1	R1	R1			9<									25	16
P2	1	5	2	(R3, 3, 2) (R3, 4, 2)		>			1	2	3	R3	R3									4			R3	R3					5<							26	21
P3	2	5	3	(R1, 4, 1)			>										1	2	3											4	R1		5<					27	22
P4	3	7	2	(R2, 1, 2) (R2, 5, 3)				>							1				R2	R2					2	3	4						5	R2	R2	R2	6	7<	30	23
P5	5	5	1	(R1, 2, 3) (R3, 4, 3)					>	1	2	R1	R1	R1					3	4	R3	R3	R3					5<										19	14
RR-TV	Q=3	31		R Qeue1	1	1	5	1	4	3	5	2	1	4	5	1	3																				25,4	19,2
				R Qeue2	2	4	2	4																															
				R Qeue3	3																																			

Tabla 2 (TV, Q=3)

Proceso	Llegada	CPU	Prioridad	I/O (rec,ins,dur)	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	TR	TE
P1	0	9	1	(R1, 4, 2) (R2, 6, 3) (R1, 8, 3)	>1	2	3	4	R1	R1				5	6	R2	R2	R2				7	8	R1	R1	R1			9<									25	16
P2	1	5	2	(R3, 3, 2) (R3, 4, 2)		>			1						2	3	R3	R3					4	R3	R3					5<								26	21
P3	2	5	3	(R1, 4, 1)			>										1	2	3										4	R1		5<						27	22
P4	3	7	2	(R2, 1, 2) (R2, 5, 3)				>							1		R2	R2							2	3	4					5	R2	R2	R2	6	7<	30	23
P5	5	5	1	(R1, 2, 3) (R3, 4, 3)						>1	2	R1	R1	R1				3	4	R3	R3	R3		5<														15	10
TV	Q=3			R1 Qeue	1	1	5	1	4	2	5	3	1	5	2	4	1	3																				24,6	18,4
				R Qeue 2	2		2	4																															
				R Qeue 3	3																																			