

```
1  #VARIABLES
2
3  MAKEFILE = makefile
4  EXE = Parking
5  COMP = g++
6  RM = rm
7  EDL = g++
8  ECHO = echo
9  CONFIG = Config.h
10 INT = Mere.h Simulation.h Entree.h Sortie.h
11 REAL = $(INT:.h=.cpp)
12 OBJ = $(INT:.h=.o)
13 OUTPUT = -o
14 RMFLAGS = -f
15 CPPFLAGS = -std=c++11 -Wall
16 EDLFLAGS = $(LIBPATH)
17 LIBS = -ltp -lncurses -ltcl
18 INCLUDES = /share/public/tp/tp-multitache
19 LIBPATH = -L$(INCLUDES)
20 INCPATH = -I$(INCLUDES)
21 CLEAN = clean
22 SOS = backup
23 BACKUPFILE = backup.sh
24 RESSOURCES =
25
26
27 #RULES
28
29 $(EXE) : $(OBJ)
30     $(ECHO) "Edition des liens"
31     $(EDL) $(OUTPUT) $(EXE) $(EDLFLAGS) $(LIBPATH) $(OBJ) $(LIBS)
32
33 %.o : %.cpp
34     $(ECHO) "Compilation de <$<)"
35     $(COMP) $(CPPFLAGS) $(INCPATH) -c $<
36
37
38 $(CLEAN) :
39     $(ECHO) "nettoyage du repertoire..."
40     $(RM) $(RMFLAGS) $(EXE) $(OBJ) core
41
42
43 $(SOS) :
44     ./$$(BACKUPFILE) $(MAKEFILE) $(INT) $(REAL) $(RESSOURCES)
45     $(BACKUPFILE) $(CONFIG)
46
47 Mere.o : Mere.h Simulation.h Entree.h Sortie.h $(CONFIG) $(MAKEFILE)
48 Simulation.o : Simulation.h $(CONFIG) $(MAKEFILE)
49 Entree.o : Entree.h $(CONFIG) $(MAKEFILE)
50 Sortie.o : Sortie.h $(CONFIG) $(MAKEFILE)
```