Subject : Computer Organization & Microprocessor
Date: 16 /02/2022
Class: SE-AIML

INSTRUCTIONS:

1. Time: 10:00AM to 11:00 AM
2. 15 minutes window timing is provided for scanning the answer sheets (Preferably A4)
3. Strict PDF Labeling Format:

   Rollnumber_SE_branch_ Abbreviation_StudentName

   Ex: Roll N0: 15

   DIV- A

   Example: **15_SE_AIML__ROHIT GUPTA**

4. Due date and time : **16 /02/2022** and 11:10 AM
5. Only PDFs Labelled in the above format will be considered for evaluation.

   Q1: Solve any 10 MCQ out of 12 questions (1 marks each MCQ Question).

   Q2: Solve any one out of two questions (5 Marks Each Question)

   Q3: Solve any one out of two questions (5 Marks Each Question)

Q1: Solve the following MCQs (Any 10)

Q2.A) Describe Von-neumann Architecture in detail. [5]

OR

Q2.B) Describe flag register of 8086 microprocessor in detail. [5]

Q3.A) Draw and discuss minimum mode of 8086. [5]

OR

Q3.B) Discuss physical address generation mechanism with example . [5]