1. Description

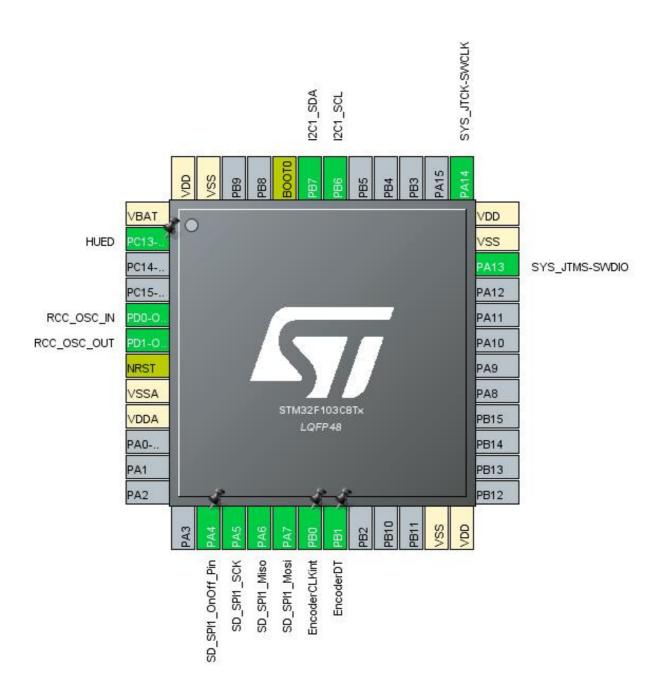
1.1. Project

Project Name	Blink294
Board Name	custom
Generated with:	STM32CubeMX 5.3.0
Date	09/02/2019

1.2. MCU

MCU Series	STM32F1
MCU Line	STM32F103
MCU name	STM32F103C8Tx
MCU Package	LQFP48
MCU Pin number	48

2. Pinout Configuration

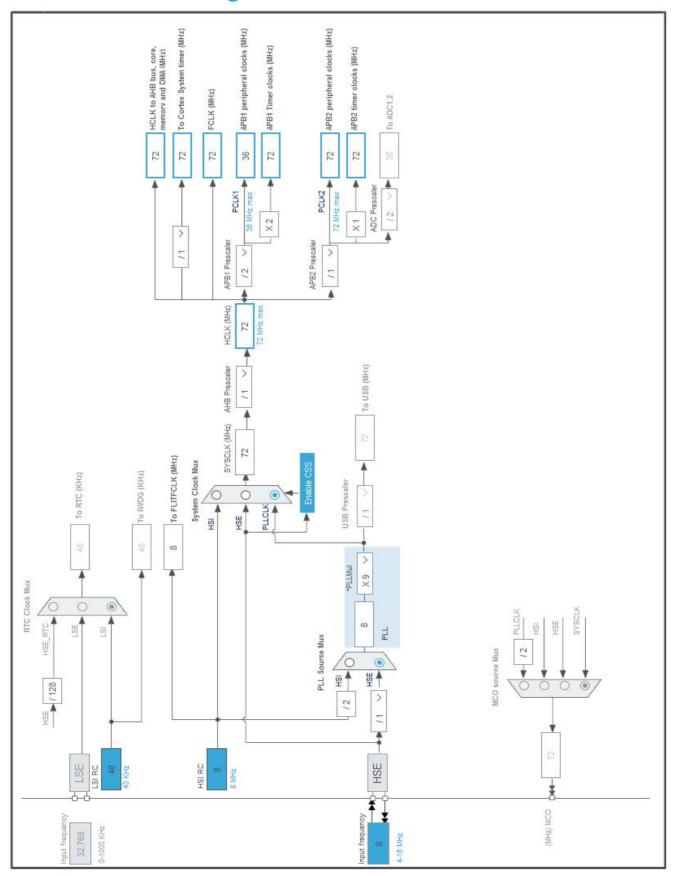


3. Pins Configuration

Pin Number LQFP48	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VBAT	Power		
2	PC13-TAMPER-RTC *	I/O	GPIO_Output	HUED
5	PD0-OSC_IN	I/O	RCC_OSC_IN	
6	PD1-OSC_OUT	I/O	RCC_OSC_OUT	
7	NRST	Reset		
8	VSSA	Power		
9	VDDA	Power		
14	PA4 *	I/O	GPIO_Output	SD_SPI1_OnOff_Pin
15	PA5	I/O	SPI1_SCK	SD_SPI1_SCK
16	PA6	I/O	SPI1_MISO	SD_SPI1_Miso
17	PA7	I/O	SPI1_MOSI	SD_SPI1_Mosi
18	PB0	I/O	GPIO_EXTI0	EncoderCLKint
19	PB1 *	I/O	GPIO_Input	EncoderDT
23	VSS	Power		
24	VDD	Power		
34	PA13	I/O	SYS_JTMS-SWDIO	
35	VSS	Power		
36	VDD	Power		
37	PA14	I/O	SYS_JTCK-SWCLK	
42	PB6	I/O	I2C1_SCL	
43	PB7	I/O	I2C1_SDA	
44	воото	Boot		
47	VSS	Power		
48	VDD	Power		

^{*} The pin is affected with an I/O function

4. Clock Tree Configuration



5. Software Project

5.1. Project Settings

Name	Value		
Project Name	Blink294		
Project Folder	F:\Basay\Dropbox\BSK\KOTYOL\STM\PROJECTS\Blink294		
Toolchain / IDE	Other Toolchains (GPDSC)		
Firmware Package Name and Version	STM32Cube FW_F1 V1.8.0		

5.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Add necessary library files as reference in the toolchain project configuration file
Generate peripheral initialization as a pair of '.c/.h' files	Yes
Backup previously generated files when re-generating	No
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power	No
consumption)	

6. Power Consumption Calculator report

6.1. Microcontroller Selection

Series	STM32F1
Line	STM32F103
мси	STM32F103C8Tx
Datasheet	13587_Rev17

6.2. Parameter Selection

Temperature	25
11/700	3.3

7. IPs and Middleware Configuration 7.1. I2C1

12C: 12C

7.1.1. Parameter Settings:

Master Features:

I2C Speed Mode Fast Mode *

I2C Clock Speed (Hz) 400000

Fast Mode Duty Cycle Duty cycle Tlow/Thigh = 2

Slave Features:

Clock No Stretch Mode Disabled
Primary Address Length selection 7-bit
Dual Address Acknowledged Disabled
Primary slave address 0

General Call address detection Disabled

7.2. RCC

High Speed Clock (HSE): Crystal/Ceramic Resonator

7.2.1. Parameter Settings:

System Parameters:

VDD voltage (V) 3.3
Prefetch Buffer Enabled

Flash Latency(WS) 2 WS (3 CPU cycle)

RCC Parameters:

HSI Calibration Value 16
HSE Startup Timout Value (ms) 100
LSE Startup Timout Value (ms) 5000

7.3. SPI1

Mode: Full-Duplex Master 7.3.1. Parameter Settings:

Basic Parameters:

Frame Format Motorola

Data Size 8 Bits
First Bit MSB First

Clock Parameters:

Prescaler (for Baud Rate) 2

Baud Rate 36.0 MBits/s *

Clock Polarity (CPOL) Low
Clock Phase (CPHA) 1 Edge

Advanced Parameters:

CRC Calculation Disabled
NSS Signal Type Software

7.4. SYS

Debug: Serial Wire

Timebase Source: SysTick

^{*} User modified value

8. System Configuration

8.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
I2C1	PB6	I2C1_SCL	Alternate Function Open Drain	n/a	High *	
	PB7	I2C1_SDA	Alternate Function Open Drain	n/a	High *	
RCC	PD0- OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
	PD1- OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	
SPI1	PA5	SPI1_SCK	Alternate Function Push Pull	n/a	High *	SD_SPI1_SCK
	PA6	SPI1_MISO	Input mode	No pull-up and no pull-down	n/a	SD_SPI1_Miso
	PA7	SPI1_MOSI	Alternate Function Push Pull	n/a	High *	SD_SPI1_Mosi
SYS	PA13	SYS_JTMS- SWDIO	n/a	n/a	n/a	
	PA14	SYS_JTCK- SWCLK	n/a	n/a	n/a	
GPIO	PC13- TAMPER- RTC	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	HUED
	PA4	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	SD_SPI1_OnOff_Pin
	PB0	GPIO_EXTI0	External Interrupt	Pull-up *	n/a	EncoderCLKint
			Mode with			
			Rising/Falling edge			
	PB1	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	EncoderDT

8.2. DMA configuration

nothing configured in DMA service

8.3. NVIC configuration

Interrupt Table	Enable	Preenmption Priority	SubPriority	
Non maskable interrupt	true	0	0	
Hard fault interrupt	true	0	0	
Memory management fault	true	0	0	
Prefetch fault, memory access fault	true	0	0	
Undefined instruction or illegal state	true	0	0	
System service call via SWI instruction	true	0	0	
Debug monitor	true	0	0	
Pendable request for system service	true	0	0	
System tick timer	true	0	0	
EXTI line0 interrupt	true	0	0	
PVD interrupt through EXTI line 16	unused			
Flash global interrupt	unused			
RCC global interrupt	unused			
I2C1 event interrupt	unused			
I2C1 error interrupt	unused			
SPI1 global interrupt	unused			

^{*} User modified value

9. Software Pack Report