ECEN405 Lab 5 Report Buck-Boost Converter

Niels Clayton: 300437590 Lab Partner: Daniel Eisen

1 Design

 $V_d = 10V, \ R_L = 500\Omega, \ L = 4mH, \ C = 100\mu F, \ D = 0.6, D_{max} = 0.67$

Switching Frequency

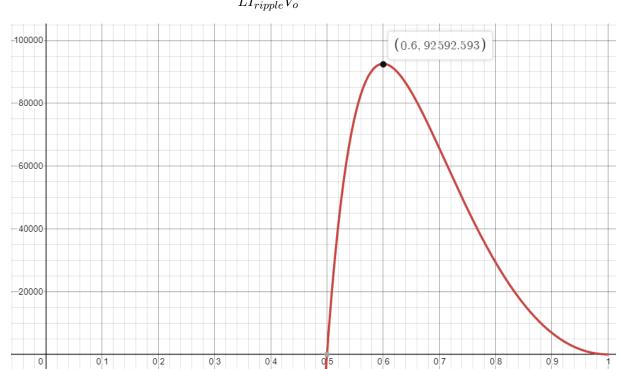
$$V_{o} = \frac{D}{1 - D}V_{d} = 15V$$

$$I_{o} = \frac{V_{o}}{R_{L}} = 0.03A$$

$$I_{ripple} = 0.2I_{o}\frac{V_{o}}{V_{d}} = 0.009A$$

$$I_{omax} = I_{o} + 0.5I_{ripple} = 0.0345A$$

$$f_{sw} = \frac{V_{d}(V_{o} - V_{d})}{LI_{ripple}V_{o}} = 92592.5925926Hz$$



Duty Cycle vs Switching Frequency

Note that a 60% duty cycle requires the highest switching frequency.

Output Voltage Ripple

a half written report on it.

Tek

person accounts of the original waveform.

Schematic

$$V_{ripple} = \frac{I_{omax}D_{max}}{f_{sw}C} = 0.00425V$$

The USB drive with the "real" screenshots are still in the lab plugged into the computer with

GND

M Pos: -520.0ms

CURSOR

Useful

2 Output

Stop

As shown from the image below, it can be remembered that there was a substantial output volt-

age ripple, with possible peaks of 20 mV, and large transients at the locations of the switching.

In light of this misplacement we have acquired an artistic recreation of the signal, based on first

