



### Module description (courses)

**Module title, ECTS credits**

*Danish title:* Signalbehandling

*English title:* Signal processing

5 ECTS

*Period:* 1 September 2018 – 31 January 2019

*Campus:* Aalborg

**Location**

ITC5, EIT5

**Module coordinator and secretary**

*The academic staff member responsible:* Søren Krarup Olesen, [sko@es.aau.dk](mailto:sko@es.aau.dk)

*Secretary:*

**Type and language**

*Module type:* Course module

*Language of instruction:* English

**Objectives**

The learning objectives are specified in the [Study Curriculum](#).

**Academic content and conjunction with other modules/semesters**

The signal processing course will assist students to acquire theoretical, methodological and practical skills in designing analog and digital filters and to do spectral estimation. Within these three main topics students will study:

*Analog filters*

- Types of filters (Butterworth, Chebyshev etc.)
- Filter characteristics, e.g. group delay
- Frequency- and impedance-scaling
- LP-HP and LP-BP transformation
- Realization of active LP, HP and BP-filters

*Digital filtering*

- Synthesis of transfer functions (FIR, IIR)
- Analysis (frequency response, signal graphs)
- Realization/implementation-aspects

*Spectral estimation*

- Discrete Fourier Transform (DFT)
- Time- and frequency-sampling, multiplication in the time- and frequency domains
- Window functions, zero padding, resolution
- Effective algorithms (FFT)

The prerequisites for the course are knowledge on calculus, circuit theory, dynamic systems, Laplace transformations, Z-transformations, Fourier Transformations and Fourier Series. During exercises it is required to be familiar with and have access to Matlab (or similar) for processing and visualizing data.

<p>The course will provide students with competences that are required in the current and/or subsequent project modules and courses on digital signal analysis and processing.</p>
<p><b>Scope and expected performance</b></p> <p>It should be emphasized that the university expects the students to spend 30 hours per ECTS credit points on their studies, corresponding to 900 hours per semester. This corresponds to a study load of approximately 45 hours per week.</p> <p>This is a 5 ECTS module and the expected workload on students is 150 hours.</p> <p>The planned lectures comprise:</p> <ul style="list-style-type: none"> <li>● 15 lectures followed by exercises (total: 15 x 4 hours)</li> <li>● 1 exercise session in the laboratory (total: 8 hours)</li> </ul> <p>Students are expected to prepare for each lecture by reading suggested literature. This will be detailed on Moodle. Students should plan to spend time on preparing for the examination. The examination will be held in January and February 2019.</p>
<p><b>Participants</b></p> <p><i>Semesters attending the module: ITC5, EIT5</i></p>
<p><b>Prerequisites for participation</b></p> <p>The prerequisites for participation are listed in the <a href="#">Study Curriculum</a>.</p>
<p><b>Module activities (course sessions etc.)</b></p> <p>Information about each teaching activity is available at <a href="#">Moodle</a>. This includes e.g.:</p> <ul style="list-style-type: none"> <li>• Type of teaching (lecture, workshop, laboratory work, study trip etc.)</li> <li>• The title and number of the teaching activity and possibly a brief description of the activity (course introduction)</li> <li>• Date and time of the activity</li> <li>• Lecture room</li> <li>• Lecturer(s)</li> <li>• Set and recommended readings</li> <li>• Slides etc.</li> <li>• Assignments/exercises</li> </ul>
<p><b>Examination</b></p> <p>Information on the examination and assessment is given in the <a href="#">Study Curriculum</a>.</p> <p>Additional information on e.g. requirements for attending the examination, date, permitted tools etc. is available at:</p> <p><a href="https://www.sict.aau.dk/electronics-and-it/exam-dates/">https://www.sict.aau.dk/electronics-and-it/exam-dates/</a></p>