

① Noisy quantum Devices

- * Qubits → Relaxation and dephasing on timescales T_1 and T_2 (Scale of 10^2 to $10^3 \mu s$)
(Superconducting qubits)

Single qubit gates

duration $T_{1q} \approx 20 ns$

Two qubit operations and measurements have duration $T_{2q} \approx 200 ns$
 $T_m \approx 700 ns$

- * Qubit coherence:

Defines timescales over which quantum information is lost

- * Gate times and errors define the duration and accuracy of a computation.

- * Duration of computation should not exceed qubit coherence time

- * Number of gates should be such that accumulation of error does not prevent algorithms from

yielding accurate results.

* Width and depth are often used to characterize the cost of a quantum circuit.

→ Width: Number of qubits that comprise the circuit. (N_q)

→ Depth: Maximal length d of a path from the input (qubit initialization) to the output (measurement operation)

* Important computational bottleneck when the physical duration of a circuit, roughly is comparable with coherence time of a qubit.

Problem at hand:

$$\left[-\frac{1}{2} \nabla^2 + V(x) \right] \psi_i = \epsilon_i \psi_i$$
$$H |\psi_i\rangle = \epsilon_i |\psi_i\rangle$$

Finite difference

\Downarrow

Sparse matrix

Quantum Subspace methods

* Construct a variational subspace spanned by a set of states $\{|\psi_\alpha\rangle\}_{\alpha=0}^{n-1}$

* $S_{\alpha\beta} = \langle \psi_\alpha | \psi_\beta \rangle$
 $H_{\alpha\beta} = \langle \psi_\alpha | \hat{H} | \psi_\beta \rangle$

$$HC = SC\tilde{E} \quad \text{--- (1)}$$

* Quantum Device computes $H_{\alpha\beta}$ and $S_{\alpha\beta}$

* classical computer solves (1) to obtain approximate eigen pairs $\{\tilde{E}_\mu^{(n)}, |\tilde{\psi}_\mu^{(n)}\rangle\}$

* QSE does not require nonlinear parameter optimization like what we have VQE

* Each update of quantum circuit parameters requires a new call of the quantum computer in VQE

* In QSE all circuits required to measure $H_{\mathcal{L}}$ and $S_{\mathcal{L}}$ can be sent to quantum computer in one call.

* QSE circuits can be naturally parallelised over multiple quantum computers (groups of qubits within a quantum computer)

* Eigenstates are never stored on a quantum devices.

Choice of basis $\{|v_d\rangle\}_{d=0}^{n-1}$

(a) Chebyshev quantum Krylov:-

- * Quantum implementation of classical Krylov space.
- * No algorithmic error in the construction of Krylov Subspace (No Trotterization and other approximate time evolution algs)
- * Powers of ^{Generating} Hamiltonian H mathematically equivalent to Subspace generated by any basis for polynomials of Hamiltonian
- * Block encoding of Hamiltonian $|v_d\rangle = T_d(H) |v_0\rangle$
- * H is key idea

* This requires the use of ancillas and deep quantum circuits!

(b) Gaussian-power quantum Krylov:

* Subspace is constructed using conventional power function acting on the Hamiltonian. Starting state $|V_0\rangle$ is replaced with

$$e^{-\frac{1}{2}(\hat{H}-E_0)\tau^2} |V_0\rangle$$

$$|V_\alpha\rangle \propto (\hat{H}-E_0) e^{-\frac{1}{2}(\hat{H}-E_0)\tau^2} |V_0\rangle$$

$$\alpha = 0 \dots n-1$$

Realized by linear combination of unitaries (LCU)

Time evolution methods

1. Quantum Filter Diagonalization
2. Quantum Lanczos based on imaginary time evolution