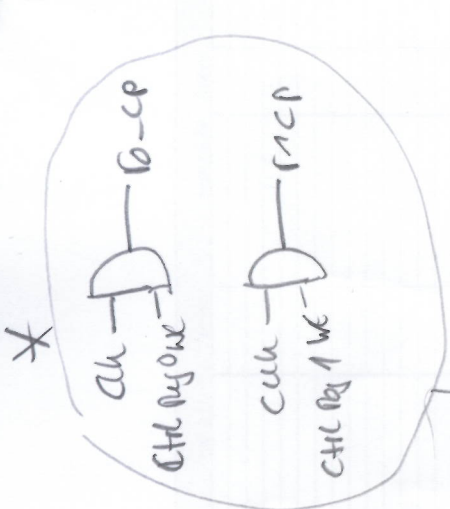
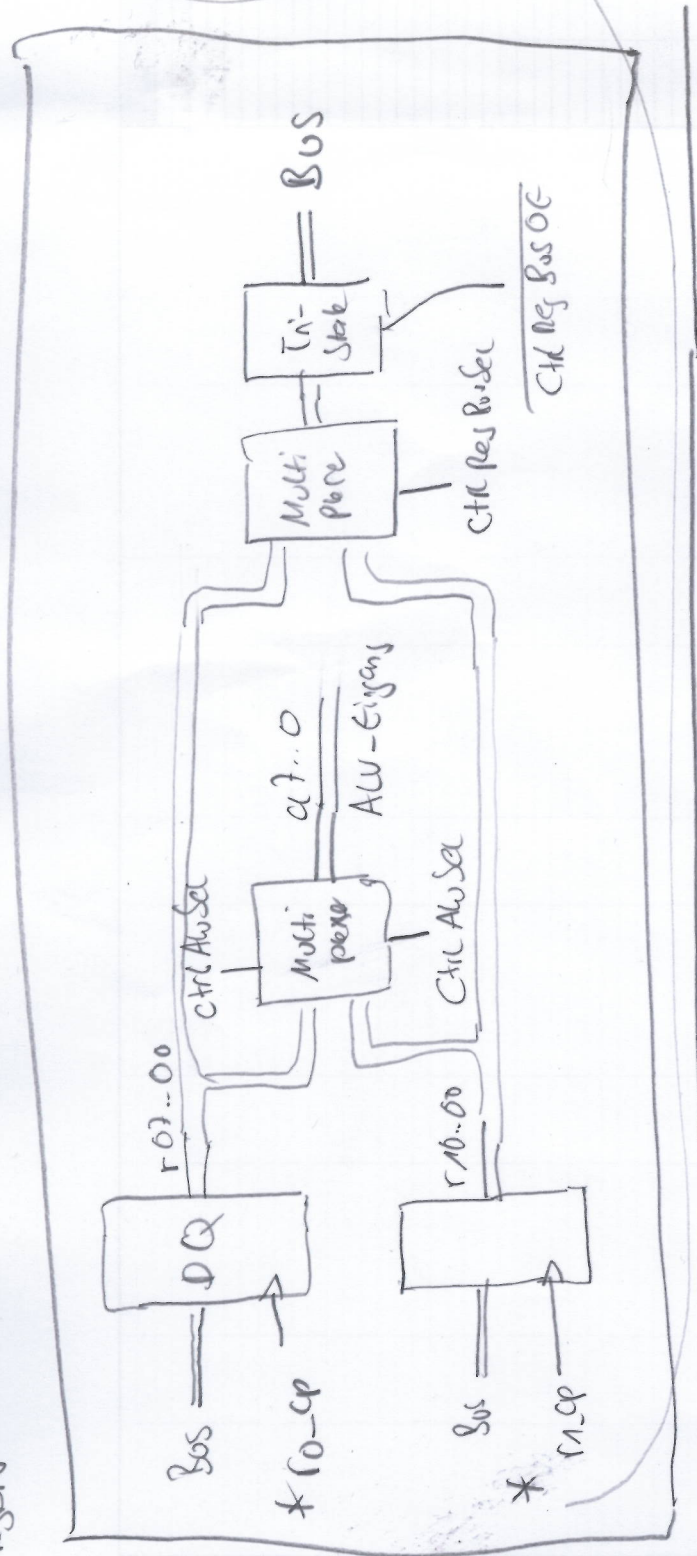


Register



ALU

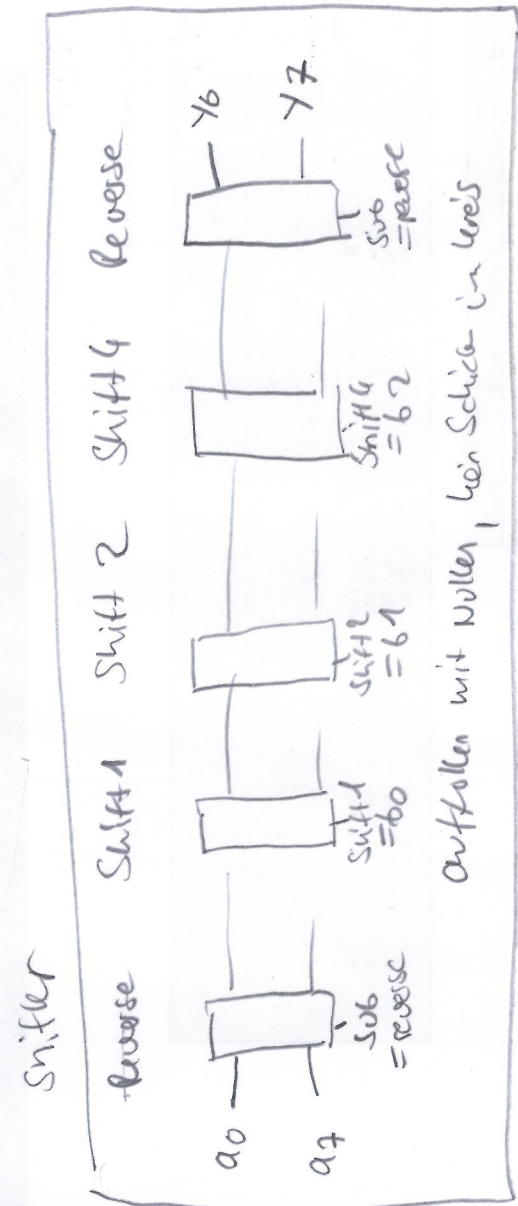
Operation

Inputs:

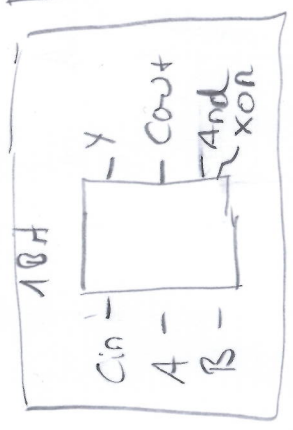
Ctrl ALU OP	Sub (Shift ALU)	Sub (Shift ALU)	Sub (Shift ALU)
00	add	add	add
01	XOR	XOR	XOR
10	AND	AND	AND
11	SHIFT	SHIFT	SHIFT

Ctrl ALU OE

Ycp-d Clock ALU-Regist



Addier



Addier 4 Bit

