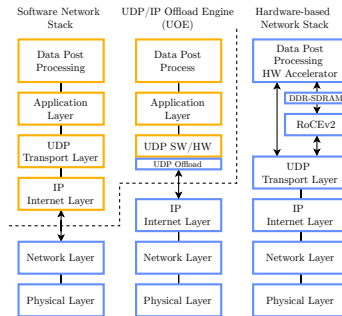# Title

Niklas Schelten

Field Prorgammable Gate Arrays (FPGAs)

- – Programmable Hardware

- – Logic Gates implemented as Lookup Tables in form of MUXs

- – SRAM used to alter functionality

- – Additional DSP blocks + direct SRAM

- – Interconnection + Clock pathes

Thank you!