

# Title

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*Abstract*—**Abstract**  
*Index Terms*—**FPGA**

## I. INTRODUCTION

Field Programmable Gate Array (FPGA) as in [1].

## REFERENCES

- [1] F. Steinert, N. Schelten, A. Schulte, and B. Stabernack, “Hardware and software components towards the integration of network-attached accelerators into data centers,” in *2020 23rd Euromicro Conference on Digital System Design (DSD)*, 2020, pp. 149–153.