Politecnico di Torino

Digital systems electronics
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Prof. G. Masera

Lab 01

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Berchialla Luca	236032
Laurasi Gjergji	238259
Mattei Andrea	233755
Lombardo Domenico Maria	233959

1 Controlling the LEDs

2 2-to-1 Multiplexer

As figure 1 shows we are implementing a 2-to-1 Multiplexer using a stuctural approach. Since its internal implementation its pretty trivial we will not discuss it any further.

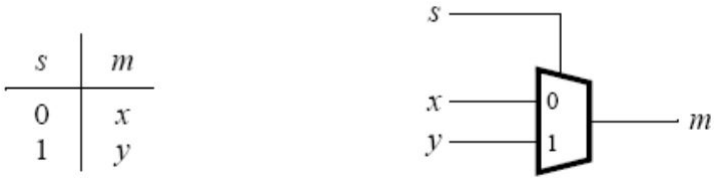

Figure 1: multiplexer + truth table

Its VHDL code has been written under Quartus prime. Than a testbench was developed under modelsim. In the testbench we fixed the two multplexed inputs and than varing the selection bit we checked the behaviour.

After the simulation was succesful we impoted the pin assignements into Quartus prime and after compiling and loading it on the board we verified the actual behaviour.

3 5-to-1 Multiplexer

We're now interested in designing the circuit shown in figure 2, where the output signal can be chosen between 5 different input signals with a 3 bit control signal as shown in the truth table.
The circuit model has been implemented using a behavioural approach, that leads to a faster but less customizable solution to design a 5 to 1 MUX.
The circuit has been proved to work thanks to a testbech simulation, checking the correspondence of input-output following the already cited truth table.
Finally the vhdl code has been loaded into the hardware fpga showing positive results as expected by simulation.

s_2	s_1	s_0	m
0	0	0	u
0	0	1	v
0	1	0	w
0	1	1	x
1	0	0	y
1	0	1	y
1	1	0	y
1	1	1	y

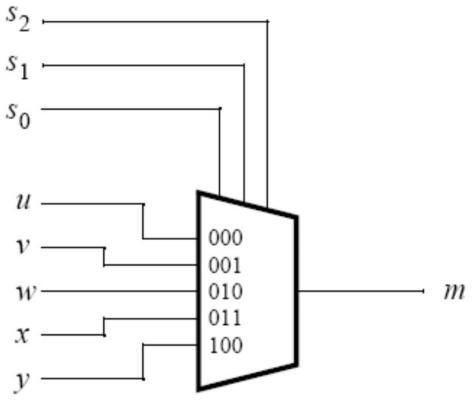

Figure 2: multiplexer + truth table