



POLITECNICO DI TORINO

DIGITAL SYSTEMS ELECTRONICS  
A.A. 2018/2019

PROF. G. MASERA

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## Lab 03

2 Apr 2019

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**1 4-bit Sequential RCA**

**2 4-bit Sequential Adder/Subtractor**

**3 3**

punto 3

**4 4**

punto 4