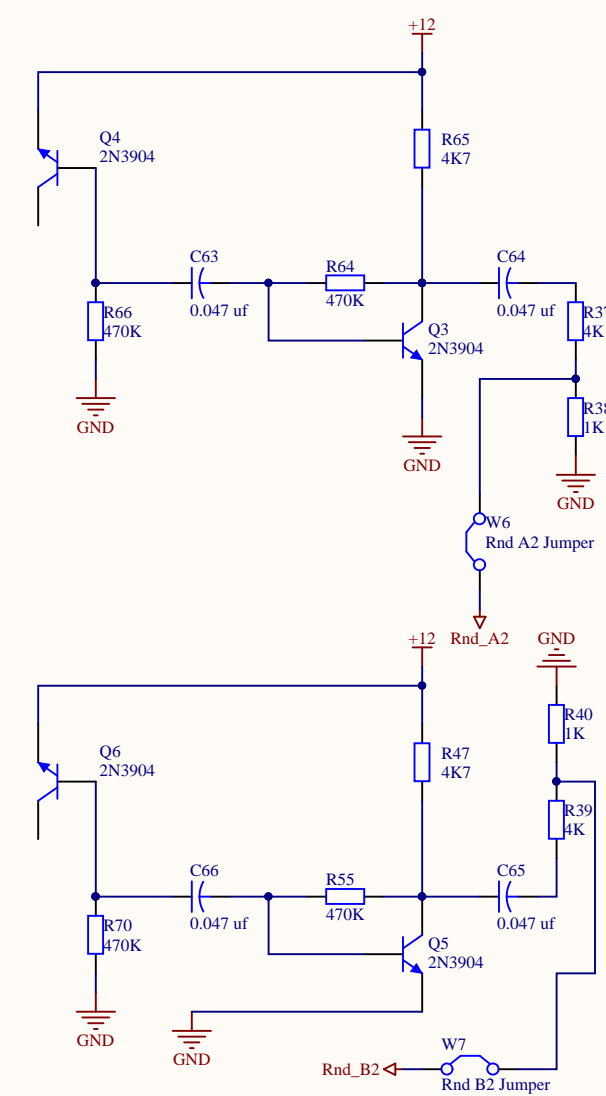
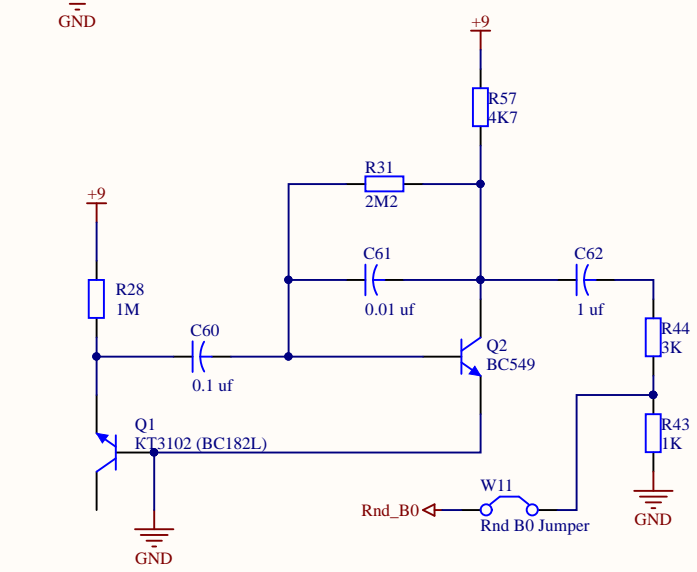
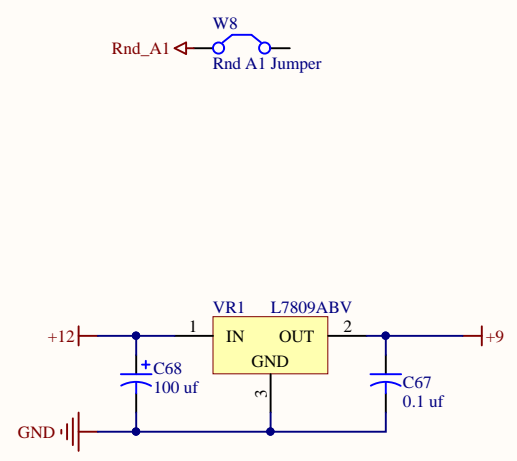
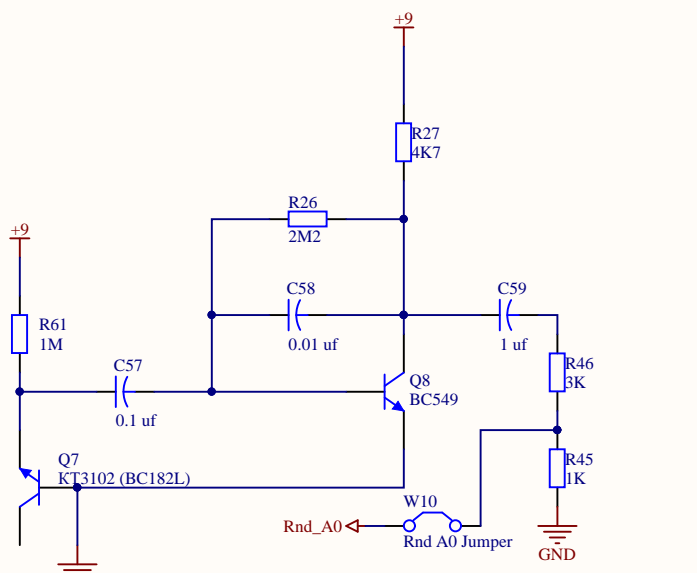
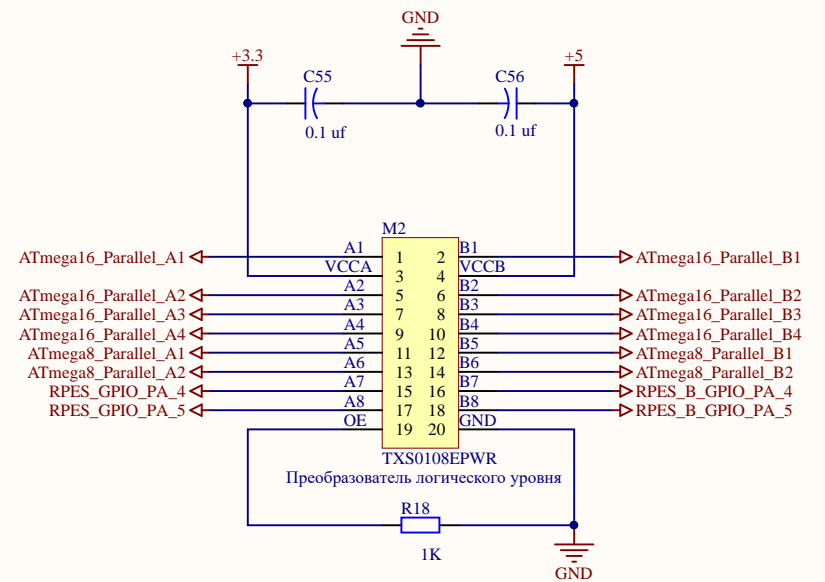
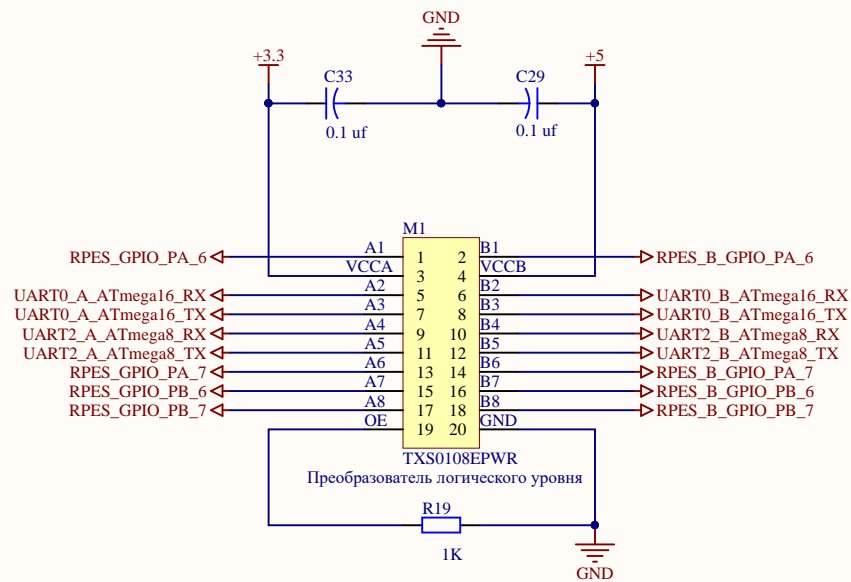


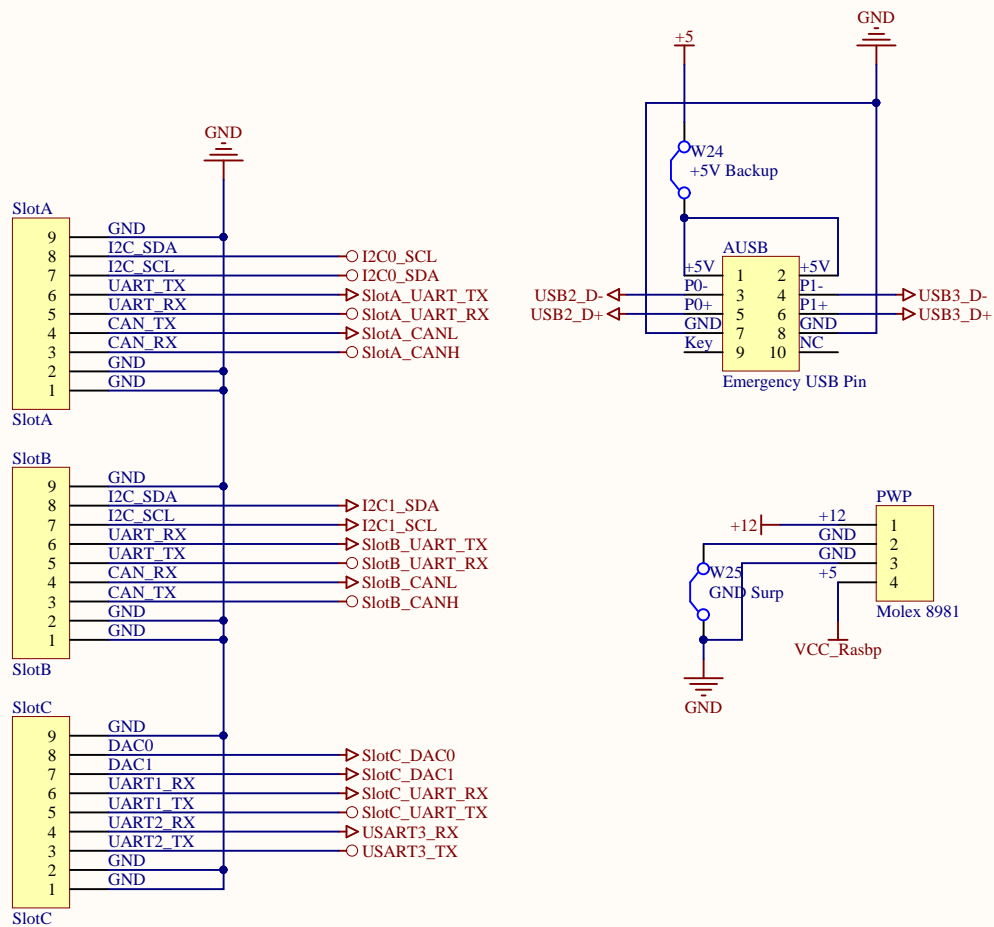
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Title		
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Title			
Size	Number		Revision
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Date:	22.06.2021	Sheet	of
File:	C:\Users\...\Sockets.SchDoc	Drawn By:	

CPU #2: STM32F415RGT6 @ 168 mhz

CPU-0, 32-bit ARM MCPU

CAN0 = [CAN1]: STM32-0 <=> SlotA (3.3V LL)

I2C2 = [I2C1] : STM32-0 <=> STM32-1 (3.3V LL)

SPI1 = [SPI2]: STM32-0 <=> STM32-1 (3.3V LL)

SPI2 = [SPI1]: STM32-0 <=> Raspberry Pi GPIO Slot (3.3V LL)

SPI4 = [SPI3]: STM32-0 <=> SD CARD (3.3V LL)

UART0 = [USART2] STM32-0 <=> ATmega16 (3.3V - 5V LL)

UART3 = [USART6]: STM32-0 <=> SlotA (3.3V LL)

UART4 = [USART1]: STM32-0 <=> Raspberry Pi GPIO Slot (3.3V LL)

UART6 = [USART3]: STM32-0 <=> STM32-1 (3.3V LL)

UART7: [UART5] STM32-0 <=> SlotC (3.3V LL)

USB2 = [USB Device]: STM32-0 <=> VT6121L (Port #2)

USB4 = [USB OTG]: STM32-0 <=> USB Mass Storage

UART7: [UART5]

UART5_TX {PC12} ==> SlotC_UART_RX

UART5_RX {PD2} ==> SlotC_UART_TX

UART0: [USART2]:

USART2_TX {PA2} ==> UART0_A_ATmega16_RX

USART2_RX {PA3} ==> UART0_A_ATmega16_TX

UART4: [USART1]:

USART1_TX {PA9} ==> RPES_UART_RX

USART1_RX {PA10} ==> RPES_UART_TX

UART6: [USART3]:

USART3_TX {PC10} ==> CPU1_UART6_RX

USART3_RX {PC11} ==> CPU1_UART6_TX

SPI2: [SPI1]

SPI1_MOSI {PA7} ==> RPES_SPI0_MOSI

SPI1_MISO {PA6} ==> RPES_SPI0_MISO

SPI1_SCK {PA5} ==> RPES_SPI0_SCK

SPI1_SS {PC6}

I2C2: [I2C1]

I2C1_SCL {PB6} ==> CPU1_I2C2_SCL

I2C1_SDA {PB7} ==> CPU1_I2C2_SDA

UART3: [USART6]:

USART6_TX {PC6} ==> SlotA_UART_RX

USART6_RX {PC7} ==> SlotA_UART_TX

SPI1: [SPI2]

SPI2_MOSI {PA7} ==> SPI3_MOSI

SPI2_MISO {PA6} ==> SPI3_MISO

SPI2_SCK {PA5} ==> SPI3_SCK

SPI2_SS {PC7}

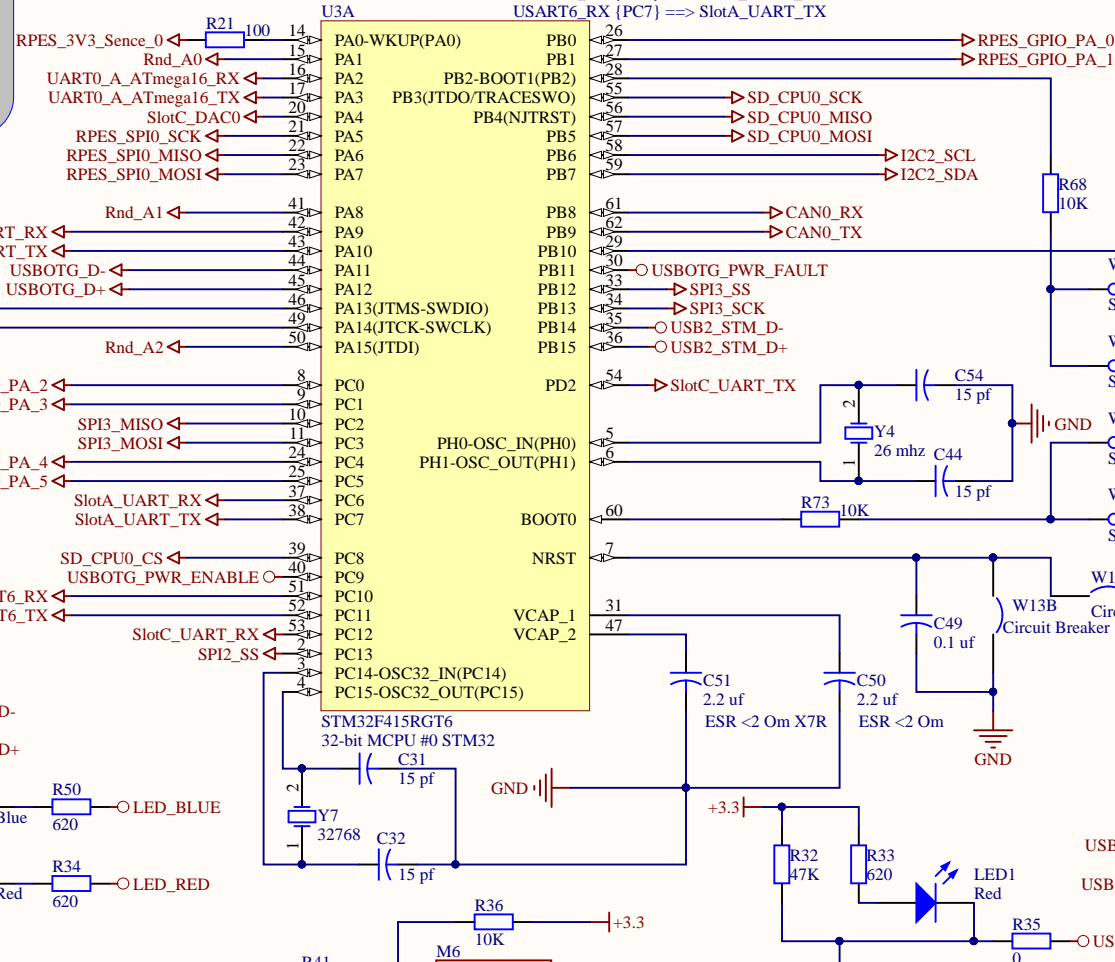
SPI4: [SPI3]

SPI3_MOSI {PB5} ==> SD_CPU0_MOSI

SPI3_MISO {PB4} ==> SD_CPU0_MISO

SPI3_SCK {PB3} ==> SD_CPU0_SCK

SPI3_SS {PC8} ==> SD_CPU0_CS



Title		
Size	Number	Revision
A4		
Date:	22.06.2021	Sheet of
File:	C:\Users\...\STM32 Motherboard.SchDoc	Drawn By:

