Nikita Ovramenko

647-9169777 | nikita.ovramenko@torontomu.ca | GitHub | LinkedIn | Portfolio

SUMMARY OF QUALIFICATIONS

- Practical experience in programming with Java, Python, and C/C++, and developed projects using JavaFX, Python (with aiogram), and web technologies.
- Have experience in MongoDB, and Linux, with strong knowledge in digital design and embedded systems.
- Demonstrated leadership by managing multiple team projects.
- Seeking a challenging engineering role to apply technical and interpersonal skills in creative projects.

EDUCATION

Toronto Metropolitan University

Toronto, ON

Bachelor of Engineering in Computer Engineering; CGPA: 3.03

Sep. 2022 - May 2026

PROJECTS

Personal Portfolio Website | React, Typescript, CSS, HTML

January 2025 – Present

- Developed a responsive and interactive **portfolio website** to showcase personal projects and achievements.
- Designed and implemented a user-friendly interface using **React**, ensuring a smooth navigation experience across different sections.
- Integrated a Google Map feature in the contact section to enhance user interaction and accessibility.
- Hosted and deployed the portfolio on **GitHub Pages** at https://nikitaaovramenko.github.io/Portfolio1/, with easy access to explore **other projects and accomplishments**.

Small Bank App | Java, JavaFX, NetBeans, SceneBuilder

February 2024 – March 2024

- Developed a bank app for around **100 users**, supporting accurate account and transaction management with zero data errors.
- Implemented account management, transaction processing, and user authentication using Java.
- Built a system where each customer's account data is stored in a separate file, ensuring **privacy** and **security**.
- Applied object-oriented principles, including encapsulation, inheritance, and polymorphism, ensuring code reusability and reducing maintenance.
- Utilized the **State Design Pattern** to develop a dynamic ranking system (Silver, Gold, Platinum), which streamlined the process of handling account-level promotions, making it more **efficient** and **responsive**.

General-Purpose Processor Design | Verilog, Quartus II, Digital Logic

December 2023

- Collaborated on designing a simple **general-purpose processor**, demonstrating core digital logic principles.
- Developed components such as latches, a finite state machine (FSM), and a 4:16 decoder to manage binary input states and produce accurate outputs.
- Implemented an Arithmetic Logic Unit (ALU) capable of performing multiple operations including sum, difference, bitwise XOR, and binary shifts.

TECHNICAL SKILLS

Programming Languages: Java, Python, C/C++, C#, JavaScript, Typescript, HTML/CSS, MATLAB

Frameworks/Libraries: JavaFX, JUnit ,React

Developer Tools: NetBeans, Visual Studio, Replit, Geany, SceneBuilder

Databases: MongoDB

Hardware and OS: Raspberry Pi 4, Linux, OpenMediaVault