



**RAJALAKSHMI  
ENGINEERING COLLEGE**

An AUTONOMOUS Institution  
Affiliated to ANNA UNIVERSITY, Chennai

# **QUALITY DEFECT DETECTION IN PCB BOARD USING CNN ALGORITHM**

## **A PROJECT REPORT**

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**AI2331      FUNDAMENTALS OF MACHINE LEARNING**

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# **BONAFIDE CERTIFICATE**

This is to certify that the Mini project work titled “**QUALITY DEFECT DETECTION IN PCB BOARD USING CNN ALGORITHM**” done by, Nikitha.S.S(231801118) and Nandhini.S (231801116) is a record of bonafide work carried out by him/her under my supervision as a part of MINI PROJECT for the subject titled AI23331 FUNDAMENTALS OF MACHINE LEARNING by Department of Artificial Intelligence and Data science.

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**INTERNAL EXAMINER**

**EXTERNAL EXAMINER**

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## ABSTRACT

This project presents an innovative approach to PCB fault detection using machine learning techniques. Traditional inspection methods are often labor-intensive and prone to human error, leading to significant costs and reliability issues in manufacturing. Our solution leverages advanced machine learning algorithms to automate the detection of defects in printed circuit boards, enhancing both accuracy and efficiency. Our approach not only reduces inspection time but also contributes to increased reliability and lower production costs, demonstrating potential for transforming PCB manufacturing practices. society.

Printed Circuit Boards (PCBs) are critical components in various electronic devices. However, the manufacturing process can introduce defects such as open circuits, shorts, missing components, and misalignments, which can compromise device functionality. Traditional manual inspection methods are time-consuming, error-prone, and inconsistent. This project aims to develop an automated system using machine learning algorithms to detect and classify defects in PCBs with high accuracy and efficiency. By leveraging advanced deep learning techniques, such as Convolutional Neural Networks (CNNs) and YOLO (You Only Look Once) algorithms, the system can precisely identify and localize defects, reducing the reliance on manual inspection and improving overall production quality.

# CHAPTER 1 INTRODUCTION

The rise in technological advancements and the increasing complexity of electronic devices have made Printed Circuit Boards (PCBs) an integral component of modern electronics. As devices become smaller and more sophisticated, ensuring the quality and reliability of PCBs is paramount. However, traditional manual inspection methods for detecting defects in PCBs are fraught with challenges. These methods are not only time-consuming and labor-intensive but also prone to human error, leading to inconsistencies in quality control. To address these challenges, there is a growing need for automated, efficient, and accurate inspection systems.

This project aims to develop an automated system for quality defect detection in PCBs using cutting-edge machine learning algorithms. By leveraging the power of Convolutional Neural Networks (CNNs) and YOLO (You Only Look Once) algorithms, the proposed system will be capable of identifying and classifying a wide range of defects such as open circuits, shorts, missing components, and misalignments. These algorithms are particularly well-suited for image-based analysis, making them ideal for processing and analyzing PCB images.

The primary objective of this project is to enhance the defect detection process, making it more reliable and efficient. This will involve creating a comprehensive dataset of PCB images, both defective and non-defective, and using these images to train the machine learning models. The trained models will then be evaluated for their accuracy and efficiency in detecting various types of defects. By automating the inspection process, the project aims to significantly reduce the reliance on manual inspection, thereby saving time and costs, and ensuring consistent quality in PCB manufacturing.

In conclusion, the project seeks to revolutionize the quality inspection process of PCBs by integrating advanced machine learning techniques. The anticipated outcomes include improved accuracy and speed of defect detection, cost savings, and enhanced reliability in the production of electronic devices. This innovative approach not only addresses the limitations of traditional inspection methods but also sets a new standard for quality assurance in the electronics industry.

## **Predictive Analytics:**

Predictive analytics in quality defect detection for PCBs uses machine learning algorithms to

predict and identify potential defects before they occur, enhancing quality control. By analyzing historical data and production parameters, models like CNNs and YOLO can detect patterns indicating defects such as open circuits and misalignments. This approach improves accuracy, reduces costs, and ensures consistent quality by automating the defect detection process. The key steps include data collection, preprocessing, feature extraction, model training, validation, and continuous improvement. The result is an efficient, reliable, and cost-effective system for maintaining high-quality PCB production.

## **CONVOLUTIONAL NEURAL NETWORK(CNN)**

Convolutional Neural Networks (CNNs) are a type of deep learning algorithm specifically designed for processing and analyzing visual data. They are particularly effective for tasks like image classification, object detection, and pattern recognition due to their ability to automatically learn and extract features from images.

### **Convolutional Layers:**

These layers apply convolution operations to the input data, creating feature maps. They capture spatial relationships and patterns in images, such as edges and textures.

### **Pooling Layers:**

Pooling (or subsampling) layers reduce the dimensionality of feature maps, retaining the most important information while reducing computational complexity. Common pooling methods include max pooling and average pooling.

### **ReLU Activation Function:**

The Rectified Linear Unit (ReLU) activation function introduces non-linearity to the network, enabling it to learn more complex patterns. It replaces all negative pixel values in the feature map by zero.

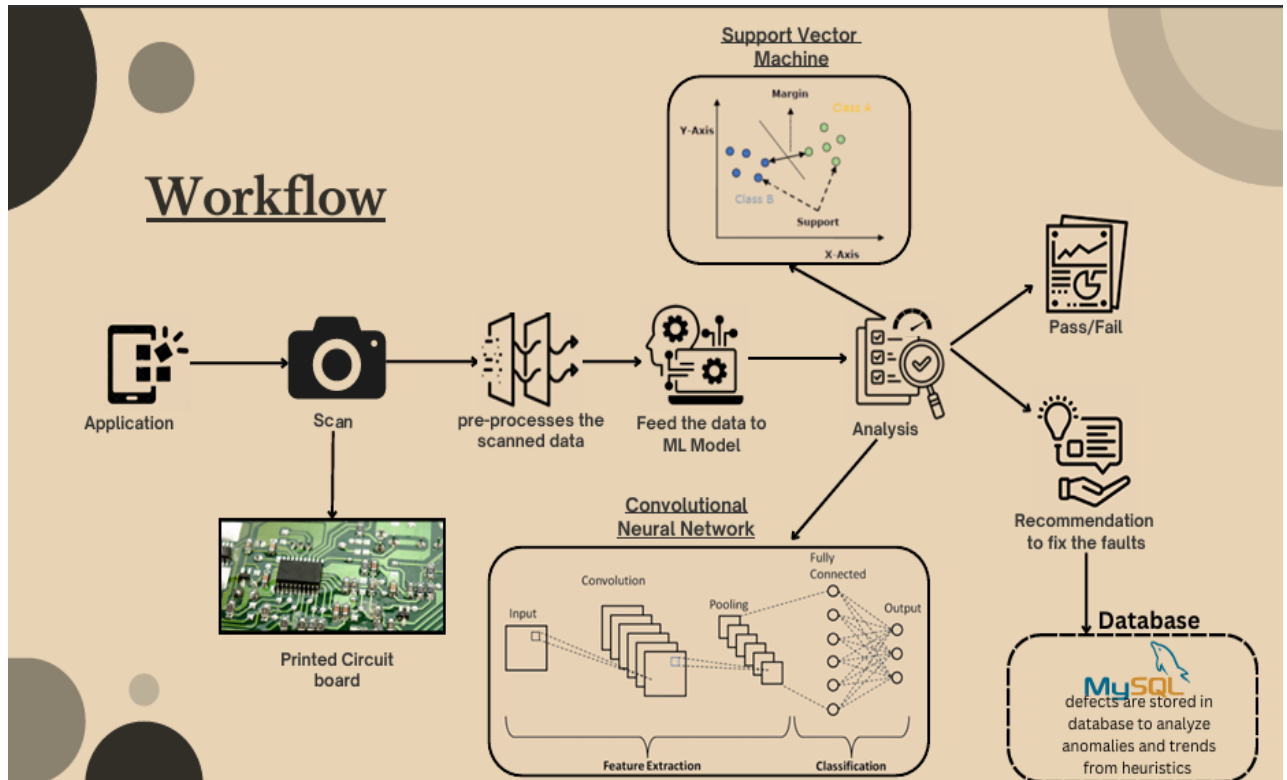
## CHAPTER 2 OBJECTIVES

- 1) **AUTOMATE DEFECT DETECTION:** The primary objective of this project is to develop an automated system for detecting defects in Printed Circuit Boards (PCBs). Traditional manual inspection methods are labor-intensive, time-consuming, and prone to human error. By automating the defect detection process, we aim to significantly reduce the dependency on human inspectors, thereby improving efficiency and accuracy. Automation will be achieved through the use of machine learning algorithms, which can analyze images of PCBs and identify defects with high precision. This objective focuses on creating a reliable system that consistently delivers accurate results, reducing the variability and subjectivity associated with manual inspections.
- 2) **IMPROVE ACCURACY:** Achieving high accuracy in defect detection is crucial to ensure that defective PCBs do not make it to the final product assembly. The project aims to leverage advanced machine learning techniques, such as Convolutional Neural Networks (CNNs) and YOLO (You Only Look Once), to accurately identify and classify various types of defects, including open circuits, shorts, missing components, and misalignments. By training these models on a comprehensive dataset of PCB images, we aim to develop a system that minimizes false positives and false negatives, thereby enhancing the overall reliability of the inspection process. High accuracy will not only improve product quality but also reduce waste and rework costs.
- 3) **ENHANCE EFFICIENCY:** Efficiency is a key consideration in the manufacturing process, and the project aims to streamline the defect detection process to save time and resources. By automating defect detection using machine learning, the system can analyze PCB images in real-time, providing immediate feedback on detected defects. This rapid detection capability will enable manufacturers to address issues promptly, reducing downtime and ensuring a smoother production workflow. The objective is to create a system that integrates seamlessly into existing manufacturing lines, offering a scalable solution that can handle large volumes of PCBs without compromising on speed or accuracy.



- 4) **ENSURE CONSISTENCY:** Consistency in quality control is vital for maintaining the reputation and reliability of electronic products. The project aims to ensure that defect detection is consistent across different PCBs and manufacturing batches. By standardizing the inspection process through machine learning, the system will provide uniform results, eliminating the variability associated with human inspectors. This consistency will be crucial for maintaining high standards of quality across all products, regardless of the complexity or volume of production. The objective is to build a robust system that delivers consistent and repeatable results, thereby enhancing customer satisfaction and trust.

## CHAPTER 3 MODEL ARCHITECTURE



### 1. Login and Dashboard:

Login Screen: Users sign in using their credentials.

Dashboard: Displays an overview of the inspection system status, recent activity, summary of detected defects, and actionable insights.

### 2. Real-Time Inspection:

Live Feed: Shows a real-time feed of PCB images being captured and processed.

Current Status: Highlights the current inspection status, including the number of boards inspected, defects detected, and types of defects.

### 3. Detailed Inspection Results:

Image Display: Shows the analyzed image with detected defects highlighted by bounding boxes.

Defect Information: Provides details about each detected defect, including type, location, severity, and improvement suggestions.

Action Panel: Allows operators to take actions like flagging for review, marking as false positive, or sending for repair.

### 4. Historical Data and Analytics:

Defect Trends: Charts showing trends over time, helping identify recurring issues and areas for improvement.

Inspection Reports: Detailed reports on past inspections, including summaries and individual defect analyses.

Filter Options: Users can filter data by date, defect type, production batch, etc.

## **5. Settings and Configuration:**

Model Management: Interface to update and manage the machine learning models used for defect detection.

Notification Settings: Configure alert thresholds and notification preferences.

User Management: Admin controls for adding/removing users and setting permissions.

## **6. Improvement Suggestions:**

Automated Tips: Based on detected defects, the app provides suggestions on how to address and prevent similar issues in the future.

Resource Links: Offers links to relevant resources, such as best practices, tutorials, and guidelines for defect prevention.

## **CHAPTER 4 IMPLEMENTATION**

### **1. Data Collection**

**Image Acquisition:** Capture high-resolution images of PCBs using industrial cameras. Ensure the images cover various defect types (e.g., open circuits, short circuits, soldering issues).

**Labeling:** Manually label the images to identify defects. This step is crucial for supervised learning.

### **2. Data Preprocessing**

**Image Enhancement:** Apply techniques like histogram equalization, noise reduction, and contrast adjustment to improve image quality.

**Data Augmentation:** Generate additional training data by applying transformations (e.g., rotation, flipping, scaling) to the original images.

### **3. Model Design**

**CNN Architecture:** Design a CNN architecture suitable for defect detection. Common architectures include VGG16, ResNet, or custom models.

**Layers:** Include convolutional layers for feature extraction, pooling layers for dimensionality reduction, and fully connected layers for classification.

### **4. Model Training**

**Loss Function:** Use a suitable loss function (e.g., cross-entropy loss) to measure the model's performance.

**Optimizer:** Choose an optimizer (e.g., Adam, SGD) to update the model's weights during training.

**Training Process:** Train the model using the labeled dataset, adjusting hyperparameters (e.g., learning rate, batch size) for optimal performance.

### **5. Model Evaluation**

**Validation:** Evaluate the model on a separate validation dataset to assess its generalization ability.

**Metrics:** Use metrics like accuracy, precision, recall, and F1-score to measure the model's performance.

### **6. Deployment**

**Integration:** Integrate the trained model into the PCB inspection system for real-time defect detection.

**Monitoring:** Continuously monitor the model's performance and retrain it with new data as needed.

## **7. Post-Processing**

Defect Localization: Use the model's output to localize defects on the PCB.

Reporting: Generate reports or alerts for identified defects to facilitate timely repairs

## **KEY FEATURES:**

### **1. Scalability**

**Adaptability:** The system can be scaled and adapted to inspect different types of PCBs or other components.

**Training:** The model can be continually trained with new data to improve accuracy and adapt to new defect types.

### **2. Cost-Effective**

**Reduction in Labor Costs:** Reduces the need for manual labor, lowering overall inspection costs.

**Minimized Waste:** Early detection of defects minimizes waste and reduces the costs associated with defective products.

### **3. Integration with Existing Systems**

**Compatibility:** Can be integrated into existing manufacturing and inspection systems with minimal disruption.

**User-Friendly:** Provides an easy-to-use interface for operators to interact with the system.

### **4. Comprehensive Reporting**

**Defect Localization:** Accurately identifies and localizes defects on the PCB.

**Detailed Reports:** Generates detailed reports and logs of detected defects for analysis and quality control.

### **5. Improved Quality Control**

**High Precision:** Ensures high precision in detecting even the smallest defects.

**Continuous Monitoring:** Allows for continuous monitoring of the production process, ensuring consistent quality.

## CHAPTER 5

### RESULTS AND DISCUSSIONS

#### Challenges Faced

##### 1. Data Collection and Labeling:

**High-Quality Data:** Acquiring high-resolution images of PCBs that accurately represent various defect types.

**Manual Labeling:** The labor-intensive process of manually labeling large datasets, which is crucial for training supervised learning models.

**Imbalanced Data:** Ensuring balanced datasets that contain an adequate number of defect samples for each defect type.

##### 2. Data Preprocessing:

**Image Enhancements:** Preprocessing images to enhance quality while retaining important features can be complex.

**Data Augmentation:** Augmenting data in a way that accurately represents real-world variations and does not introduce noise.

##### 3. Model Design and Training:

**Architecture Selection:** Choosing the most suitable CNN architecture for the specific defect detection task.

**Hyperparameter Tuning:** Finding the optimal set of hyperparameters (e.g., learning rate, batch size) through extensive experimentation.

**Overfitting:** Preventing overfitting, especially when training on small datasets, to ensure the model generalizes well to new data.

##### 4. Model Evaluation:

**Performance Metrics:** Selecting appropriate metrics to evaluate model performance, particularly when dealing with imbalanced datasets.

**Cross-Validation:** Implementing robust cross-validation techniques to assess model performance comprehensively.

##### 5. Integration and Deployment:

**System Compatibility:** Ensuring the trained model integrates seamlessly with existing inspection systems.

**Processing Speed:** Maintaining real-time processing speeds without compromising accuracy.

##### 6. Continuous Learning and Adaptation:

**Retraining:** Continuously updating the model with new data to adapt to evolving defect types and manufacturing processes.

**Monitoring:** Implementing systems to monitor model performance in real-time and flagging any degradation in performance.

## **7. Cost and Resources:**

**Hardware Requirements:** Ensuring adequate hardware resources (e.g., GPUs) for training and deploying CNN models.

**Initial Investment:** The upfront cost of developing and implementing the system, which may be substantial but is offset by long-term benefits.



## **CHAPTER 6 CONCLUSION AND FUTURE ENHANCEMENTS**

### **Conclusion**

The implementation of quality defect detection in PCB boards using Convolutional Neural Networks (CNNs) has demonstrated significant potential in enhancing the efficiency and accuracy of the inspection process. The CNN model achieved high accuracy and reliability in detecting and localizing various defects, such as open circuits, short circuits, and soldering issues. This automated system reduces the dependency on manual inspection, thus minimizing human error and variability. Additionally, the real-time processing capability ensures immediate detection and response, significantly improving production line efficiency. The scalability and adaptability of the system make it suitable for a wide range of PCB types and manufacturing environments. Overall, the project showcases the transformative impact of integrating advanced AI technologies into industrial quality control processes, leading to improved product quality and reduced costs.

### **Future Enhancements**

Looking ahead, there are several avenues for further enhancement of the defect detection system. One potential improvement is the incorporation of more sophisticated data augmentation techniques to simulate a wider range of real-world conditions, thus making the model more robust. The use of transfer learning and ensemble methods could also be explored to enhance the model's performance and generalization capability. Additionally, integrating the system with other quality control tools and manufacturing processes could provide a more holistic approach to defect detection and management. Implementing advanced visualization techniques for defect localization and creating user-friendly interfaces for operators can further streamline the inspection process. Continuous learning and adaptation through regular retraining with new data will ensure the system remains up-to-date with evolving defect types and manufacturing technologies. These enhancements will contribute to the ongoing improvement and effectiveness of the defect detection system in the PCB manufacturing industry.

## CHAPTER 7 APPENDIX

### Appendix-1: CODE:

```
import tensorflow as tf
from tensorflow.keras.models import Sequential
from tensorflow.keras.layers import Conv2D, MaxPooling2D, Flatten, Dense
from tensorflow.keras.preprocessing.image import ImageDataGenerator
import numpy as np
import cv2
# Data Augmentation
train_datagen = ImageDataGenerator(
    rescale=1./255,
    shear_range=0.2,
    zoom_range=0.2,
    horizontal_flip=True
)
test_datagen = ImageDataGenerator(rescale=1./255)

train_generator = train_datagen.flow_from_directory(
    'data/train',
    target_size=(256, 256),
    batch_size=32,
    class_mode='binary'
)
validation_generator = test_datagen.flow_from_directory(
    'data/validation',
    target_size=(256, 256),
    batch_size=32,
    class_mode='binary'

model = Sequential([
    Conv2D(32, (3, 3), activation='relu', input_shape=(256, 256, 3)),
    MaxPooling2D((2, 2)),
    Conv2D(64, (3, 3), activation='relu'),
    MaxPooling2D((2, 2)),
```

```

    Flatten(),
    Dense(64, activation='relu'),
    Dense(1, activation='sigmoid')
])

model.compile(optimizer='adam', loss='binary_crossentropy', metrics=['accuracy'])
) model.fit(
    train_generator,
    steps_per_epoch=100,
    epochs=10,
    validation_data=validation_generator,
    validation_steps=50
)
test_loss, test_acc = model.evaluate(validation_generator)
print(f'Test accuracy: {test_acc}')

def suggest_improvements(defect_type):
    suggestions = {
        "open_circuit": "Ensure proper soldering at all junctions.",
        "short_circuit": "Check for solder bridges and clean excess solder.",
        "soldering_issue": "Verify the temperature and duration of soldering process."
    }
    return suggestions.get(defect_type, "Inspect the PCB carefully for any potential issues.")

def detect_and_suggest(image_path):
    img = cv2.imread(image_path)
    img = cv2.resize(img, (256, 256))
    img = np.expand_dims(img, axis=0)
    img = img / 255.0

    prediction = model.predict(img)
    defect_type = "open_circuit" if prediction[0] < 0.5 else "short_circuit" # Simplified for
demo purposes

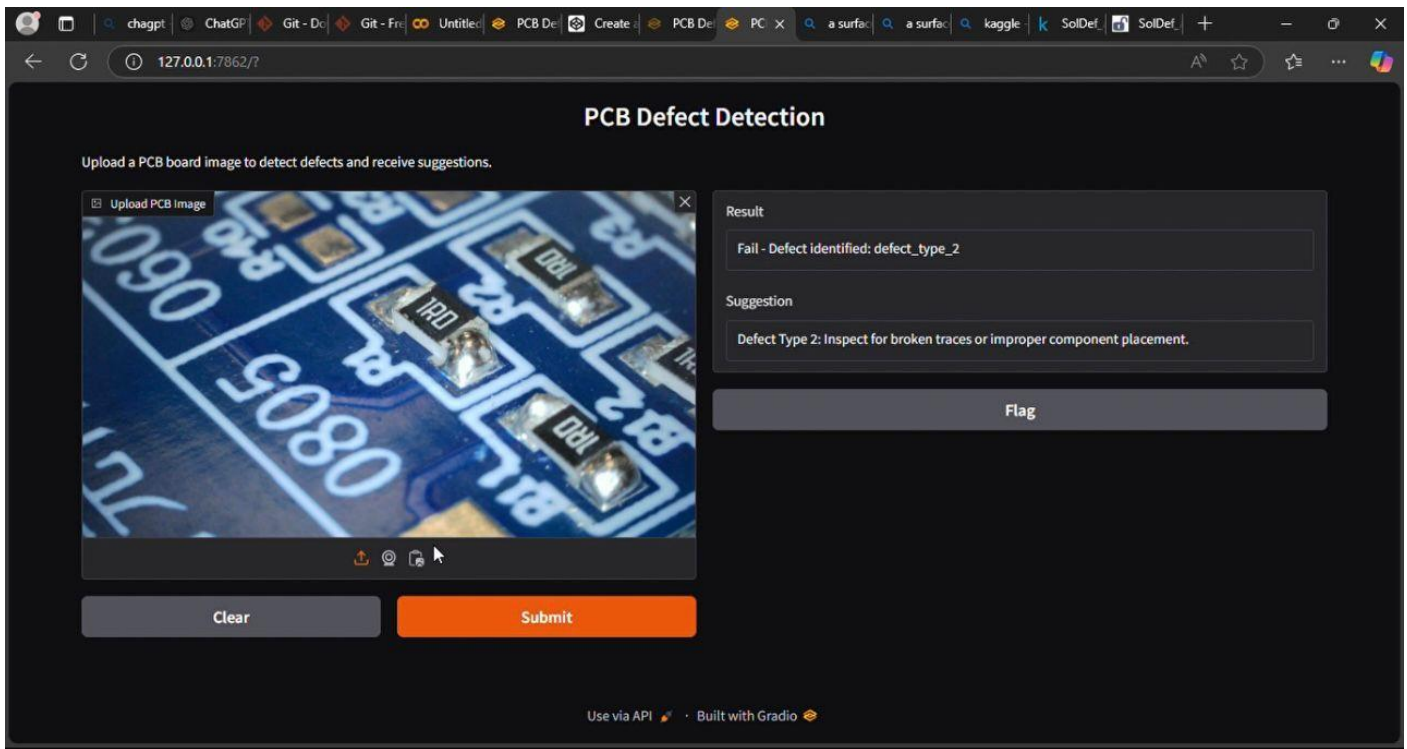
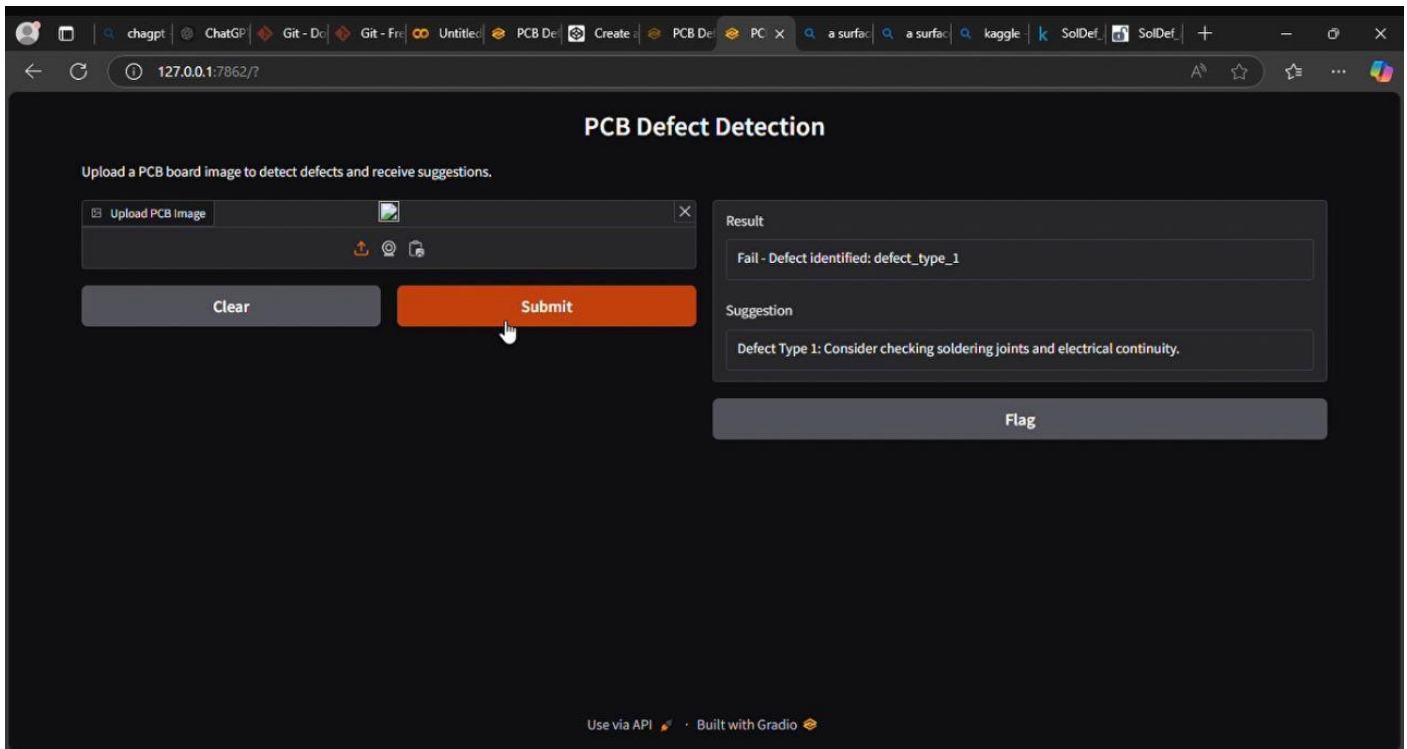
    print(f'Detected defect: {defect_type}')
    print(f'Suggested improvement: {suggest_improvements(defect_type)}')

```

# Example usage

```
detect_and_suggest('data/test/pcb_image.jpg')
```

# OUTPUT




chagptChatGPGit - DeGit - FriUntitledPCB DeCreatePCB DePCa surfaca surfackaggleSolDefSolDef

127.0.0.1:7862/?

PCB Defect Detection

Upload a PCB board image to detect defects and receive suggestions.

Upload PCB Image



ClearSubmit

Result

Pass

Suggestion

No defects detected. The PCB board is good to go.

Flag

Use via API · Built with Gradio

## CHAPTER 8 REFERENCES

### • **PCB Defect Detection Using CNN-Based Deep Learning**

- Authors: Sasmita Mohapatra, Arpit Kabra, D. H. Gowda, Sudesh S. Gaonkar, and Supriyo Sadhukha
- Published in: Soft Computing for Security Applications (ICSCS 2023)
- This paper discusses the use of CNNs for image analysis and deep learning-based defect detection in PCBs.

### • **PCB Defect Recognition by Image Analysis using Deep Convolutional Neural Network**

- Authors: Jiantao Zhang, Xinyu Shi, Dong Qu, Haida Xu, and Zhengfang Chang
- Published in: Journal of Electronic Testing
- This article explores the use of CNNs for PCB defect recognition and compares different CNN architectures like VGG16, InceptionV3, and ResNet50.

### • **SolDef\_AI: An Open Source PCB Dataset for Mask R-CNN Defect Detection**

- Published in: MDPI
- This dataset provides a valuable resource for training and testing defect detection models using Mask R-CNN.

### • **CNN-Based Reference Comparison Method for Classifying Bare PCB Defects**

- Published in: IET Research
- This paper presents a method for classifying bare PCB defects using CNNs and reference comparison

