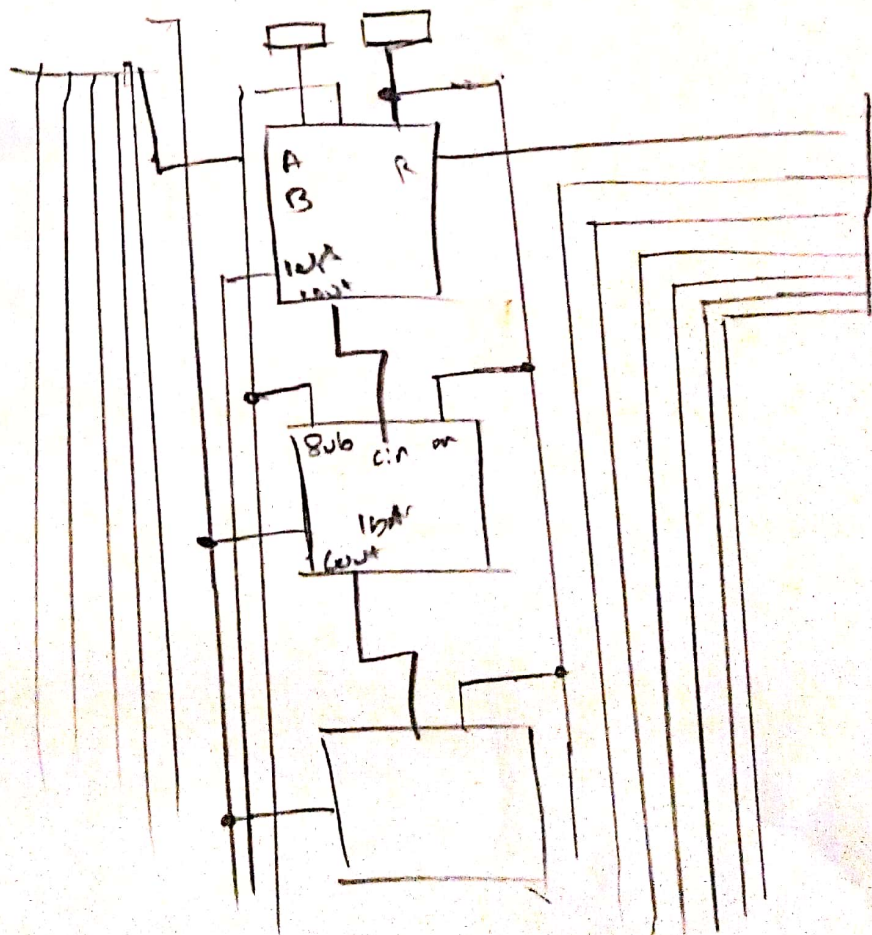


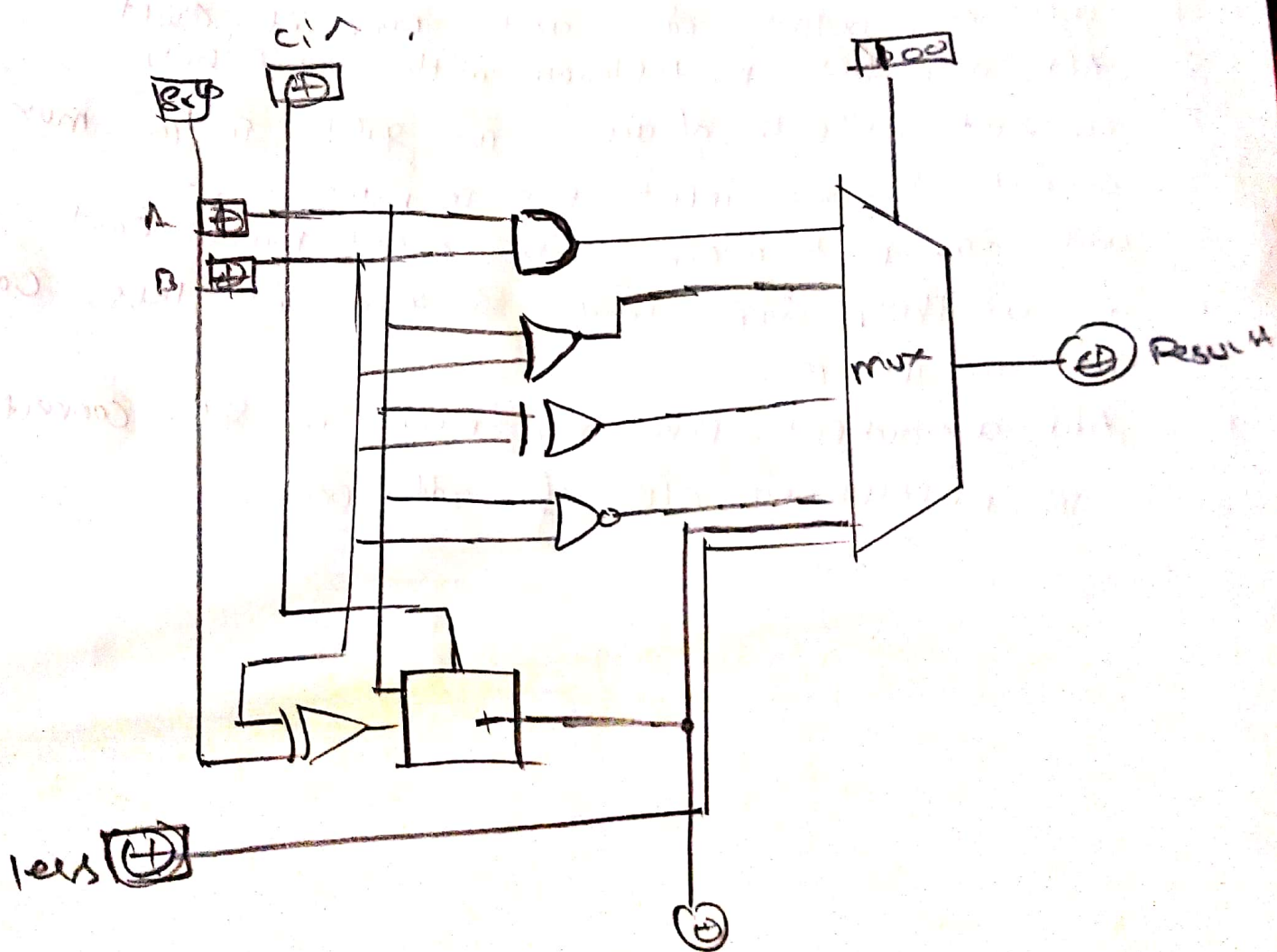
name: - Kunal Nishii
usn: - 19CS405

Activity :- 5

List out the steps in designing ALU

1. Add the two i/p pins, name them A and B
2. Add or, and, ex-or, nand gates and 1-bit adder
3. Connect the A's and B's of all the gates to their respective pins
4. Add an output pin and name it Result
5. Add a 1-bit multiplexer with select bits
6. Connect outputs of all the gates to the mux
7. Connect 3-bit input pin to mux
8. Add i/p pin 0 cin, and output pin to cout
9. Add another i/p and same bit less, connect it to the mux
11. Add an output pin and name it Sel. Connect it to the multiplexer o/p of adder unit





Name: - Kudur Nithi
USN: - 1ms1acs405

Activity - 6

Write out the steps in designing ALU.

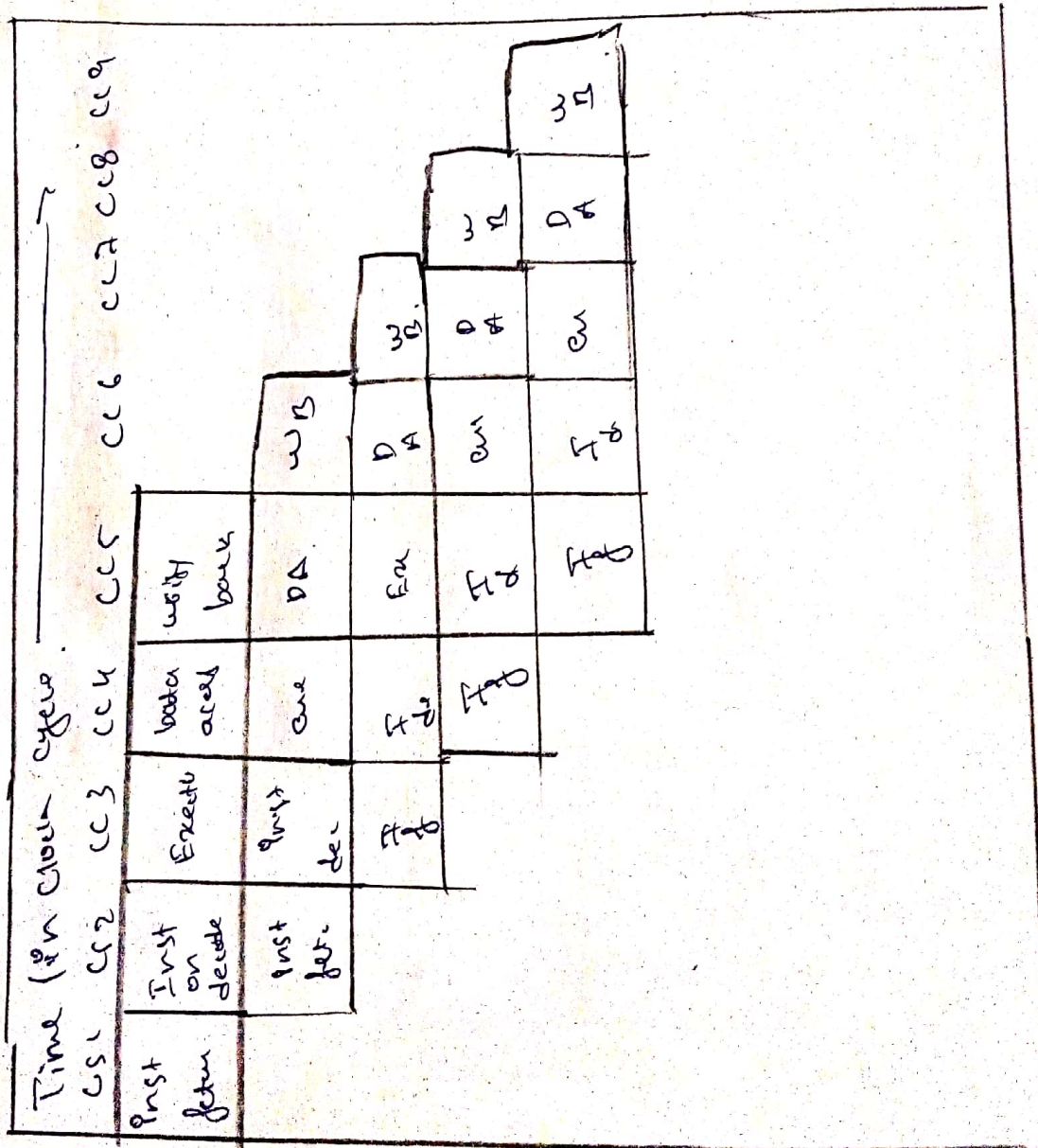
1. Add the two I/P Pins. name.
1. Add a RAM with separate load and store selected
2. Add counter and connect to A of the RAM
3. Add a controller buffer and connect its O/P to the RAM.
4. Add a clock and connect to the I/P of the buffer
5. Add a TTY unit with 32 bus and columns make connection with RAM
6. Add a 7-bit random number generator, connect to D.
7. connect the output of the second buffer to the counter
8. connect a button to the counter

QSN :- IMS IACSWS

Activity - 7

with diagram demonstrate the execution of the following instructions using pipelining technique.

lw \$10, 20(\$1)
 sub \$11, \$2, \$3
 add \$12, \$3, \$4
 lw \$13, 24(\$1)
 add \$14, \$5, \$6



lw \$10, 20(\$1)

sub \$11, \$2, \$3

add \$12, \$3, \$4

lw \$13, 24(\$1)

add \$14, \$5, \$6

