Ramaiah Institute of Technology (Autonomous Institute, Affiliated to VTU)

Department of CSE

Programme: B.E Term: Jan to May 2019
Course: Computer Organization Course Code: CS45

Activity V: Designing an ALU to perform arithmetic and logical functions using Logisim simulator.

Name: Kashish Gulati	Marks: /10	Date:
USN: 1MS18CS056	Signature of the Faculty:	

Objective: To simulate the working of Arithmetic and Logical Unit using simulator.

Simulator Description: Logisim is an educational tool for designing and simulating digital logic circuits. With its simple toolbar interface and simulation of circuits as you build them, it is simple enough to facilitate learning the most basic concepts related to logic circuits. With the capacity to build larger circuits from smaller sub circuits, and to draw bundles of wires with a single mouse drag, Logisim can be used (and is used) to design and simulate entire CPUs for educational purposes.

Activity to be performed by students:

KASHISH GOLL ATI 1MS18C5056 Settie - B

Activity - \$5

a List out the steps is designing ALU Ans 1. Add the two i/p pins. Name them A and B.

2. Add or, and, xox, nor gates and a 1 ditadder.

3. Connect the 4's and B's of all the gates to their respective pine

A. Add an output pix and none it Reput.

5. Add a 1 bit multiplion will 3 seld but

8. Consect 3 bit isput per to two mux.

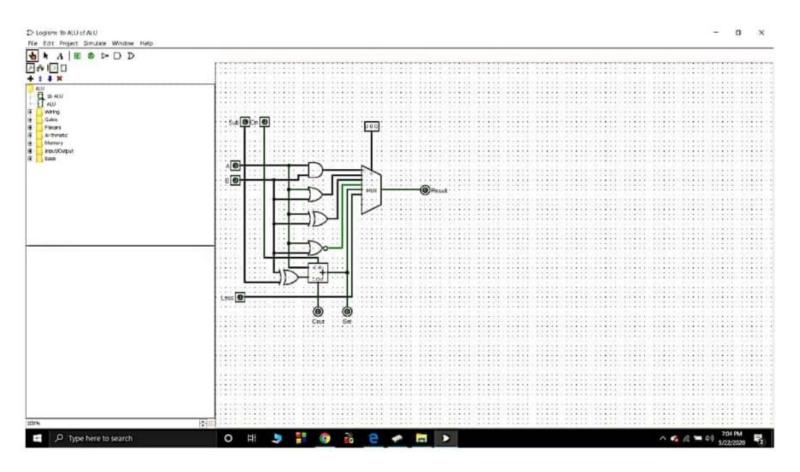
8. Add i p pie to Ci, and output per to Cont.

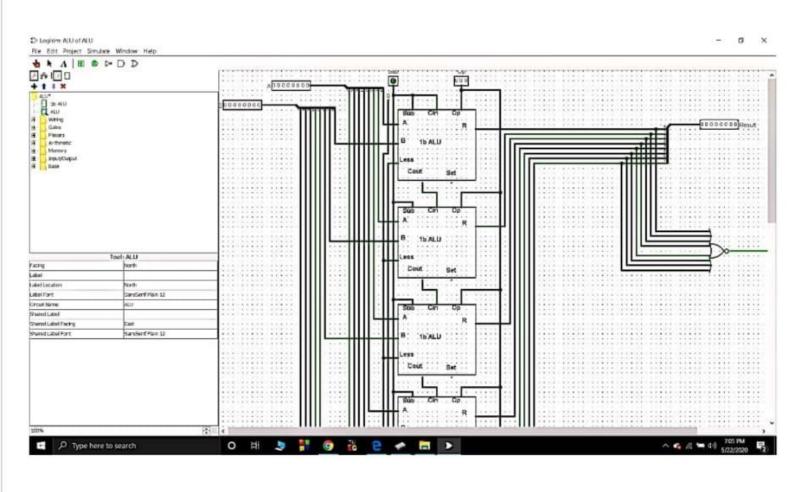
a. Add as XOR gate. Consort to output to Cost. The first ilp must be consider to B and the second to another i/p pin sub

10. Add, another i/p and name it a says loss - count it to the mun.

11. Add an output pix and me it Set. Count it to the output of adder went.

Snapshots:





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Activity VI: Designing memory system using Logisim simulator.

Term: Jan to May

Date:

Course Code:

Programme: B.E

Name:

USN:

Course: Computer Organization

Observations and Snapshots:

2019

CS45

Objective: To simulate the writing operation on memory.
Simulator Description: Logisim is an educational tool for designing and simulating digital logic circuits. With its simple toolbar interface and simulation of circuits as you build them, it is simple enough to facilitate learning the most basic concepts related to logic circuits. With the capacity to build larger circuits from smaller sub circuits, and to draw bundles of wires with a single mouse drag, Logisim can be used (and is used) to design and simulate entire CPUs for educational purposes.
Activity to be performed by students:
Activity to be performed by students: List out the steps in designing memory system

Activity - 6

List out the steps in deliging menory steps: -

2. All a RAM with separate look and store sebeted.

2. All a counter out count a to A of the RAM. 3. Add a controller buffer and correct its output.

A Ast a clock and cornect its o/1 to the NAM. 4. All a clock and comed to the ip of the buffer.

RAM aTTV unit with 32 groves and columns. trake the correction with

6. Add a 7 bit grandom number generator, comed & to D.

7. Add another controlled buffer, count to TTY.

8 Cornert the output of the second buffer to the counter.

q. Conert a butter to the counter.

Snapshot:

