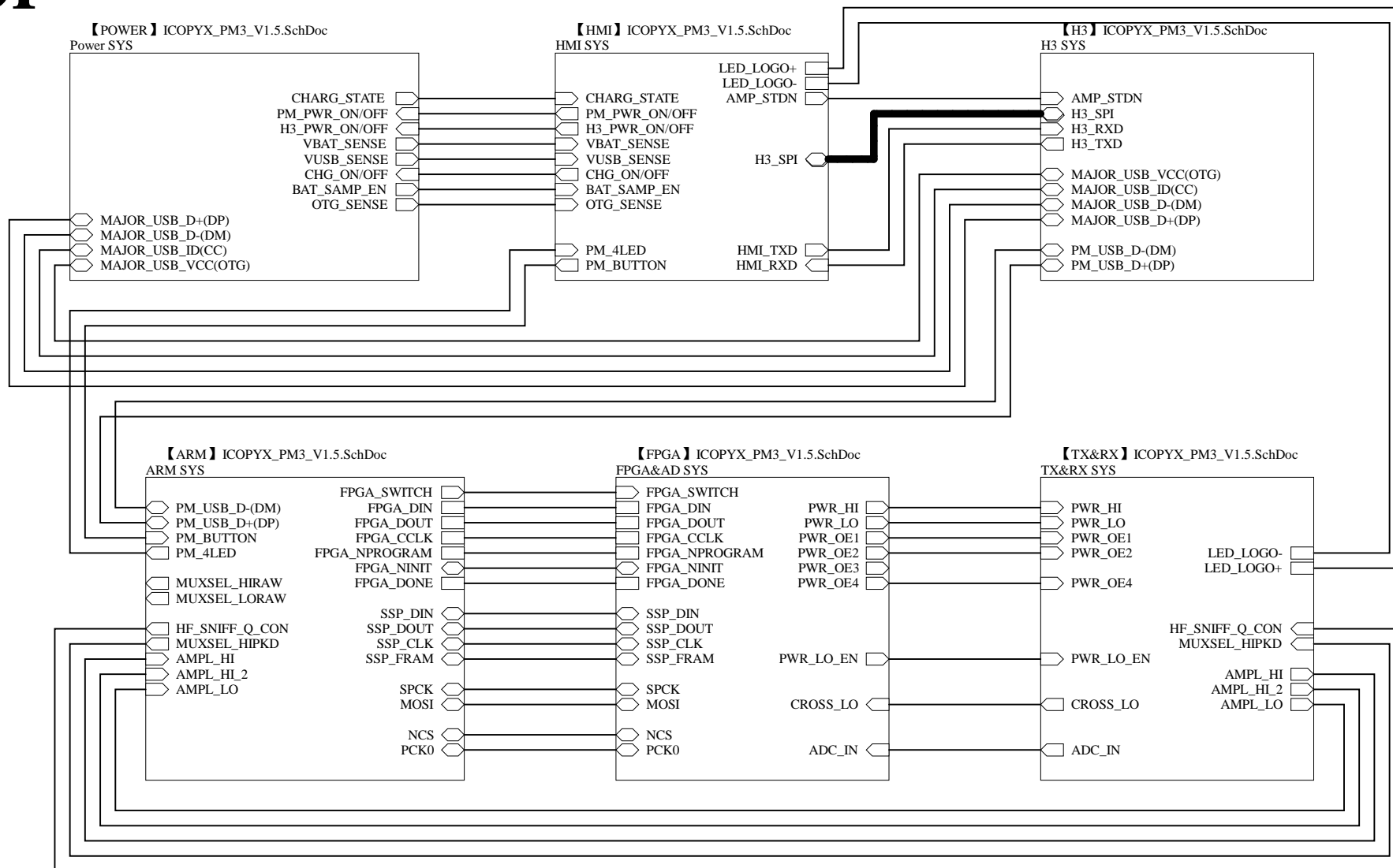
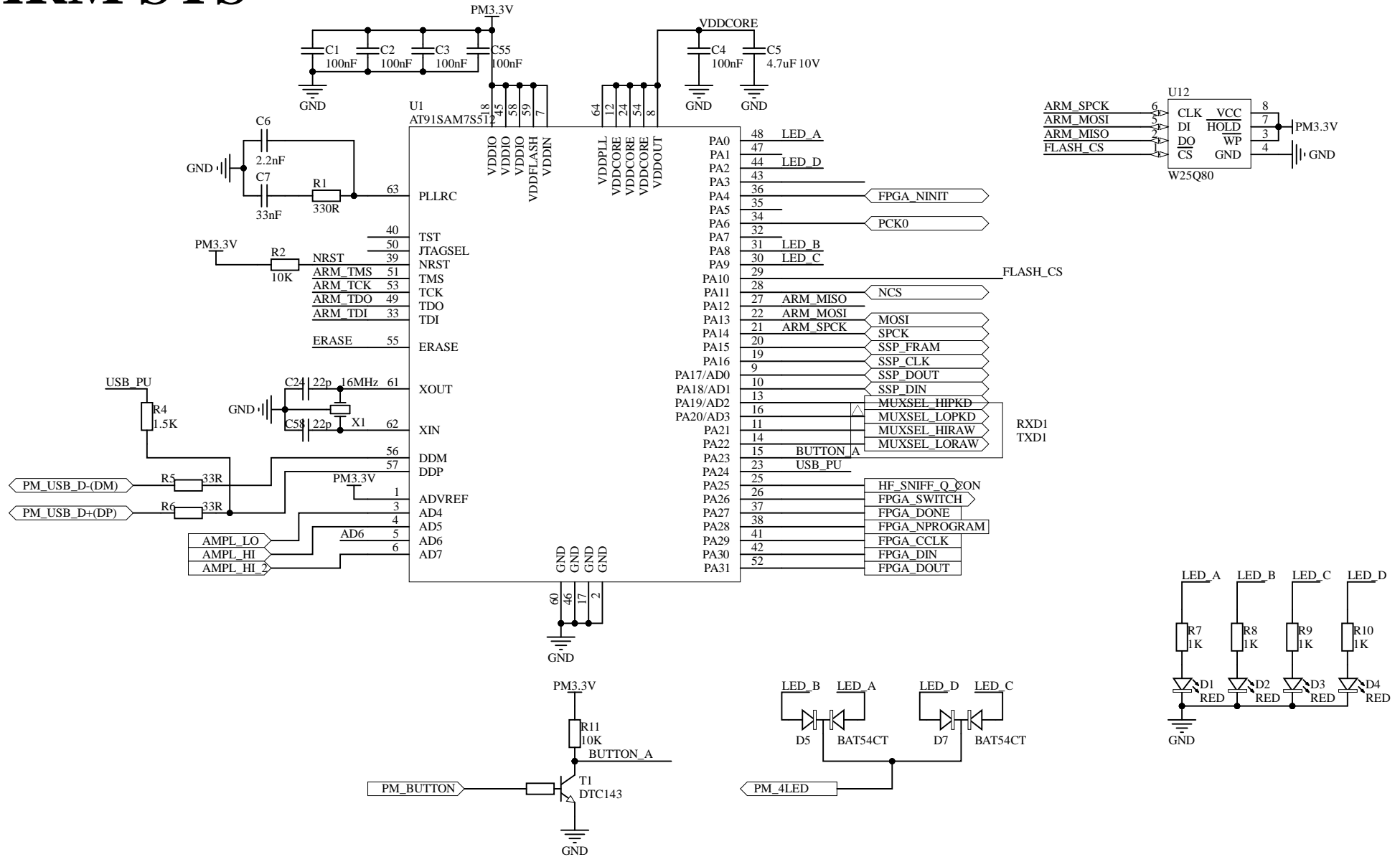


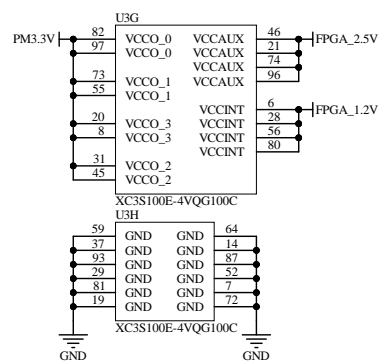
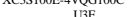
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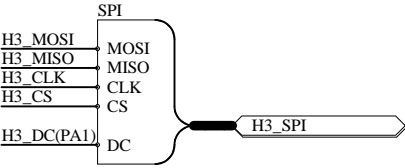
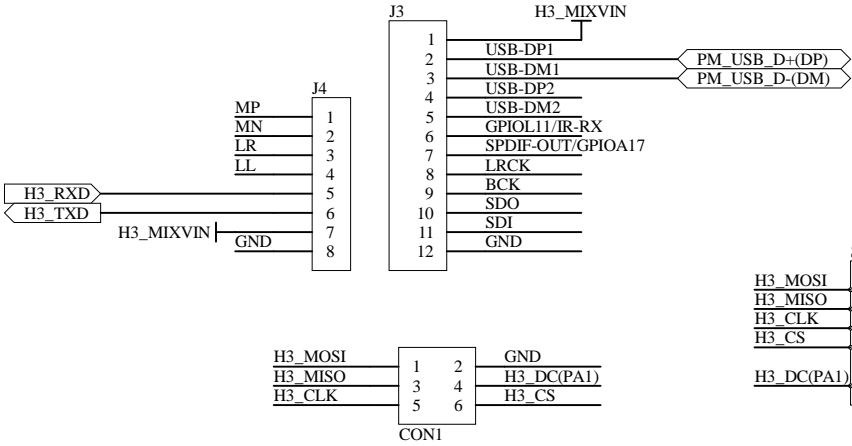
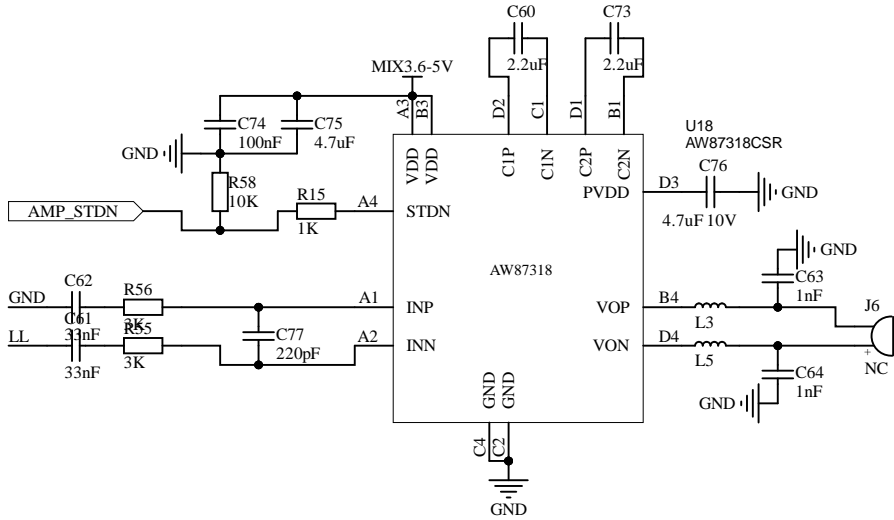
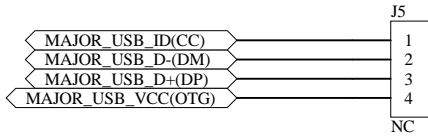
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# ARM SYS





# H3 SYS



HMI SYS

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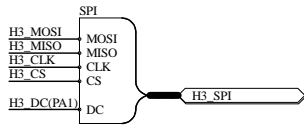
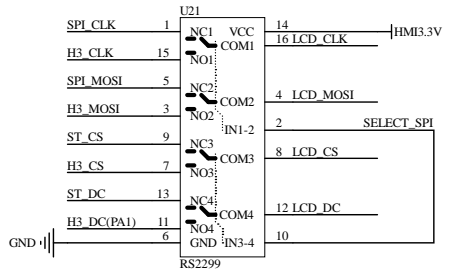
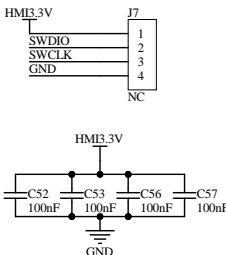
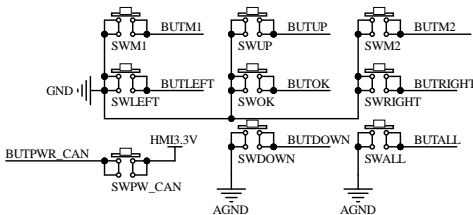
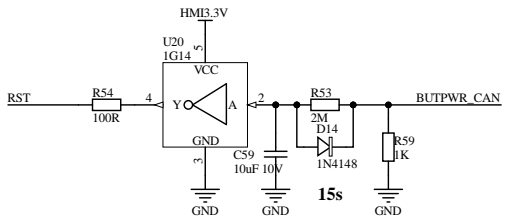
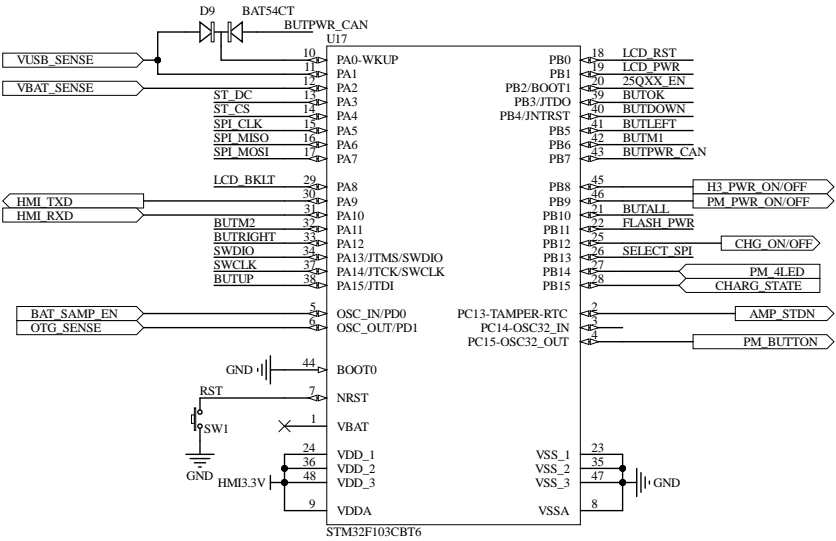
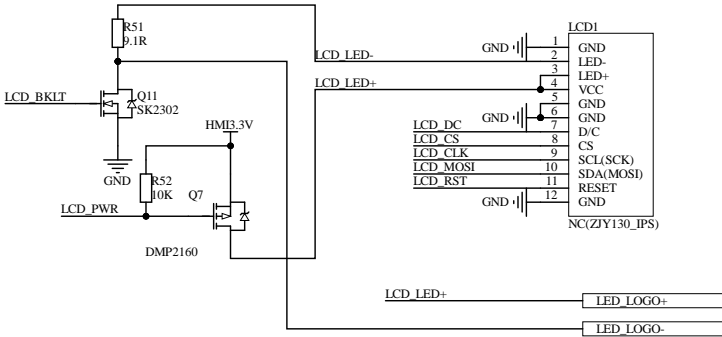
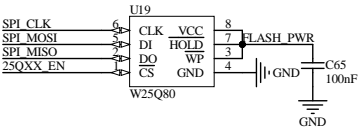
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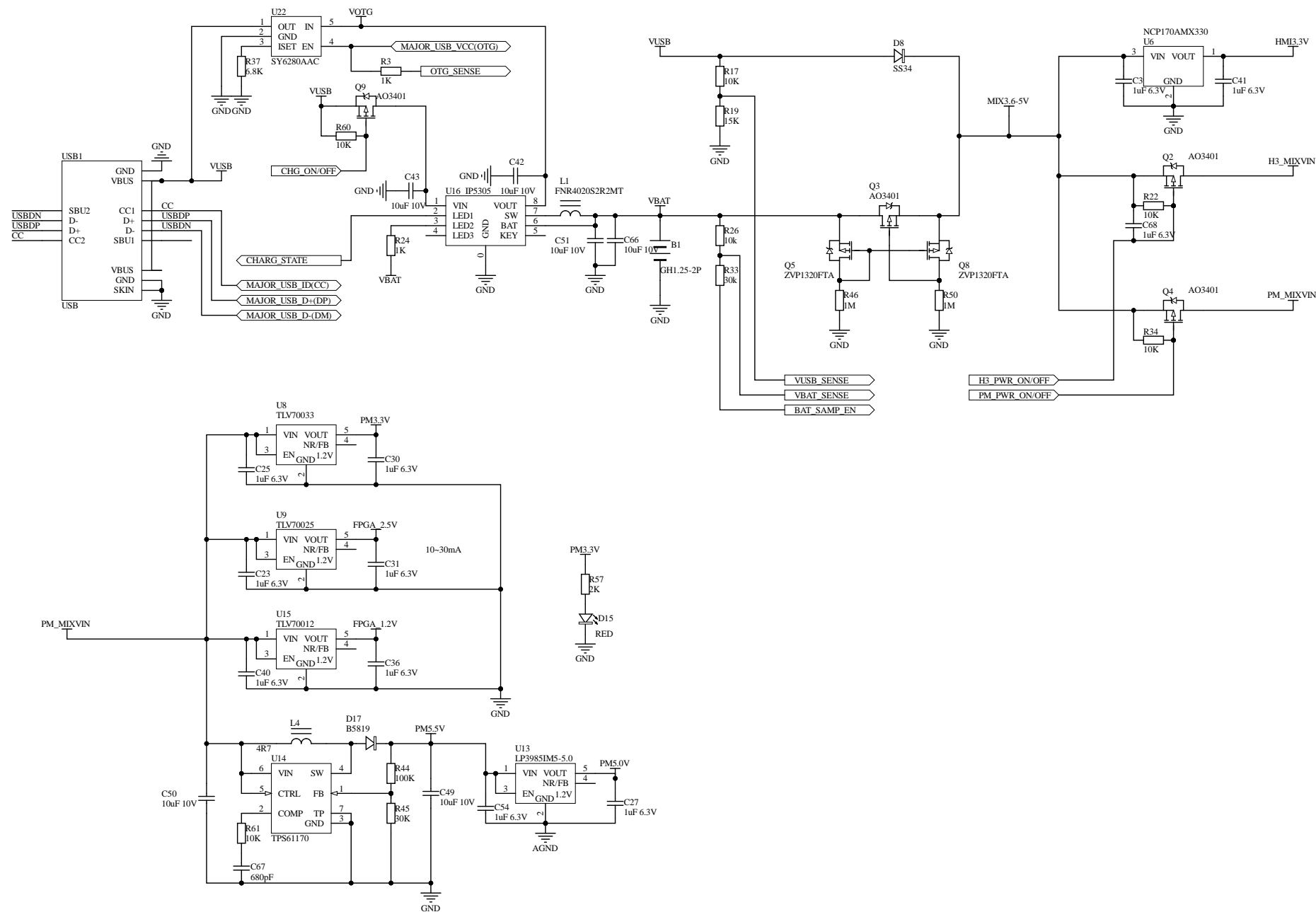
B

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D



# POWER SYS



# TX&RX SYS