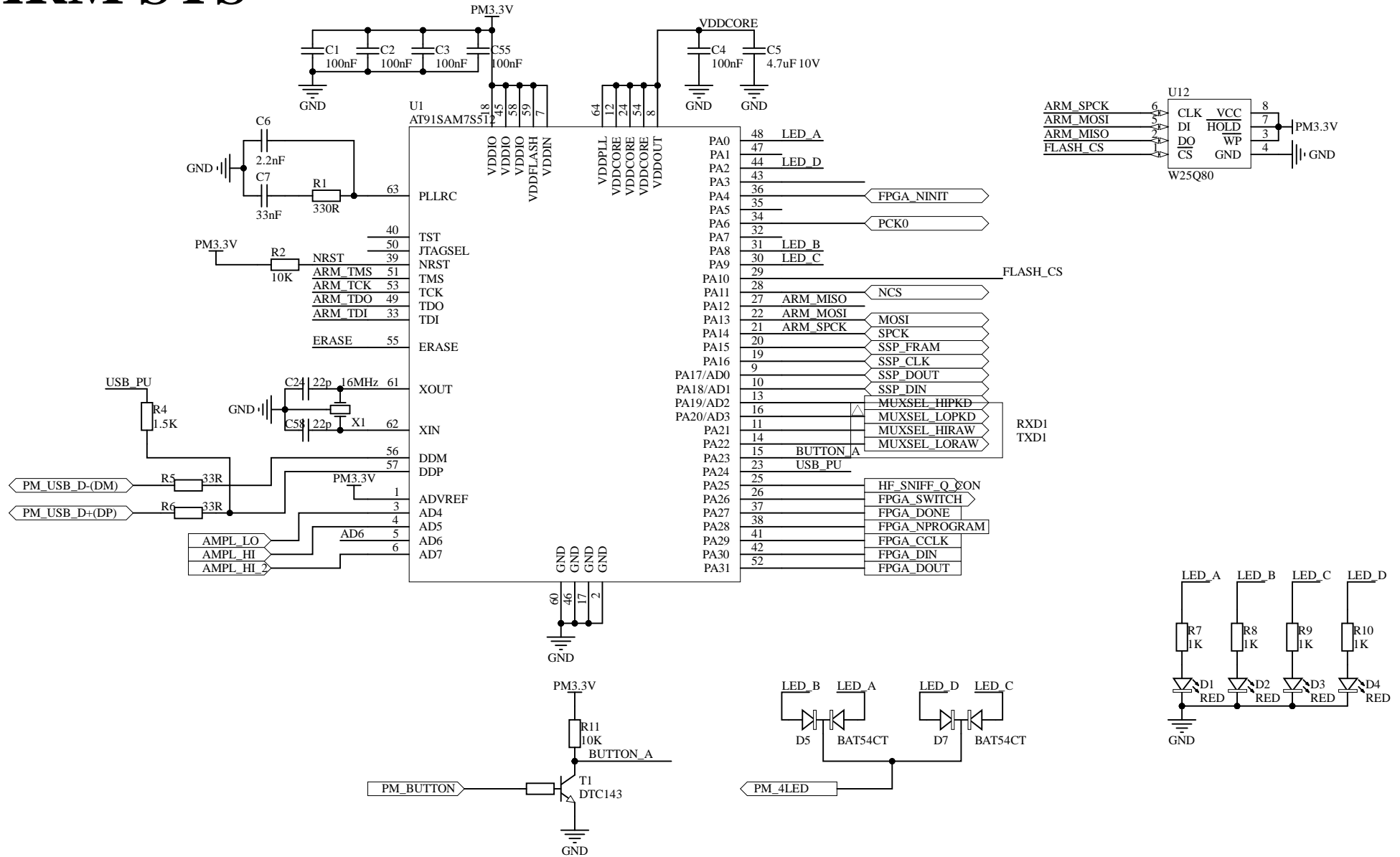
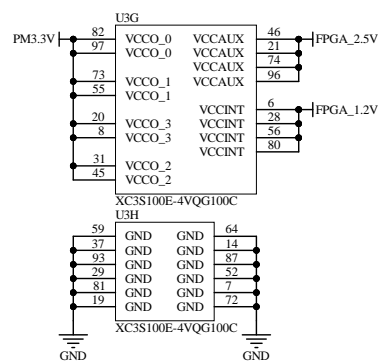
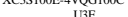
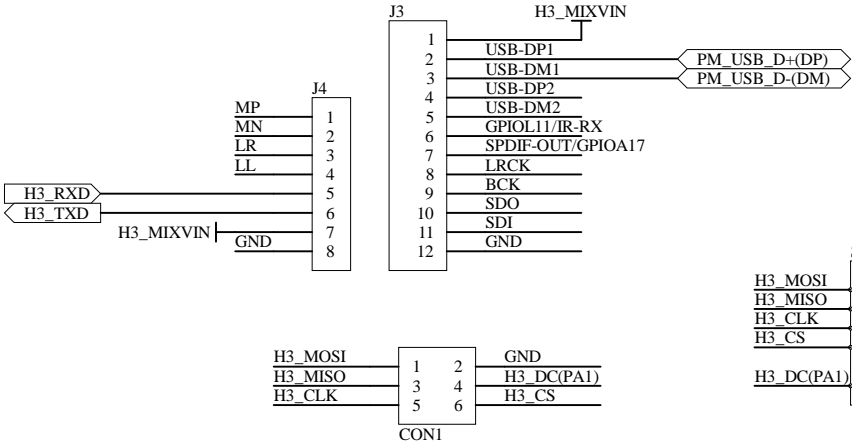
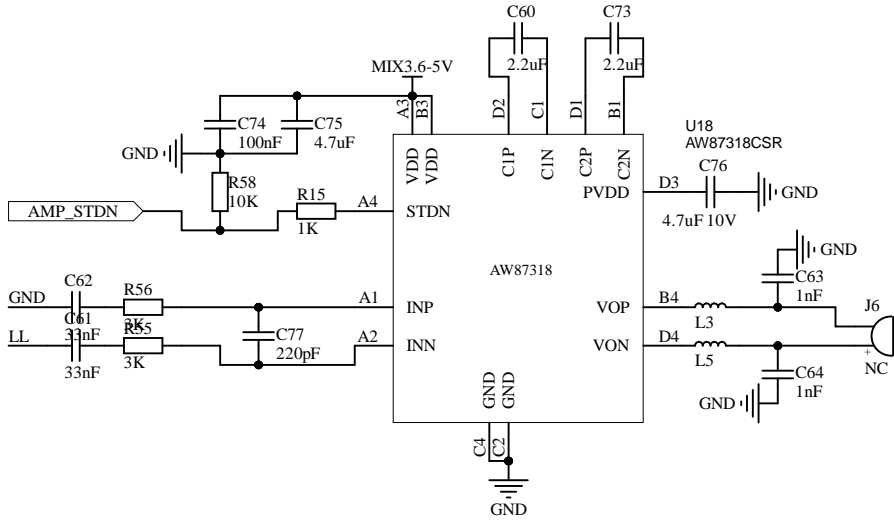
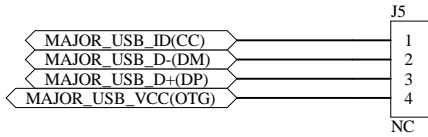


# ARM SYS





# H3 SYS



HMI SYS

A

B

C

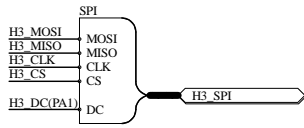
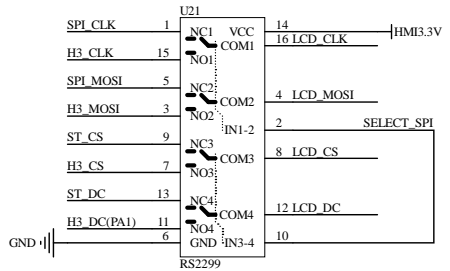
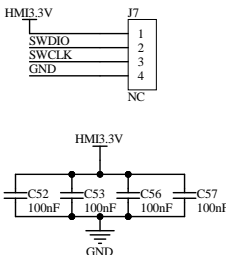
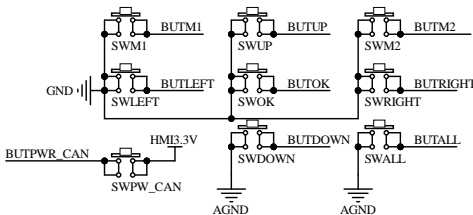
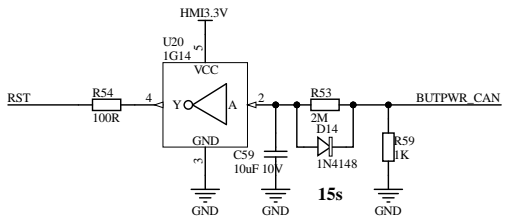
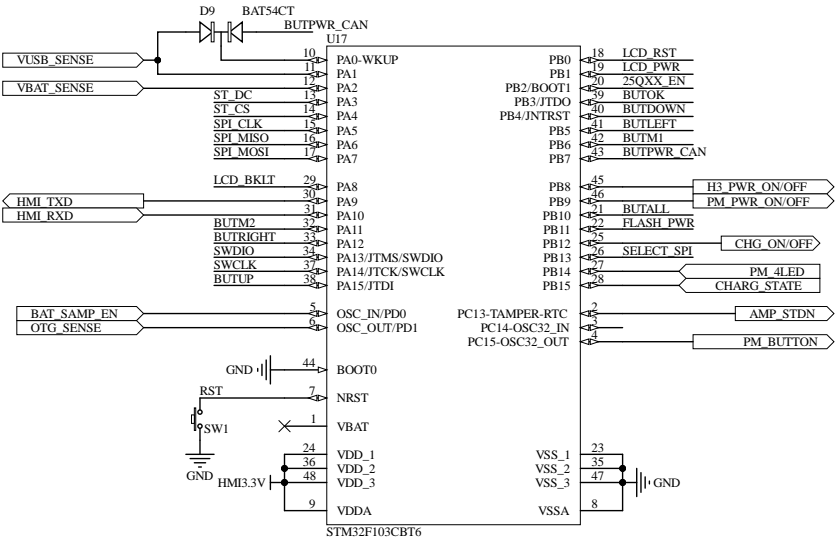
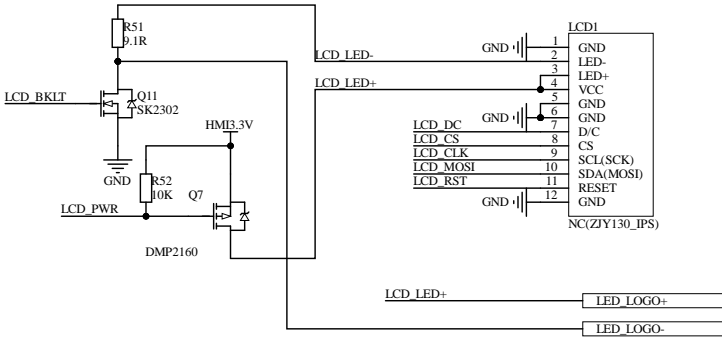
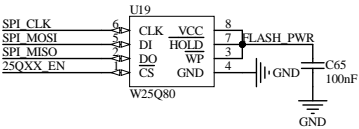
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A

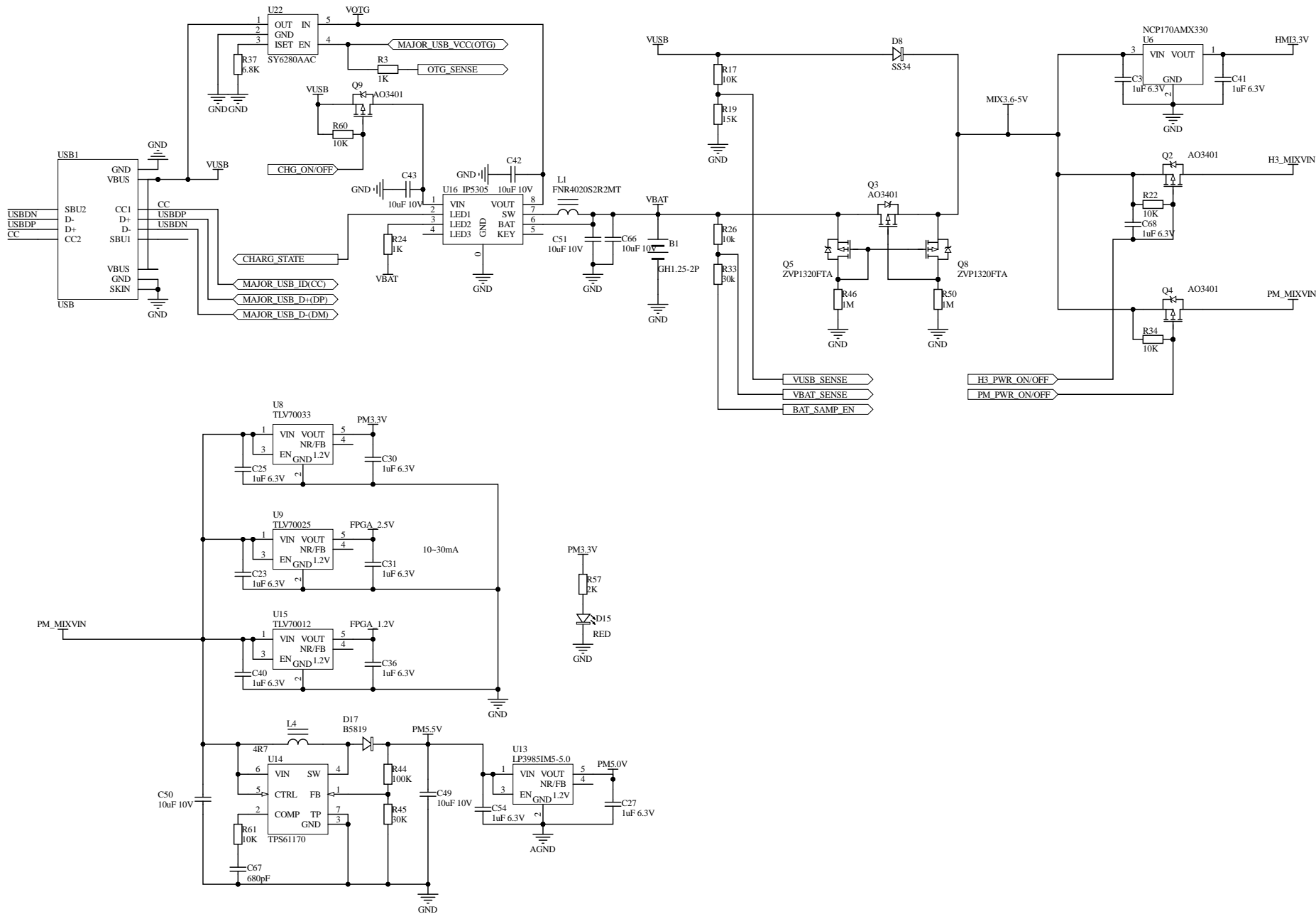
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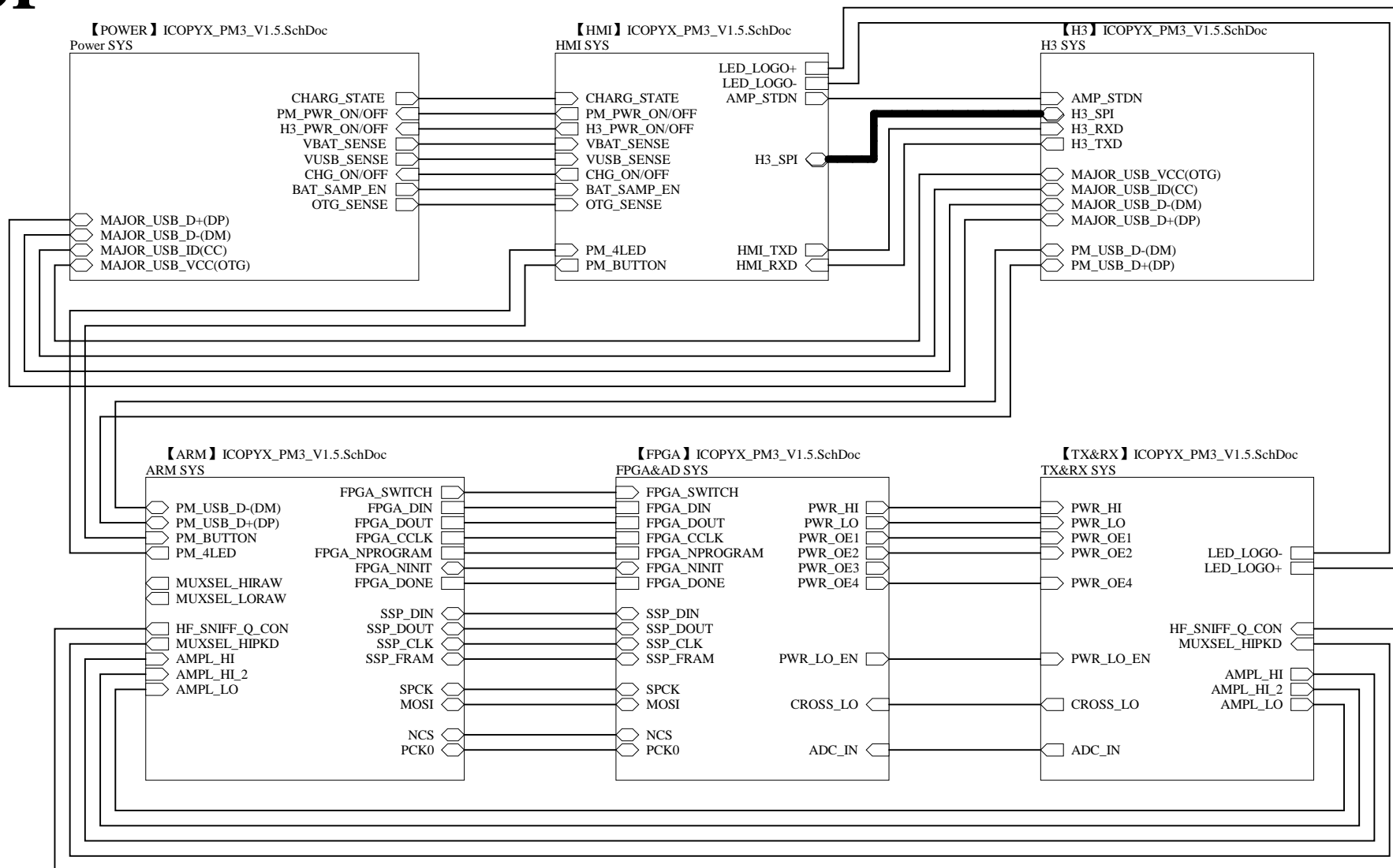
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# POWER SYS



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# TX&RX SYS